Lab # - Report Title

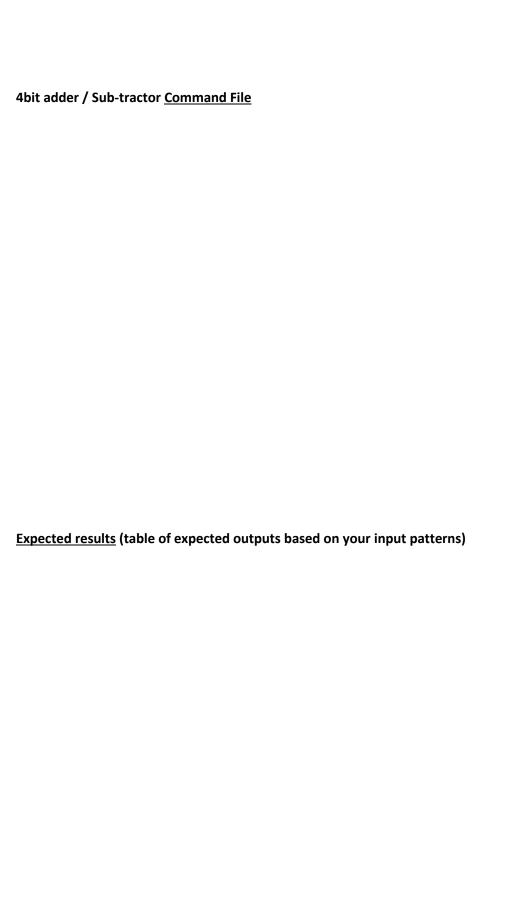
Name

Date

Second summer 2014

Purpose:	
	At least one paragraph
Analysis:	
	At least one paragraph







## Appendix I

Symbol/Schematic/CMD / Expected results /Simulation for the followings <u>using real components</u>:

1 bit adder

Xor gate