ARTIK PLATFORM BOARD REV1.0

Revision	Date	Description
0.0	2016.05.13	First Version
0.1	2016.06.08	
0.2	2016.07.04	
1.0		

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Abbreviation List

This schematic was designed using standard symbol.

Schematics Design By Name and Name

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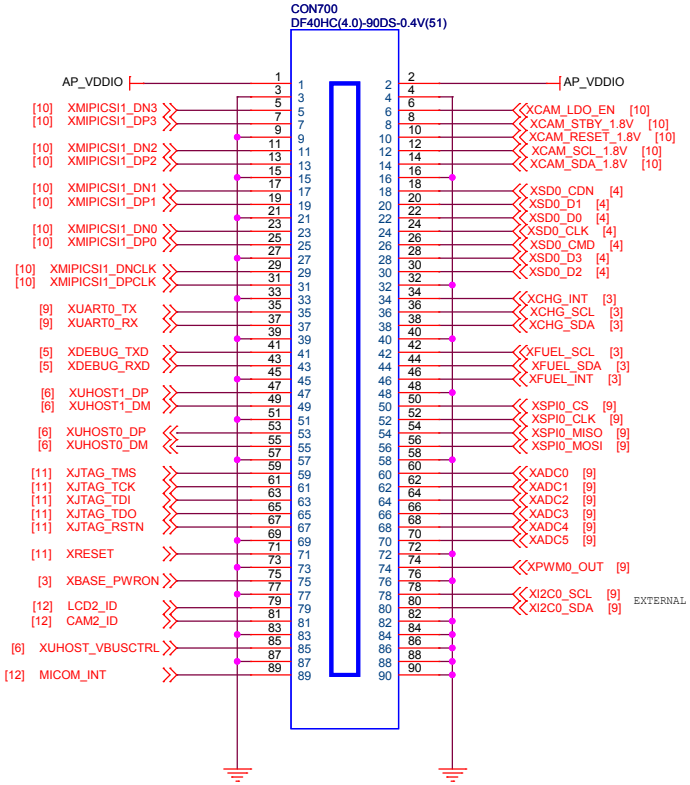
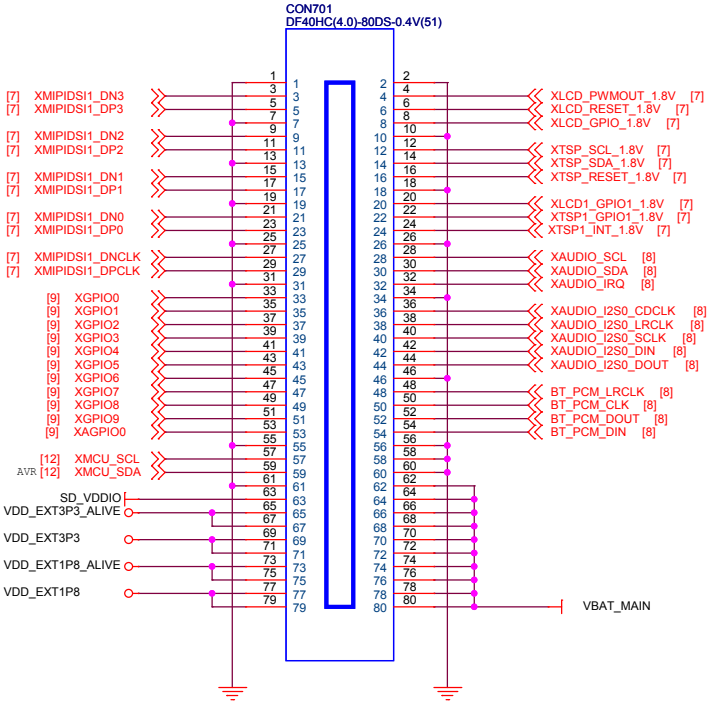
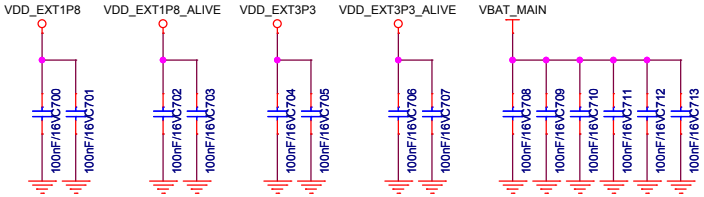
B2B Connector

Stacking height - 4mm

HIROSE Receptacle







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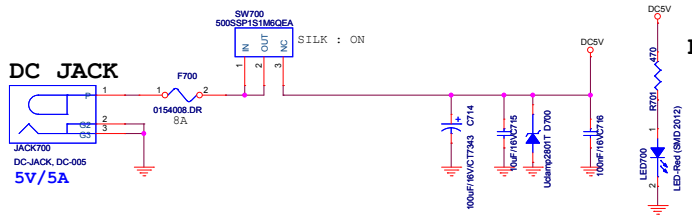
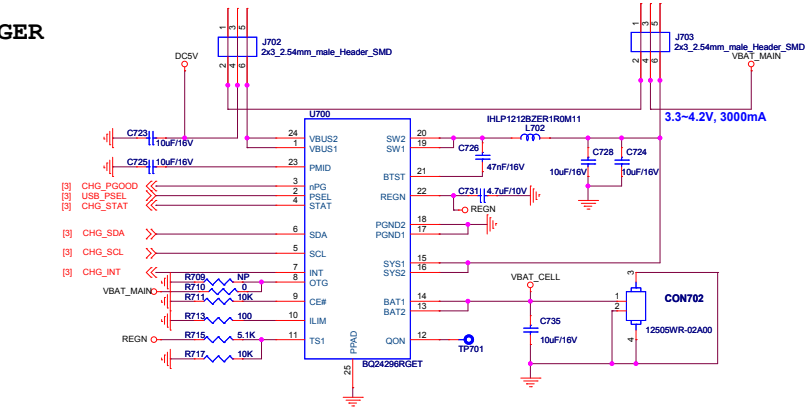
MAIN POWER

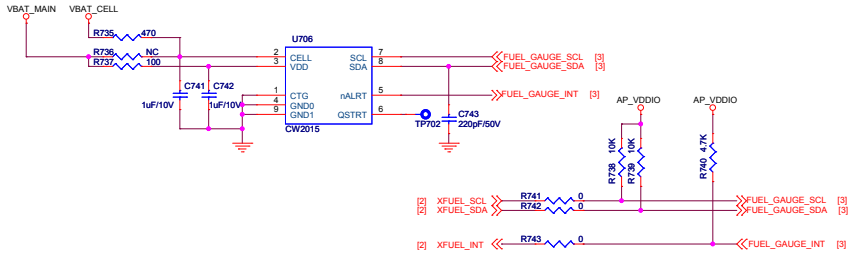


POWER LED

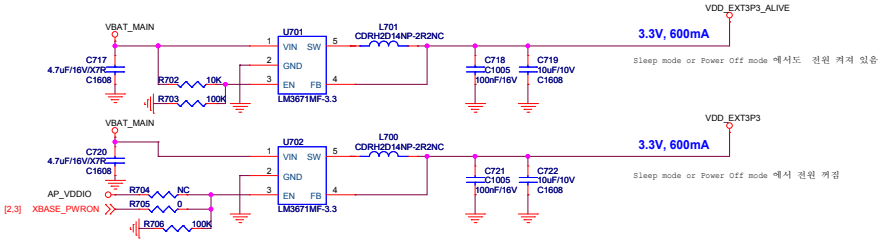
CHARGER

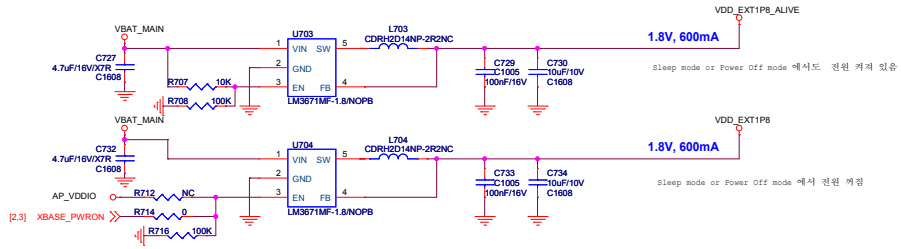


FUEL GAUGE

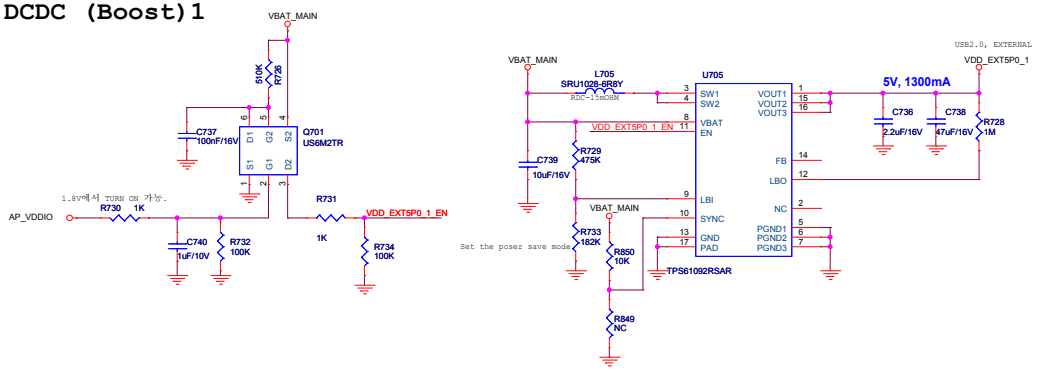


EXT LDO





EXT DCDC (Boost)1



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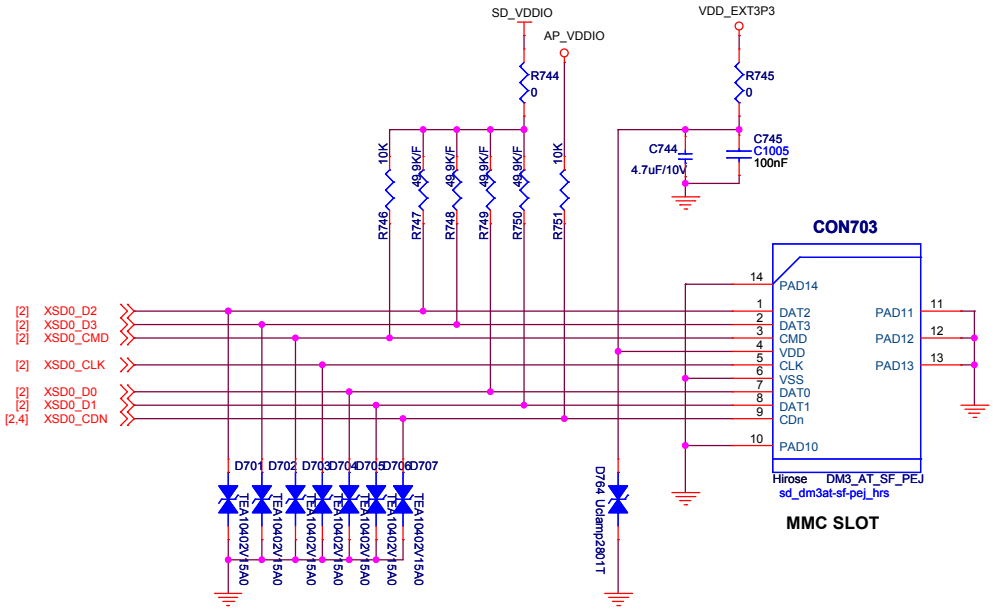
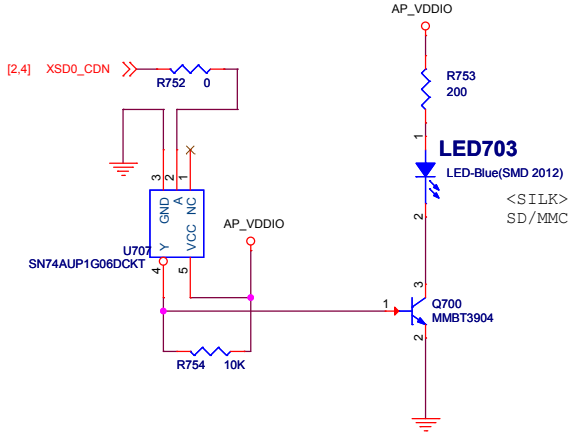
SD/MMC





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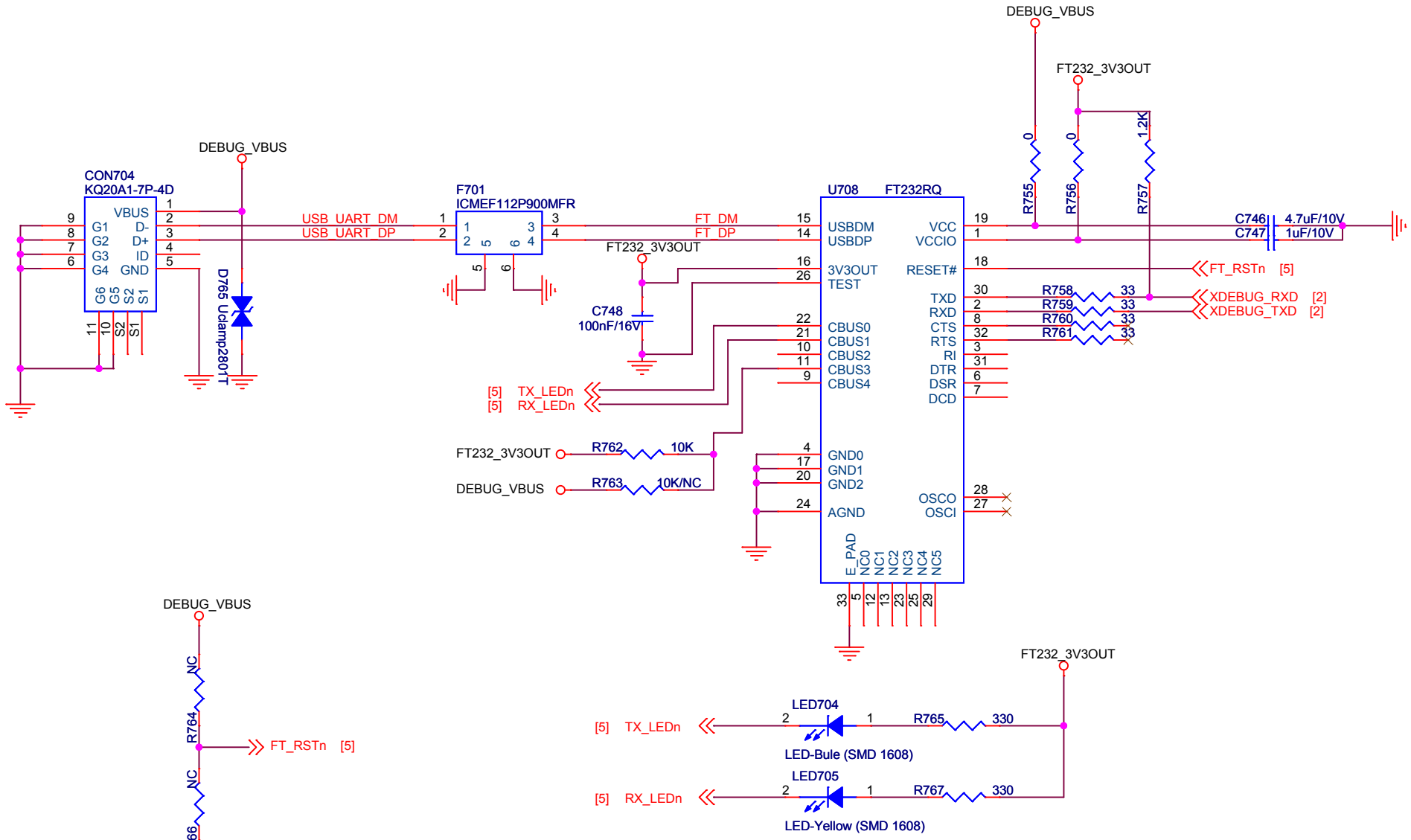
USB UART



UART STATE MONITORING LED

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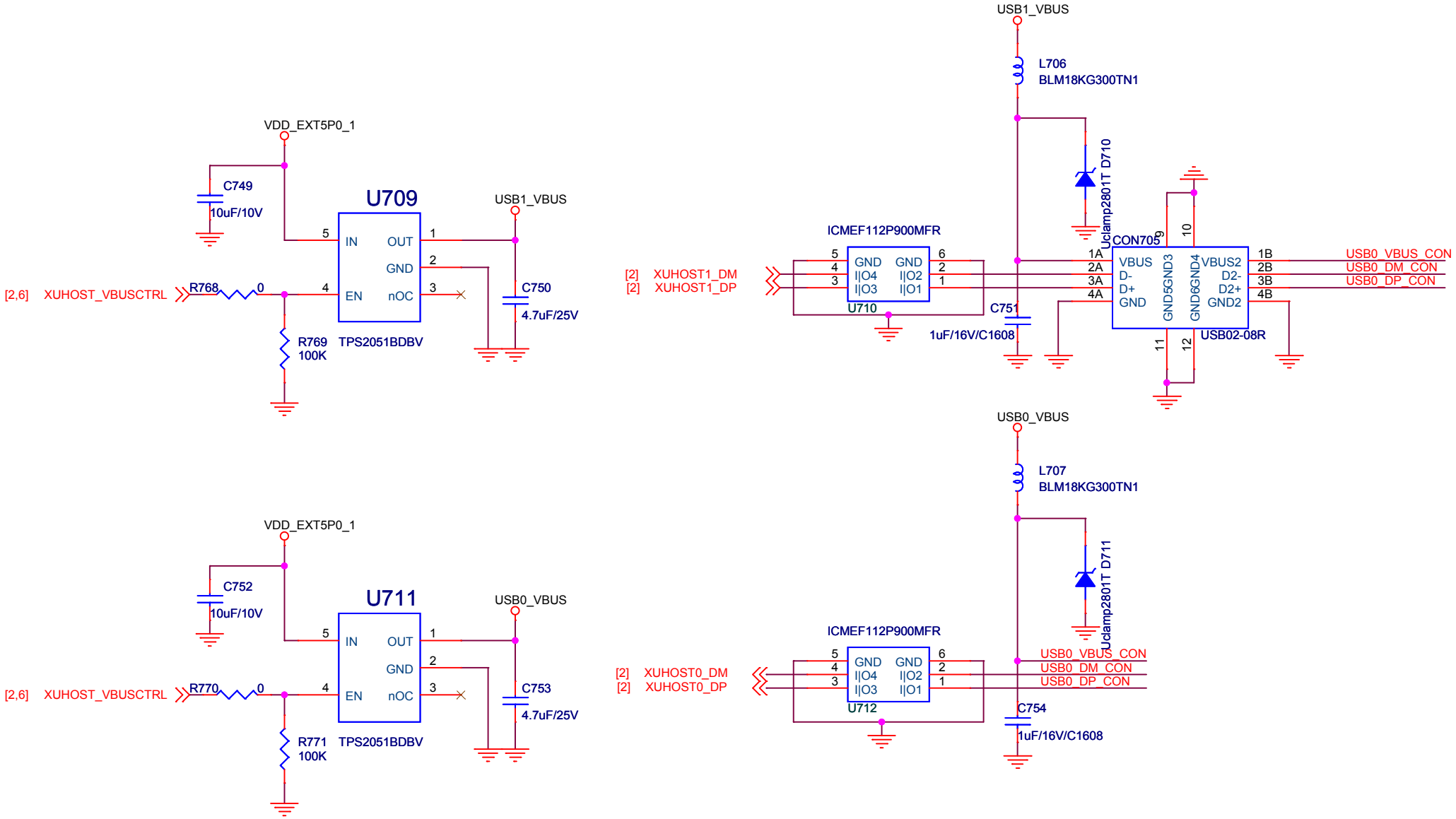
USB2.0 connector



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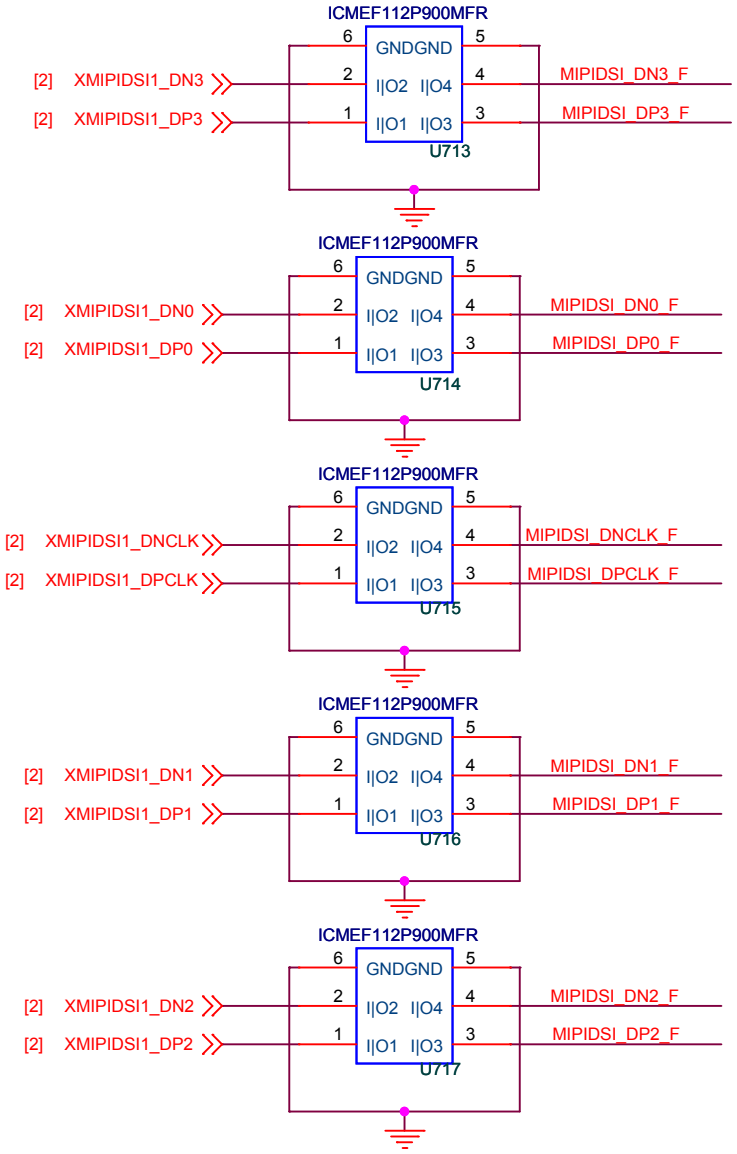
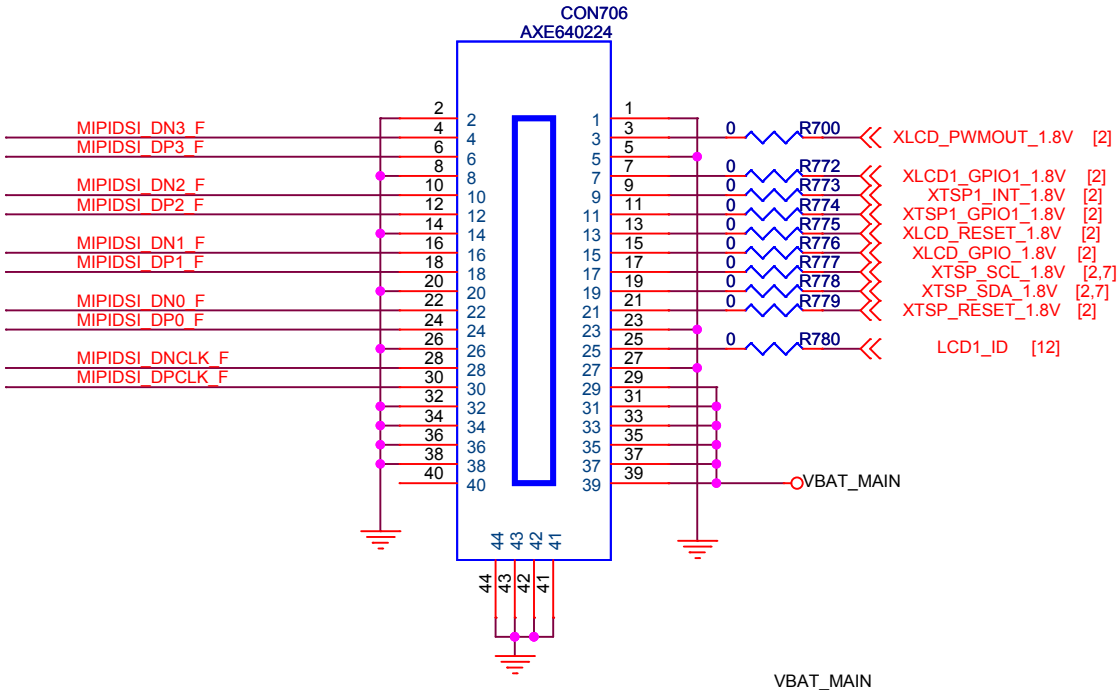
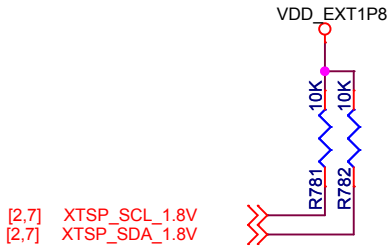
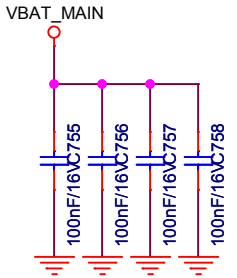
MIPI DSI



Common Mode ESD Filter







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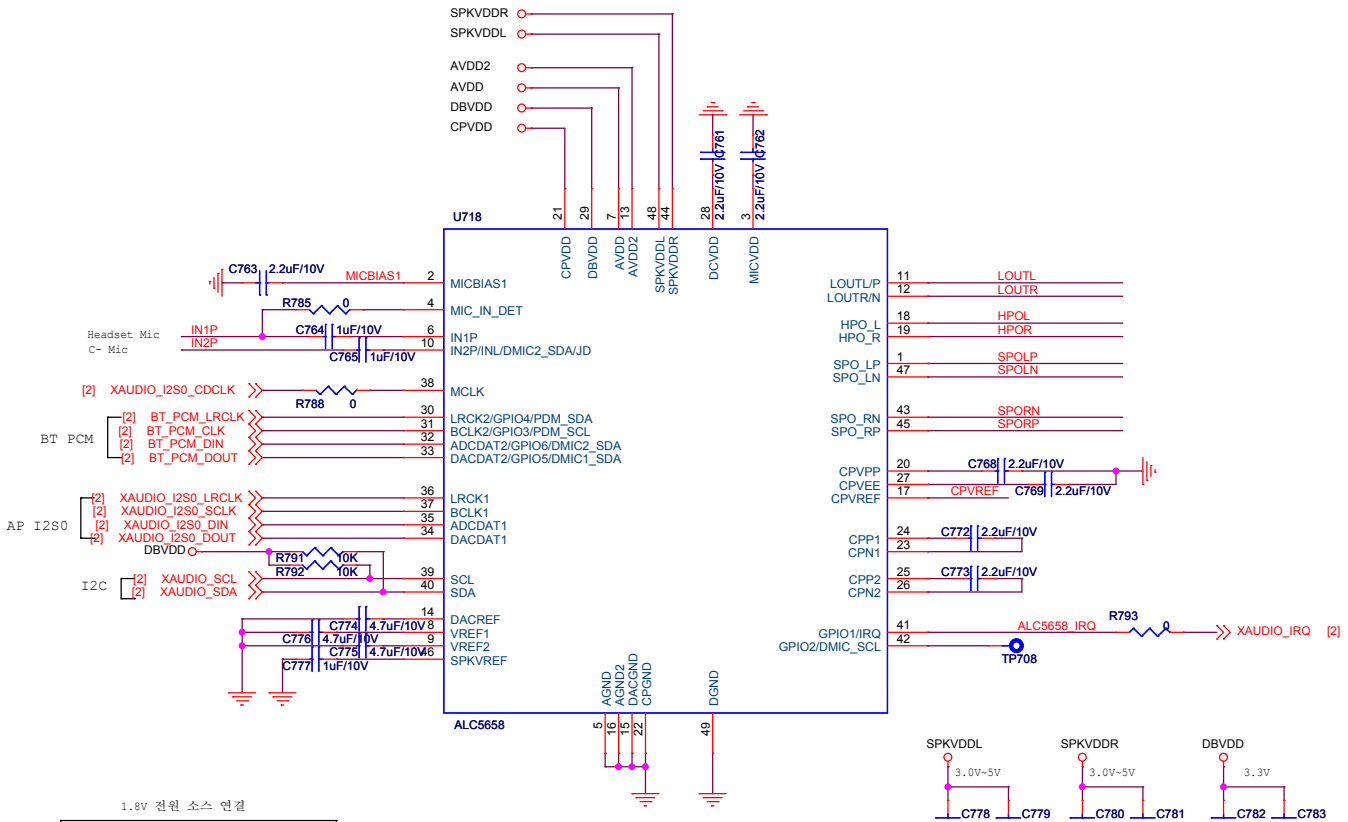
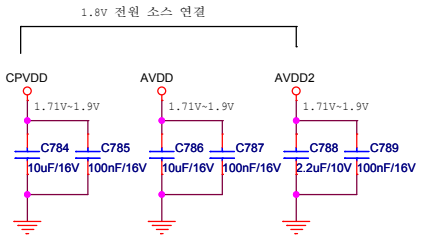
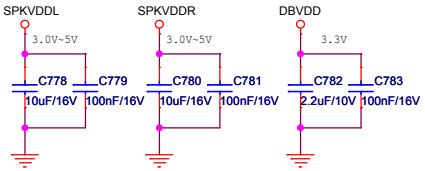
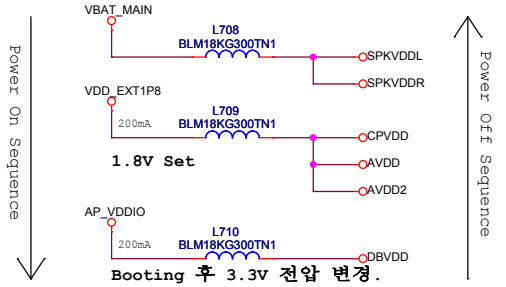
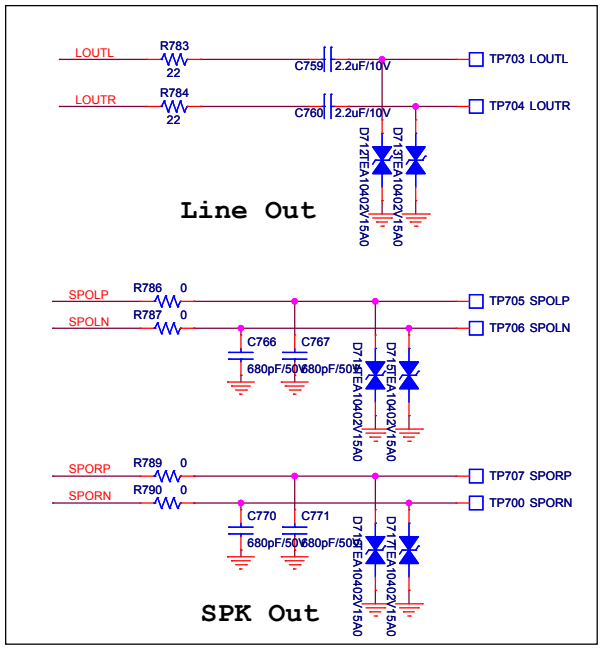
AUDIO CODEC

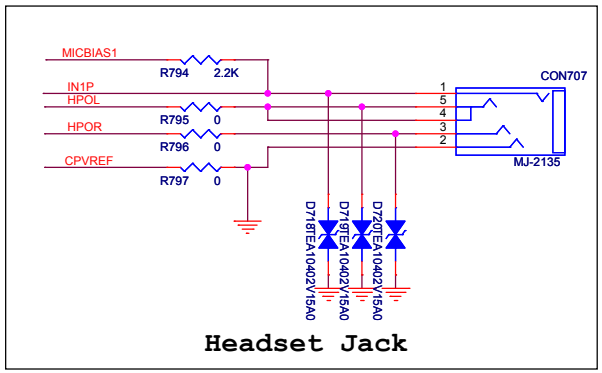


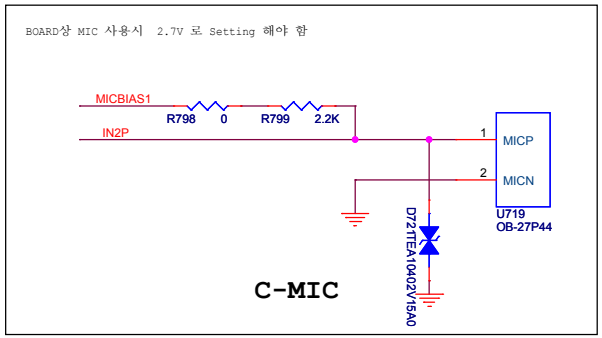












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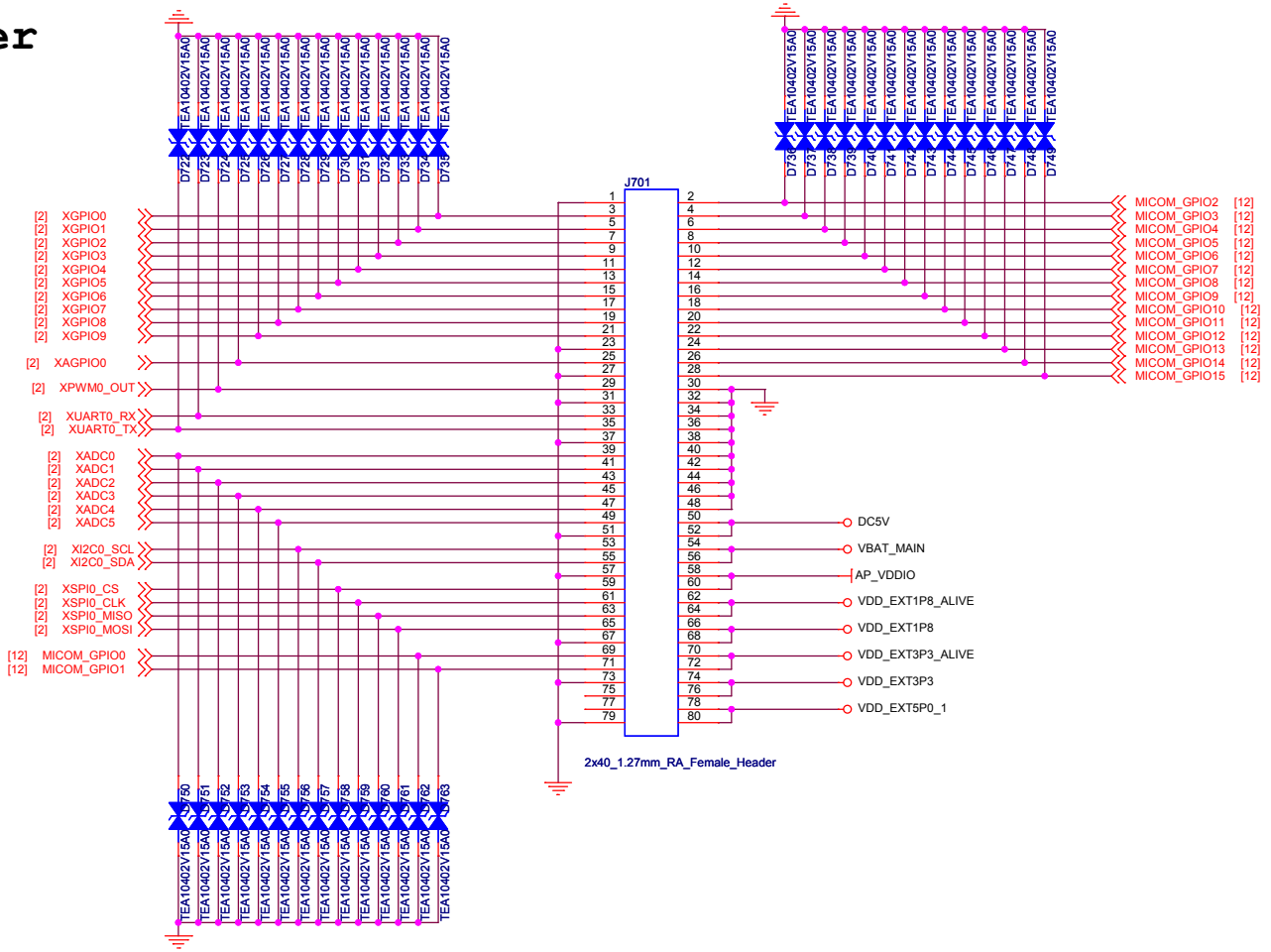
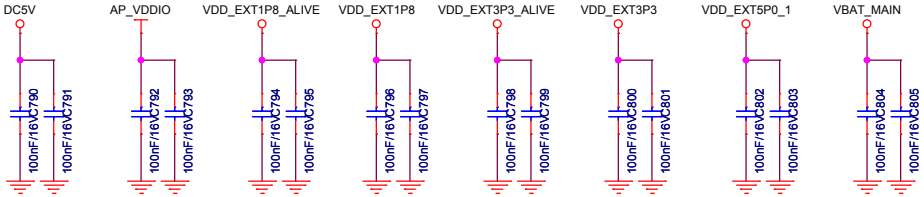
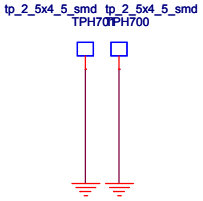
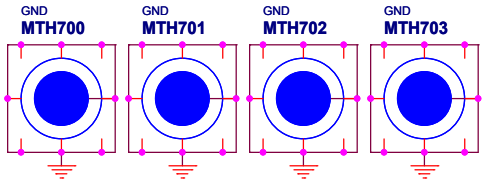
GPIO Test Header







GND TP HOLE



Mount Hole

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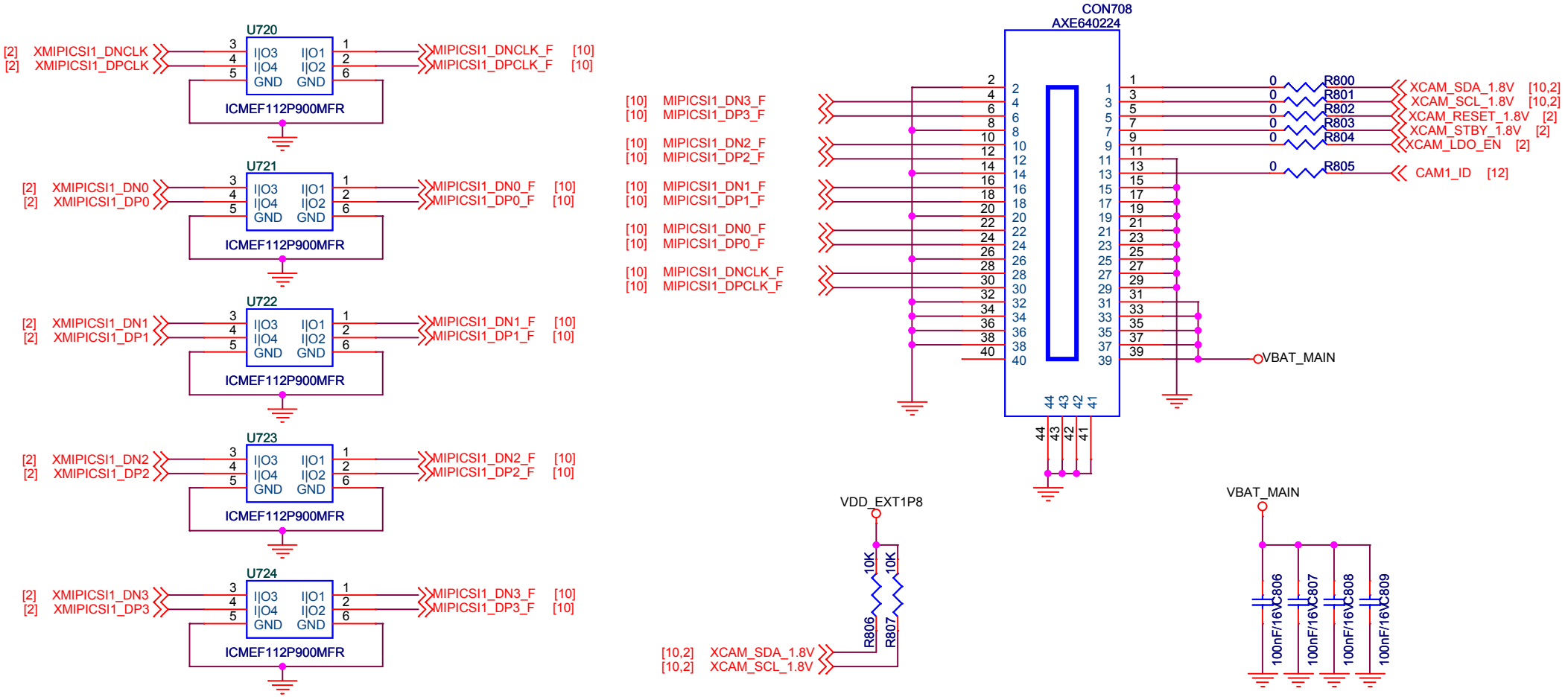
MIPI Camera1



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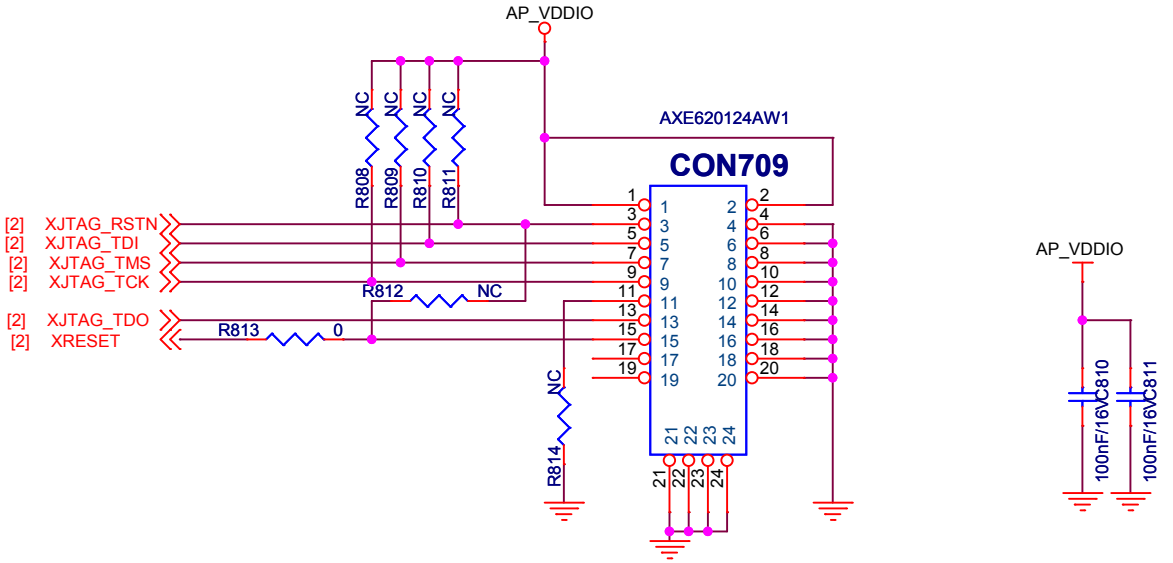
ARTIK JTAG



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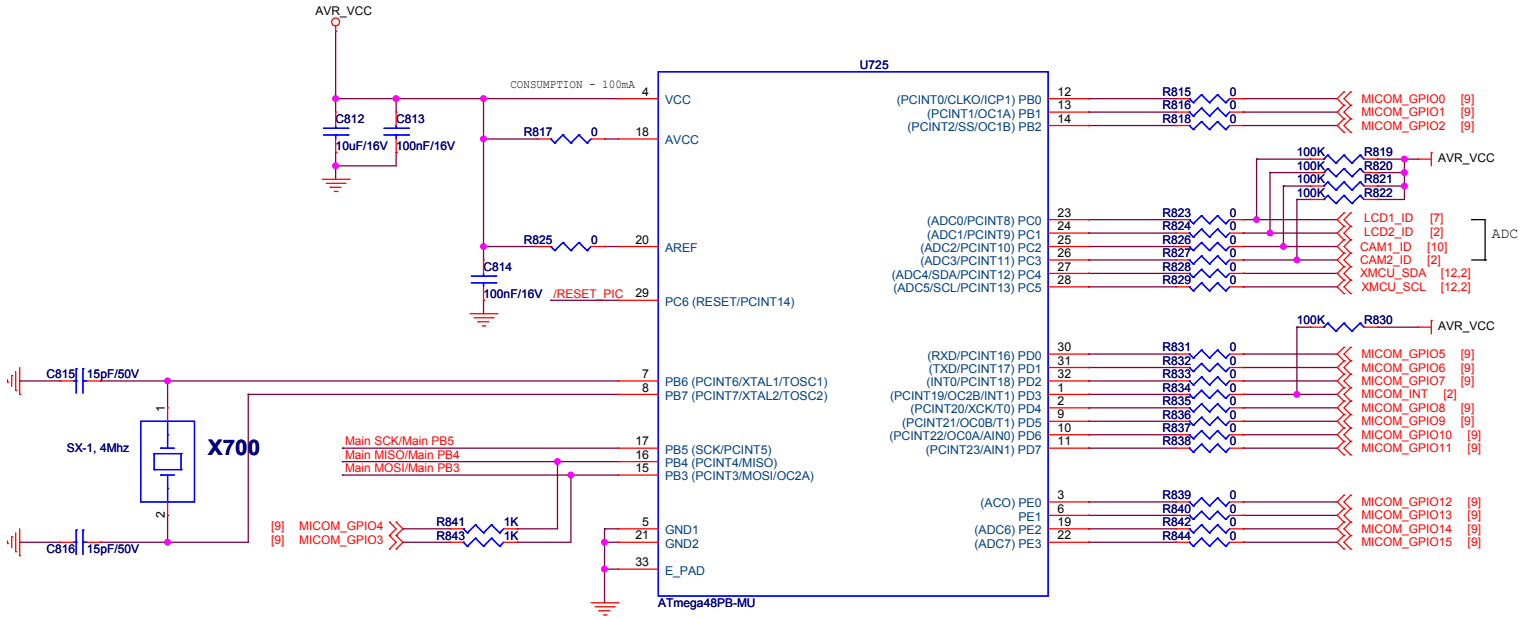
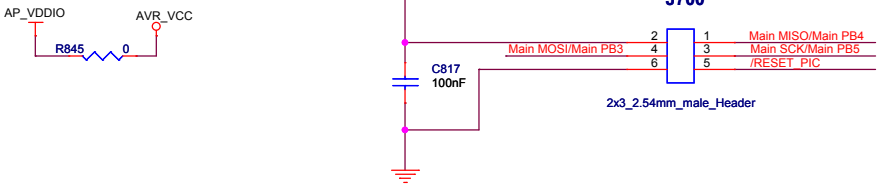
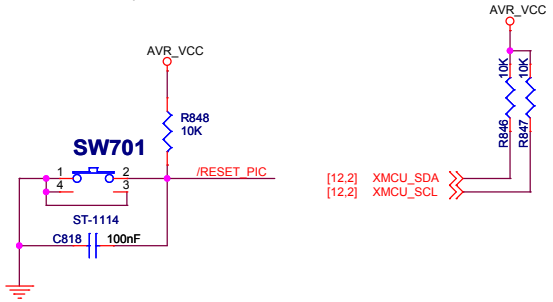
AVR MICOM



ISP Connector



Reset S/W



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REV #.#	DATE	REVISION HISTORY
0.1	16.05.30	U9 - RX PU 추가
	16.05.31	CON9 - DEBUG_VBUS 추가
	16.06.02	U52(MICBIAS1_2P0) - 삭제
		CON3, 6 - AXE640224으로 변경, 39PIN - 삭제
	16.06.03	L8 - SRU1028-6R8Y 변경
		AVR PU SOUCE - AVR_VCC로 변경
	16.06.07	X4 - SX-1로 변경
0.2	16.06.23	JP1, 2 - SMD로 변경
		reference number 변경
	16.06.24	CON704 - KQ20A1-7P-4D로 변경
	16.07.04	U719(MICROPHONE) - OB-27P44로 변경
	16.07.06	C738, 749, 752 - 원복
		U705 - SYNC : VBAT 10K로 연결
0.3	16.07.15	C815, 816 - 15pF으로 튜닝
	16.10.18	R713 - 240ohm -> 100ohm

REV #.#	DATE	REVISION HISTORY

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