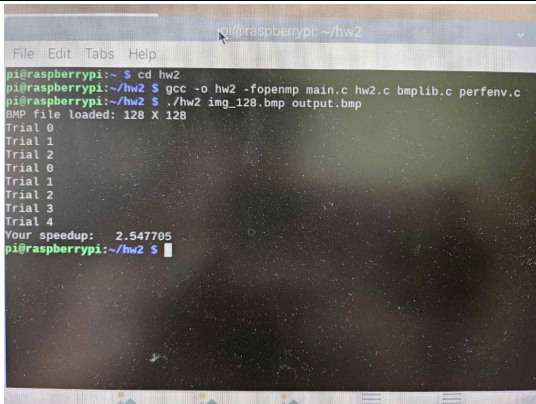
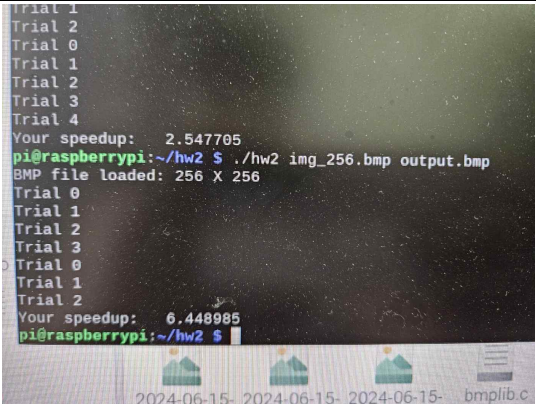
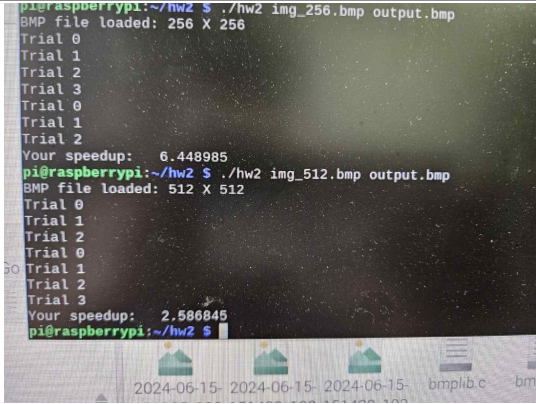
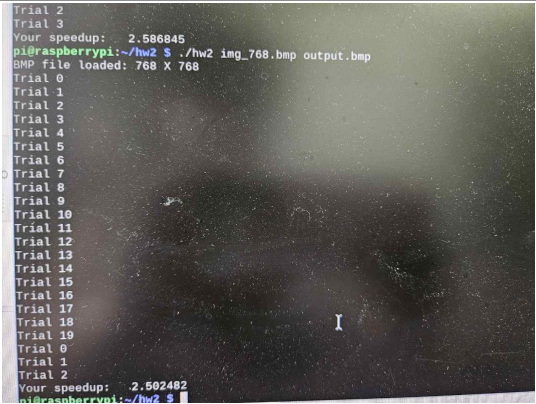


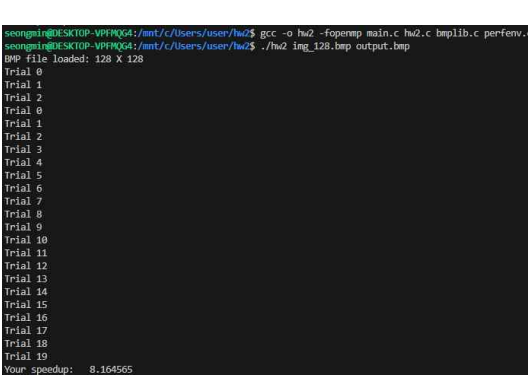
System programming report

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1. Implementation result

First, when the existing hw2.c code was executed for each bmp size, the speedup value was identified. As a result of the execution, it showed an average speedup of about 1.00. And after the code optimization, the code was first executed on the desktop in the Wsl environment. It showed 8.16 times the speedup performance based on 128 x 128 images, and ran with the corresponding code on Raspberry Pi. The execution time for each image is shown in the table below. It showed an average speed-up performance of 3.42 times in five images.

	
128 x 128 speed up : 2.55	256 x 256 speed up : 6.45
	
512 x 512 speed up : 2.59	768 x 768 speed up : 2.50

 <pre> Trial 13 Trial 14 Trial 15 Trial 16 Trial 17 Trial 18 Trial 19 Trial 0 Trial 1 Trial 2 Your speedup: 2.502482 pi@raspberrypi:~/hw2 \$./hw2 img_1024.bmp output.bmp BMP file loaded: 1024 X 1024 Trial 0 Trial 1 Trial 2 Trial 3 Trial 4 Trial 5 Trial 6 Trial 7 Trial 8 Trial 9 Trial 10 Trial 11 Trial 12 Trial 13 Trial 14 Trial 15 Trial 16 Trial 17 Trial 18 Trial 19 Your speedup: 3.000661 pi@raspberrypi:~/hw2 \$ </pre>	 <pre> seongmin@DESKTOP-VPMQ64:/mnt/c/Users/user/hw2\$ gcc -o hw2 -fopenmp main.c hw2.c bmpLib.c perfenv.c seongmin@DESKTOP-VPMQ64:/mnt/c/Users/user/hw2\$./hw2 img_128.bmp output.bmp BMP file loaded: 128 X 128 Trial 0 Trial 1 Trial 2 Trial 3 Trial 4 Trial 5 Trial 6 Trial 7 Trial 8 Trial 9 Trial 10 Trial 11 Trial 12 Trial 13 Trial 14 Trial 15 Trial 16 Trial 17 Trial 18 Trial 19 Your speedup: 8.164565 </pre>
1024 x 1024 speed up : 3.00	Wsl 기준 128 x 128 speed up : 8.16

2. Optimization approach

1. Removing Unnecessary Memory Allocation

In the case of the original code, memory is dynamically allocated and released for each pixel, which is a costly task, so minimizing this can greatly improve the performance. Therefore, the memory was not dynamically allocated to each pixel, but the result was directly allocated to the output image. This improved the performance by reducing the overhead of memory allocation and release.

2. Loop Order for Memory Access

The original code loops x first and y, but I looped y first and then x. Sequential memory access increases cache utilization, allows faster access, and uses locality to access continuous memo locations, enabling efficient processing.

3. Using Static Allocation for Pixel

Static memory allocation is faster than dynamic memory allocation. In other words, the performance was increased by reducing the overhead of dynamic memory management by using static allocation unlike the previous one.