

1. Description

1.1. Project

Project Name	stm32g070rbt6_cube
Board Name	custom
Generated with:	STM32CubeMX 6.8.1
Date	05/06/2023

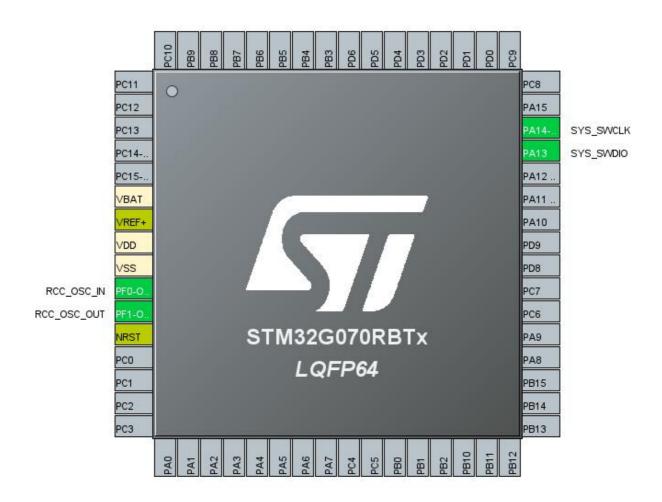
1.2. MCU

MCU Series	STM32G0
MCU Line	STM32G0x0 Value line
MCU name	STM32G070RBTx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	ARM Cortex-M0+

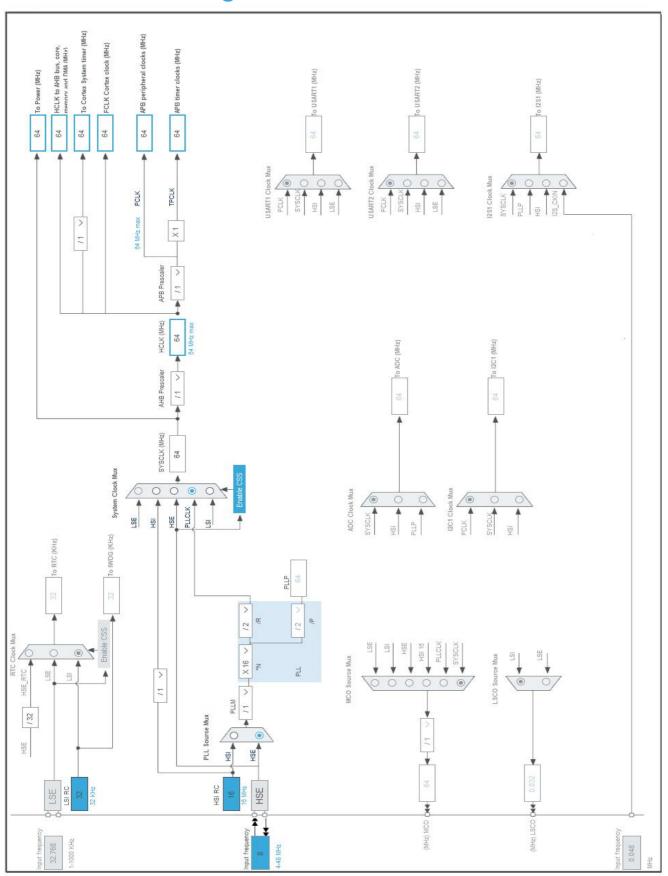
2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
7	VREF+	MonolO		
8	VDD	Power		
9	VSS	Power		
10	PF0-OSC_IN (PF0)	I/O	RCC_OSC_IN	
11	PF1-OSC_OUT (PF1)	I/O	RCC_OSC_OUT	
12	NRST	Reset		
45	PA13	I/O	SYS_SWDIO	
46	PA14-BOOT0	I/O	SYS SWCLK	

4. Clock Tree Configuration



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5. Software Project

5.1. Project Settings

Name	Value	
Project Name	stm32g070rbt6_cube	
Project Folder	C:\Users\gns2l\Documents\stm32cubeIDE\ws_jLController\stm32g070rbt6_cube	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_G0 V1.6.1	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32G0
Line	STM32G0x0 Value line
мси	STM32G070RBTx
Datasheet	DS12766_Rev0

6.2. Parameter Selection

Temperature	25
Vdd	3.0

6.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

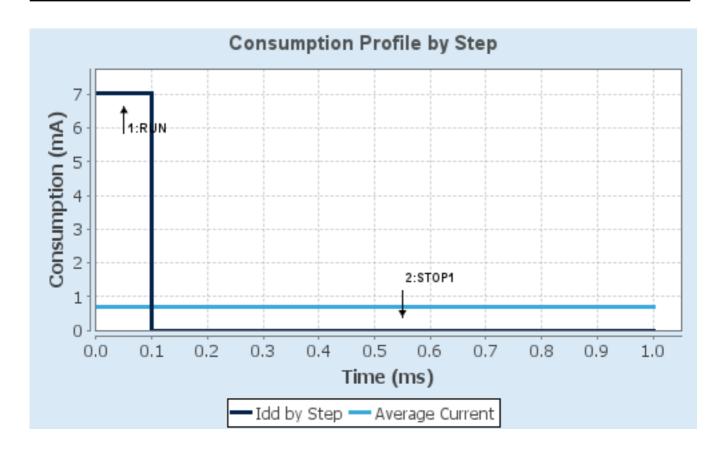
6.4. Sequence

Ston	Cton1	Ston 2
Step	Step1	Step2
Mode	RUN	STOP1
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	Flash-PowerDown
CPU Frequency	64 MHz	16 MHz
Clock Configuration	HSI PLL	HSI
Clock Source Frequency	16 MHz	16 MHz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	7.04 mA	3.74 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	80.0	0.0
Ta Max	128.63	130
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	707.37 µA
Battery Life	1 month, 10 days,	Average DMIPS	80.0 DMIPS
	18 hours		

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 64
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

Peripherals Clock Configuration:

Generate the peripherals clock configuration TRUE

7.2. SYS

mode: Debug

Timebase Source: SysTick

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PF0-OSC_IN (PF0)	RCC_OSC_IN	n/a	n/a	n/a	
	PF1- OSC_OUT (PF1)	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14- BOOT0	SYS_SWCLK	n/a	n/a	n/a	

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
System service call via SWI instruction	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	3	0	
Flash global interrupt	unused			
RCC global interrupt	unused			

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

^{*} User modified value

9. System Views

- 9.1. Category view
- 9.1.1. Current



10. Docs & Resources

Type Link

IBIS models https://www.st.com/resource/en/ibis_model/stm32g0_ibis.zip

System View https://www.st.com/resource/en/svd/stm32g0_svd.zip

Description

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Description

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stm8_embedded_software_solutions.pdf

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tools_portfolio.pdf

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