

ELECTRICAL ENGINEER PASSIONATE ABOUT SCALEABLE DATA INFRASTRUCTURE AND ACTIONABLE ANALYTICS IN MANUFACTURING. FOCUSED ON ENABLING SUCCESS THROUGH
ENTHUSIASTIC CROSS-FUNCTIONAL COLLABORATION.

□+1 979 633 8359 | ☑lisamaeanders@gmail.com | ☑leesahanders | 匝lisa-anders-engineer

Employment History

Illumina Feb 2019-Present

SR. PRODUCT ENGINEER

San Diego, CA, USA

- Deployed RStudio Connect server on Centos 8 / Red Hat on-prem server and aligned with information security / data governance requirements leading to successful adoption cross-company with usage by over 7 depaartments and migration to cloud server.
- Developed and maintained scaleable data pipelines and ETL's on Microsoft SQL, Denodo, Postgres, and tool log data sources for transparent, scaleable, and democratized data insights.
- Created Rmarkdowns, Shiny webapps, Tableau reports, and SPC charts enabling users to make strategic decisions. Example: Developed quasi-realtime Graft Expiring Flow Cells widget which has resulted in >17k / quarter scrap avoidance by the manufacturing team.
- CAPA / excursion response team member gathering and visualizing key process data and led causal analysis studies leading to successful resolution of issues on two separate customer impacting events.

Tronics MEMs, A TDK Group Company

Sept 2017-Feb 2019

BIOMEMS PROCESS AND INTEGRATION ENGINEER

Addison, TX, USA

- Product lead for developing the processes and metrology for manufacturing 4 unique bioMEMS devices. Managed project schedule and inventory tracking using Waterfall and Agile Methodology. Developed first ever dashboard reporting on critical product and equipment metrics, including tool up/down time, SPC data, and product CPK.
- Renegotiated customer specifications on custom product after improving quality of manufacturing process. Identified root causes of low anodic bond yield through statistical data analysis and DOE's and implemented new clean procedure and best practices to improve process yield by over 30 percent. Developed KOH wet etch for achieving very low surface roughness.

Texas Instruments: Leadership Development Rotation Program

Aug 2014-Aug 2017

PROCESS INTEGRATION ENGINEER

Dallas, TX, USA

• Designed test structures, ran DOE's, and analyzed electrical data and Wafer Level Reliability (WLR) data to characterize parasitic diode elements on new precision analog technology for automotive applications.

PROBE PRODUCT/APPLICATION ENGINEER

Clark, Philippines

• Implemented SQL/Java parametric drift web application as part of team of developers. Applied statistical process controls, six sigma, and WECO statistics, to catch failures before yield impact.

FAB PRODUCT ENGINEER Dallas, TX

• Analyzed wafer signatures to improve site-wide yield and reduce sources of failure and enabled manufacturing ramp of 5 new devices as part of New Product Introduction (NPI) team.

Education

Virginia Tech Aug 2014

M.S. ELECTRICAL ENGINEERING, BIOMEDICAL EMPHASIS

Blacksburg, VA

- Thesis: Lab on a chip rare cell isolation platform with dielectrophoretic smart sample focusing, automated whole cell tracking analysis script, and bioinspired on-chip electroactive polymer micropump
- Highlight Courses: Machine Vision I and II (OpenCV w/ Matlab), MEMS Manufacturing

University of Alaska Fairbanks

Aug 2012

Fairbanks, AK

· Minor: Biology

B.S. ELECTRICAL ENGINEERING

Skills

Data Skills / Programming: SQL/VQL, Matlab, R, RMarkdown, Shiny, HTML, CSS, Denodo, Microsoft SQL Server, AWS (S3), Git/Github, DBeaver

Software: Spotfire, Dataware, Cadence, Wafer Sleuth, JMP, Solidworks, Matlab, MES, Comsol/Matlab, Klayout, CAD, RStudio, Centos / RedHat Linux, Windows, Microsoft Office, Asana, Jira

• Currently learning: Python, Neo4J, Docker

General: Analysis, manufacturing, bench testing, project management, FMEA, ISO 9001, ISO 13485, ITAR, GMP