

Operating System: Swapping Policies

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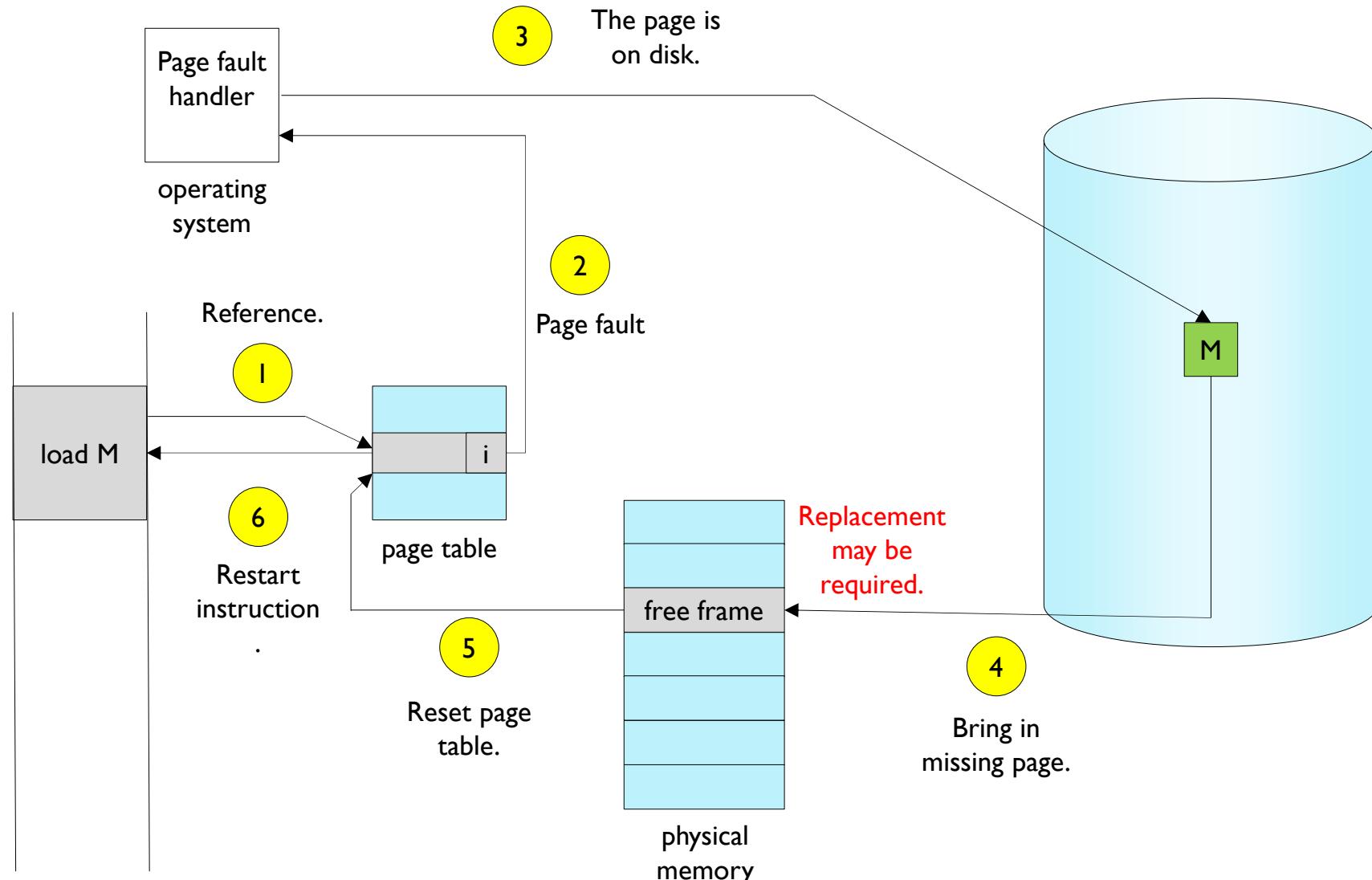
Beyond Physical Memory: Policies

- Memory pressure forces the OS to start **paging out** pages to make room for actively-used pages
- Deciding which page to evict is encapsulated within the replacement policy of the OS

기억압박
어떤 페이지를 제거할까?

Demand Paging (review)

- Page fault handling



Demand Paging (Cont.)

- Page Fault Rate $0 \leq p \leq 1.0$
 - if $p = 0$, no page faults
 - if $p = 1$, every reference is a fault
- Effective Access Time (EAT)
 - $EAT = (1 - p) \times \text{memory access time} + p \times \text{page fault service time}$
 - Example
 - Memory access time = 200 ns
 - Average page fault service time = 8 ms (=8,000,000ns)
 - $EAT = (1 - p) \times 200 + p \times 8,000,000 = 200 + p \times 7,999,800$
 - If one access out of 1,000 causes a page fault, then
 - ✓ $EAT = 8,200$ ns
 - ✓ This is a slowdown by a factor of 40
 - It is important to keep the page fault rate low
 - Good page replacement policy is required

여기서는 하드웨어
page fault rate이
낮으면 낮을수록 EAT를
낮출수 있으니 replacement policy가
적극적이어야 하는 것은 page fault rate를
줄여주기 위해서
할수 있는지 있음.

Page Replacement

- Page replacement
 - find some page not really in use → swap it out
 - Good page replacement algorithm
 - results in minimum number of page faults
- Locality of reference *Locality가 기반하여 일쓰는 page 2/3 예측*
 - Same pages may be brought into memory several times
 - A phenomenon observed in most programs in practice
 - A program behavior that intensively references only a small number of pages at a certain time
 - ex. loop
 - Reason for the better performance of the paging system

Page Replacement

- Ideal algorithm
 - gains the **lowest page-fault rate**
- Algorithm can be evaluated by
 - running it on a particular string of memory references,
➤ **reference string** ~ **이전에 사용한 주소를 통해 평가**
 - and computing **the number of page faults** on that string
- In some examples, the reference string is
 - 1, 2, 3, 4, 1, 2, 5, 1, 2, 3, 4, 5

The Optimal Replacement Policy

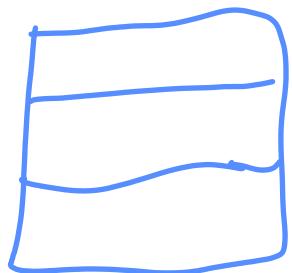
이상적인 교체정책

- Leads to the fewest number of misses overall
 - Replaces the page that will be accessed furthest in the future 가장 나중에 참조될 페이지를 내쫓는거임.
 - Resulting in the **fewest-possible** cache misses
결과적으로 가장 낮은 캐시 미스가 생기는데 이쪽에 이론적으로 가장 낮은 페이지 fault 를 넣어 ease 를 만들고 이와 실제 구현되는 알고리즘과
- Used for measuring how well your algorithm **비교하기위함**. performs
 - Optimal algorithm presents **lower bound** of page
 - Serve only as a comparison point, to know how close we are to **perfect**


Tracing the Optimal Policy

빈통이 놓기 때문에
초기에는 빙가는 미스~

cold-start miss →



한번에
여러개를 빙기하는 것

Reference Row										
---------------	--	--	--	--	--	--	--	--	--	--

Access	Hit/Miss?	Evict	Resulting Cache State
0	Miss		0
1	Miss		0,1
2	Miss		0,1,2
0	Hit		0,1,2
1	Hit		0,1,2
3	Miss	2	0,1,3
0	Hit		0,1,3
3	Hit		0,1,3
1	Hit		0,1,3
2	Miss	3	0,1,2
1	Hit		0,1,2

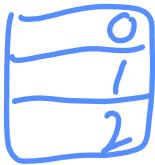
$$\text{Hit rate is } \frac{Hits}{Hits + Misses} = 54.6\%$$

Future is not known

A Simple Policy: **FIFO**

- Pages were placed in a queue when they enter the system
- When a replacement occurs, the page on the tail of the queue(the “First-in” pages) is evicted
 - It is simple to implement, but can’t determine the importance of blocks

Tracing the FIFO Policy



Reference Row

0 1 2 0 1 3 0 3 1 2 1

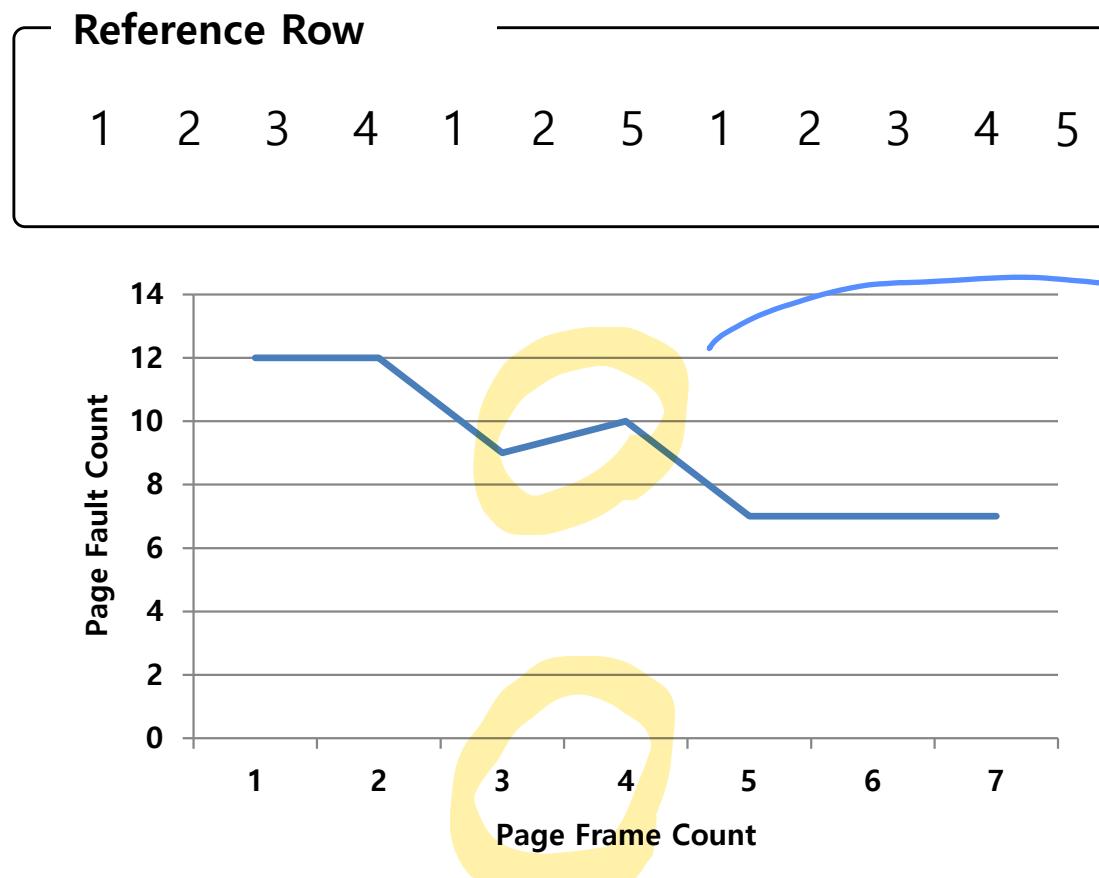
Access	Hit/Miss?	Evict	Resulting Cache State
0	Miss		0
1	Miss		0,1
2	Miss		0,1,2
0	Hit		0,1,2
1	Hit		0,1,2
3	Miss	0	1,2,3
0	Miss	1	2,3,0
3	Hit		2,3,0
1	Miss	2	3,0,1
2	Miss	3	0,1,2
1	Hit		0,1,2

$$\text{Hit rate is } \frac{Hts}{Hts + Misses} = 36.4\%$$

Even though page 0 had been accessed a number of times, FIFO still kicks it out

BELADY'S ANOMALY

- We would expect the cache hit rate to increase when the cache gets larger. But in this case, with FIFO, it gets worse



Frame의 크기↑
늘었을 때도 fault↑
는 줄 알았는데
BelADY'S
ANoMALY
라고 함.

BELADY'S ANOMALY (Cont'd)

이전 열이 옮겨오는 이유는 stack이며
Frame을 수거할 증가하는 그 증가로
Frame이 있던 크기의 Frame을 다 가지면
있는 경우임. 그래서 FIFO는 고장나는 편.
증가.

Reference:

$$PF \text{ rate} = 9 / 12$$



Reference:

$$PF \text{ rate} = 10 / 12$$



frame이 증가하는 이유로 이전 크기의 인스를 제거함.
로마 국립 대학교
Kyung Hee University

Another Simple Policy: Random

기능 간접적

시스템 관리 학습자에게
안정성이 선호된
다는 관점에서는
그리 좋은
알고리즘은
아님.

- Picks a random page to replace under memory pressure

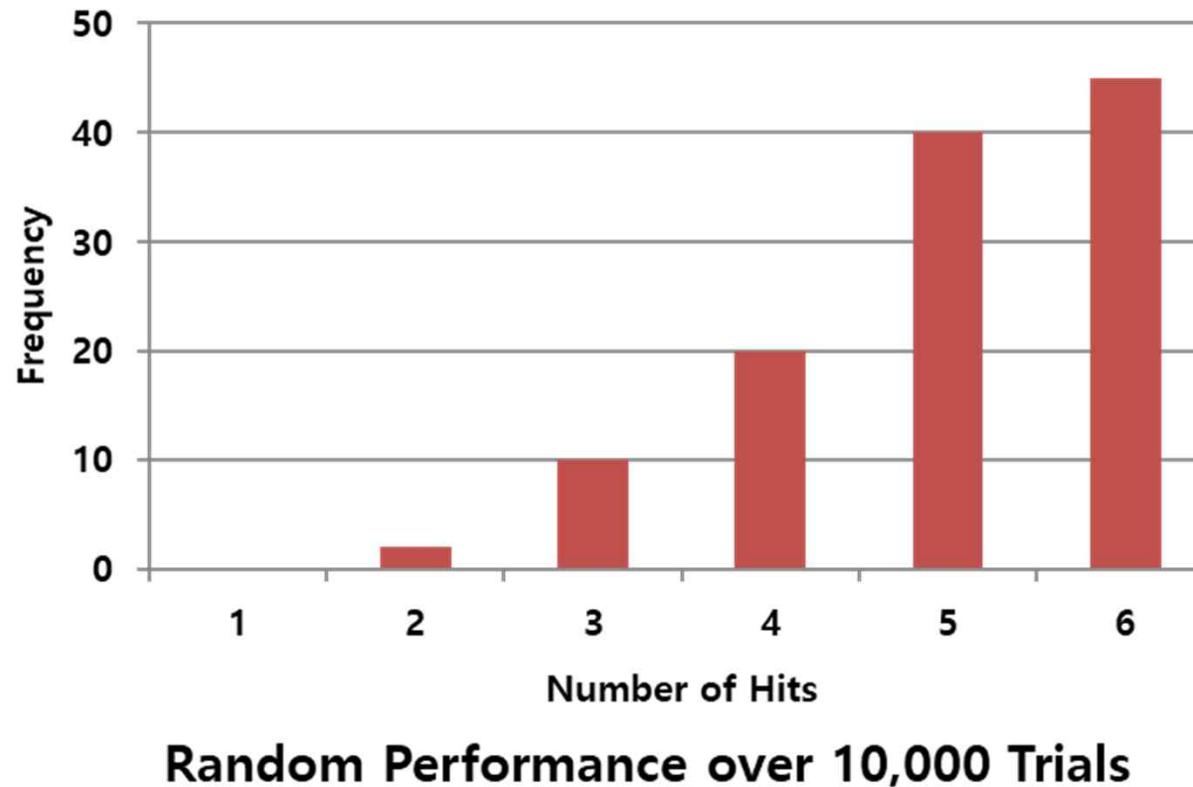
- It doesn't really try to be too intelligent in picking which blocks to evict

- Random depends entirely upon how lucky Random gets in its choice

Access	Hit/Miss?	Evict	Resulting Cache State
0	Miss		0
1	Miss		0,1
2	Miss		0,1,2
0	Hit		0,1,2
1	Hit		0,1,2
3	Miss	0	1,2,3
0	Miss	1	2,3,0
3	Hit		2,3,0
1	Miss	3	2,0,1
2	Hit		2,0,1
1	Hit		2,0,1

Random Performance

- Sometimes, Random is as good as optimal, achieving 6 hits on the example trace



Using History

이전을 보고

- Lean on the past and use history

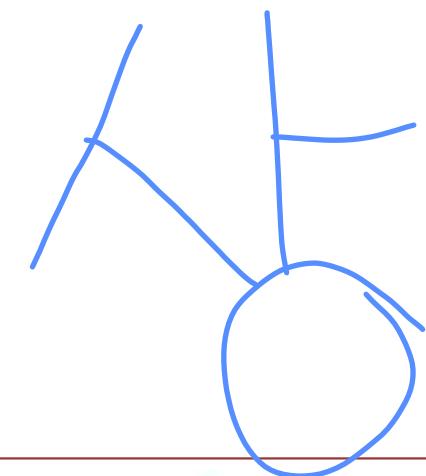
- Two type of historical information

Historical Information	Meaning	Algorithms
recency	The more recently a page has been accessed, the more likely it will be accessed again	LRU
frequency	If a page has been accessed many times, It should not be replaced as it clearly has some value	LFU

least recent used
least frequently used

가장 최근에 사용된 것

고장 CH



Using History : LRU

- Replaces the least-recently-used page

Reference Row										
0	1	2	0	1	3	0	3	1	2	1

Access	Hit/Miss?	Evict	Resulting Cache State
0	Miss		0
1	Miss		0,1
2	Miss		0,1,2
0	Hit		1,2,0
1	Hit		2,0,1
3	Miss	2	0,1,3
0	Hit		1,3,0
3	Hit		1,0,3
1	Hit		0,3,1
2	Miss	0	3,1,2
1	Hit		3,2,1

Using History : LRU (Cont'd)

- Least Recently Used
 - Replace the page that has not been used for the longest time in the **past**
 - Use past to predict the future
 - cf. OPT wants to look at the future
 - With locality, LRU approximates OPT
 - “**Stack**” algorithm: does not suffer from Belady's anomaly.
 - 이거 안일어남.
 - Harder to implement: must track which pages have been accessed
 - 접근했던 시간을 저장해두면 주전이 어려움.
 - Does not consider the frequency of page accesses
 - Does not handle all workloads well

Using History : LRU (Cont'd)

<Flame>



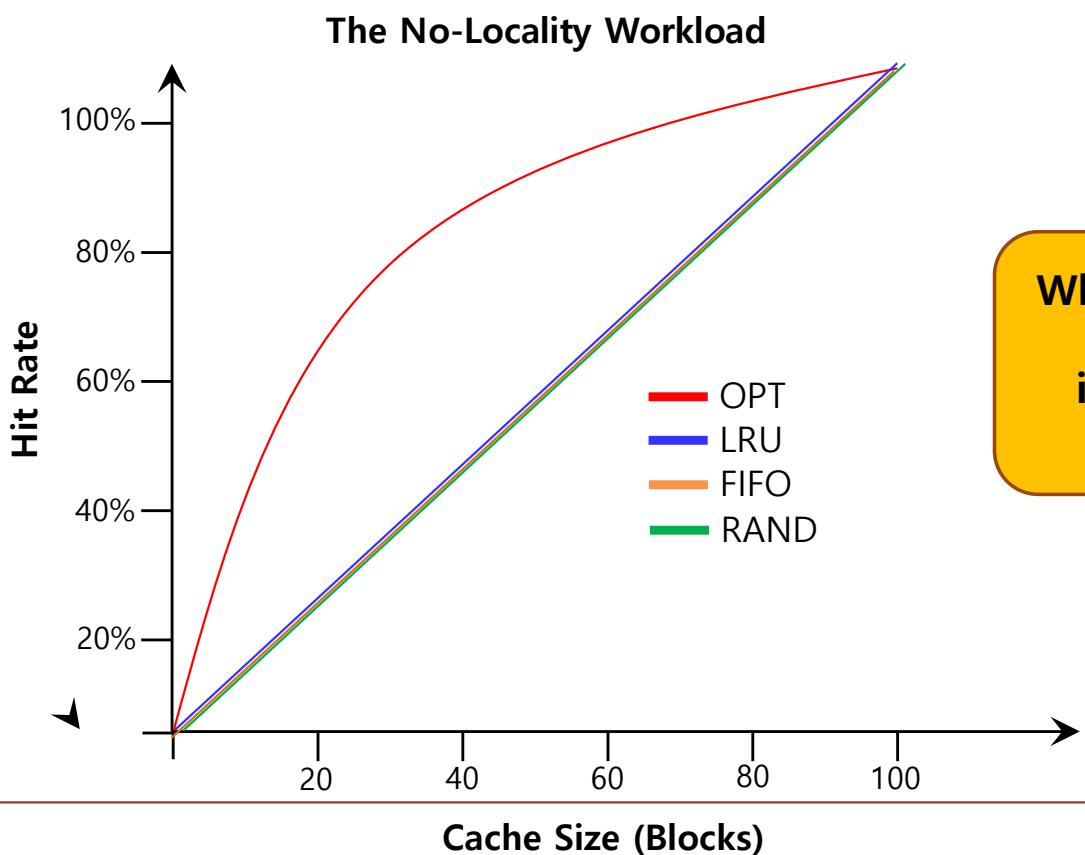
- Stack algorithms

- Policies that guarantee increasing memory size does not increase the number of page faults (e.g. OPT, LRU, etc.)
- Any page in memory with m frames is also in memory with $m+1$ frames (called **stack property**)



Workload Example : The No-Locality Workload

- Each reference is to a random page within the set of accessed pages
 - Workload accesses 100 unique pages over time
 - Choosing the next page to refer to at random

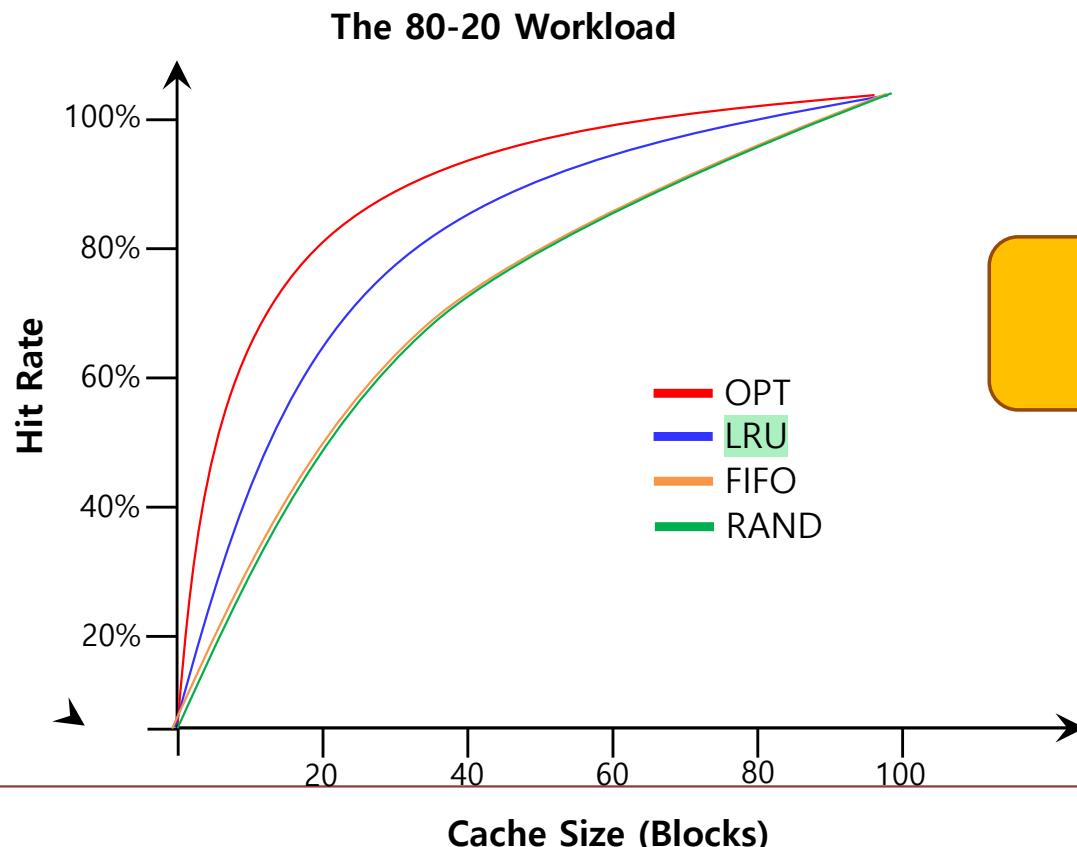


work load의 Locality가 높거나 낮거나.
알고리즘은 차이가 있다.

When the cache is large enough to fit the entire workload, it also **doesn't matter** which policy you use

Workload Example : The 80-20 Workload

- Exhibits locality: 80% of the **reference** are made to 20% of the page
- The remaining 20% of the **reference** are made to the remaining 80% of the pages



작업의 80%가 20%
페이지에 접근한다
= Locality 고유 .

LRU is more likely to
hold onto the **hot pages**

LRU는 LRU가 더 좋다.

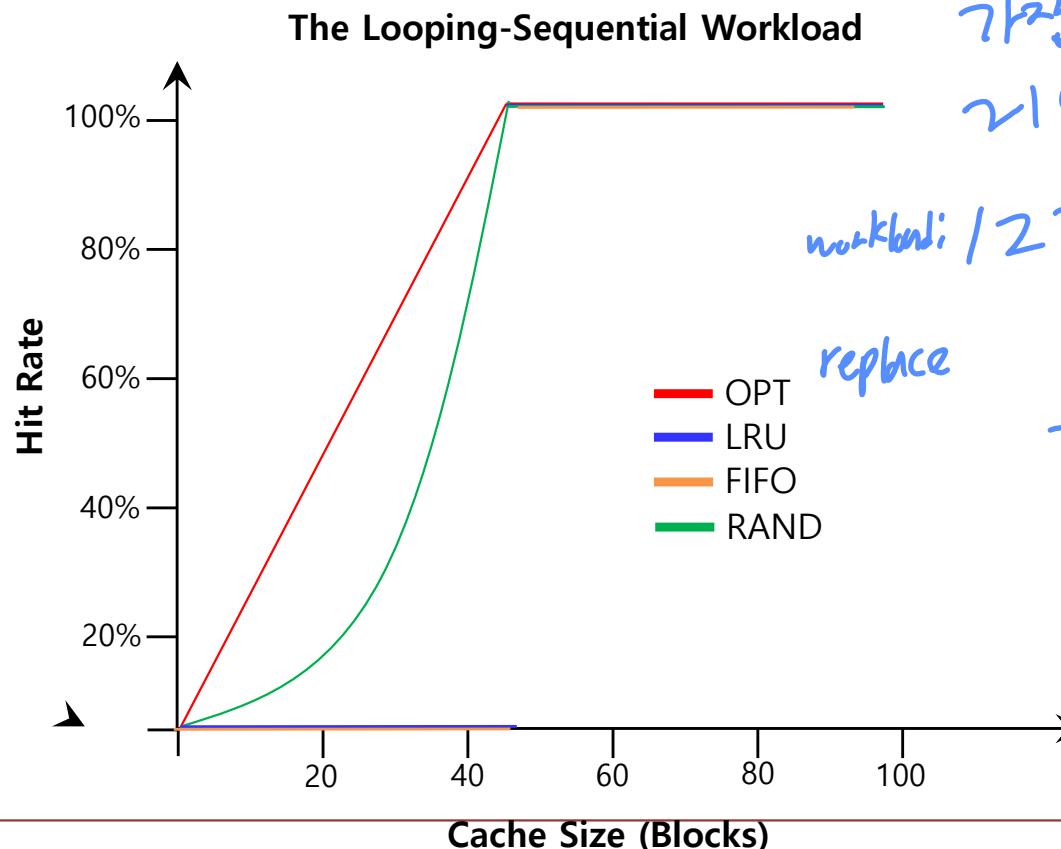
Workload Example : The Looping Sequential

워크로드에 따른 알고리즘의
성능이 필요.

수프를 든다고 생각하면
가장 예전 것을 계속 사용하는
경우 LRU 성능이 ↓

- Refer to 50 pages in sequence

- Starting at 0, then 1, … up to page 49, and then we Loop, repeating those accesses, for total of 10,000 accesses to 50 unique pages



workload: 123 123 123 123
↓
replace 1 2 3 1 2 3 1 2 3 1
수프를 드는 그릇을 넣어놓았을 때
가장 0%인 수발.

Implementing Historical Algorithms

- To keep track of which pages have been least-and-recently used, the system has to do some accounting work on every memory reference
 - Add a little bit of hardware support

페이지 카운트 기록을 2진수로 미트
Page 9.

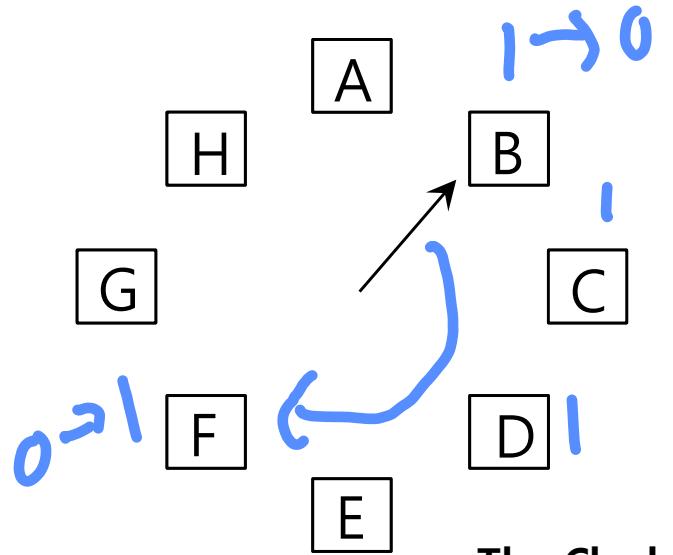
Approximating LRU

- Require some hardware support, in the form of a use bit
 - Whenever a **page is referenced**, the use bit is set by hardware to 1
 - Hardware **never** clears the bit, though; that is the responsibility of the OS
- Clock Algorithm
 - All pages of the system arranges in a circular list
 - A clock hand points to some particular page to begin with

Clock Algorithm

- The algorithm continues until it finds a use bit that is set to 0

시간 기록 X
참조 여부만
LRU와
동일 X



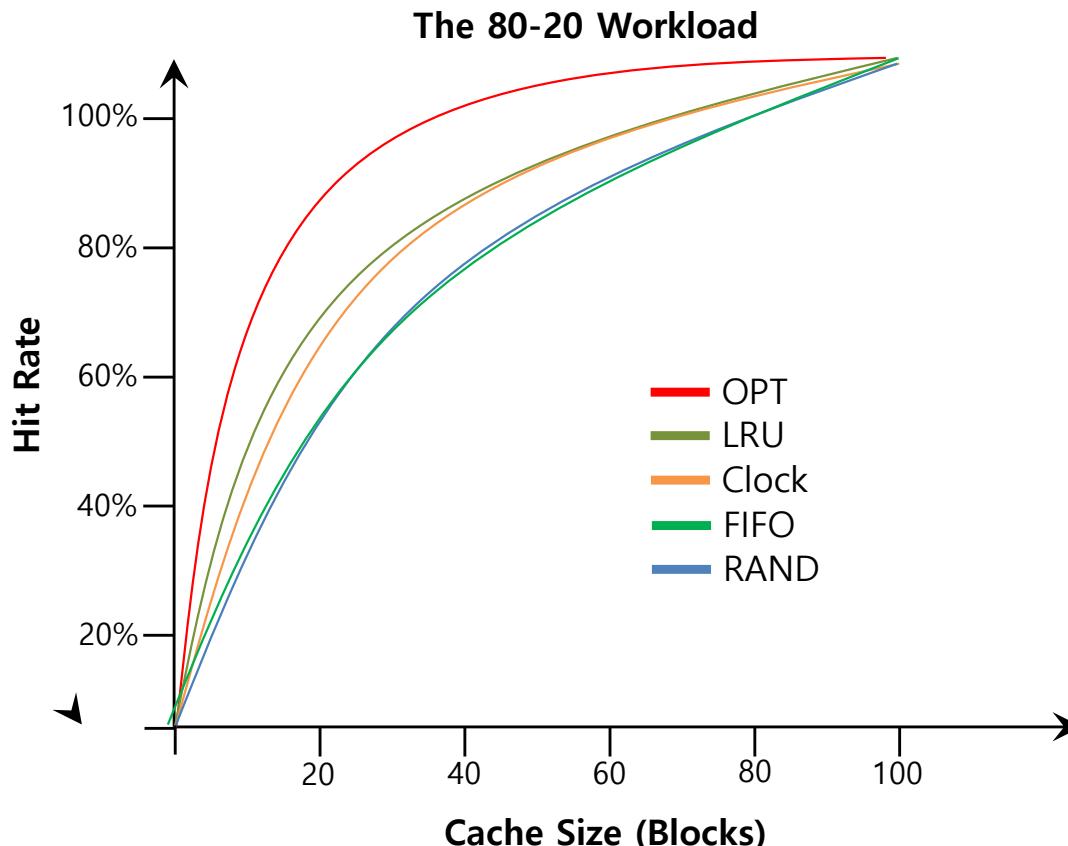
Use bit	Meaning
0	Evict the page
1	Clear Use bit and advance hand

The Clock page replacement algorithm

When a page fault occurs, the page the hand is pointing to is inspected.
The action taken depends on the Use bit

Workload with Clock Algorithm

- Clock algorithm doesn't do as well as perfect LRU, it does better then approach that don't consider history at all



Considering Dirty Pages

고체하드웨어

- The hardware include a modified bit (a.k.a dirty bit)
 - Page has been modified and is thus dirty, it must be written back to disk to evict it
 - Page has not been modified, the eviction is free

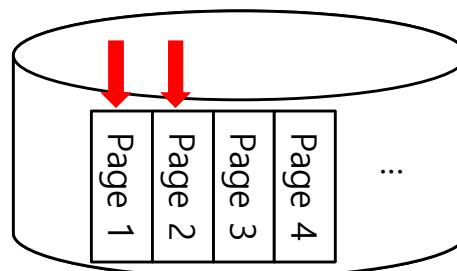
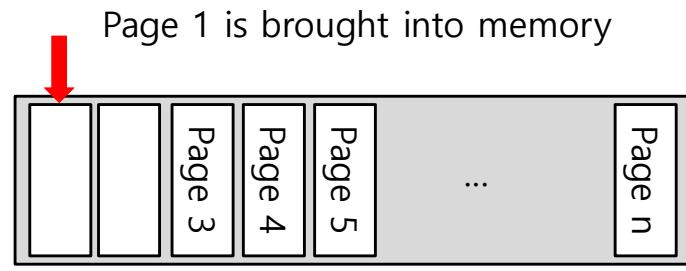
이전까지
도는 알고.

Page Selection Policy

- The OS has to decide when to bring a page into memory
- Presents the OS with some **different options**

Prefetching

- The OS guess that a page is about to be used, and thus bring it in ahead of time



Page 2 likely soon be accessed and thus should be brought into memory too

Locality \approx 고려

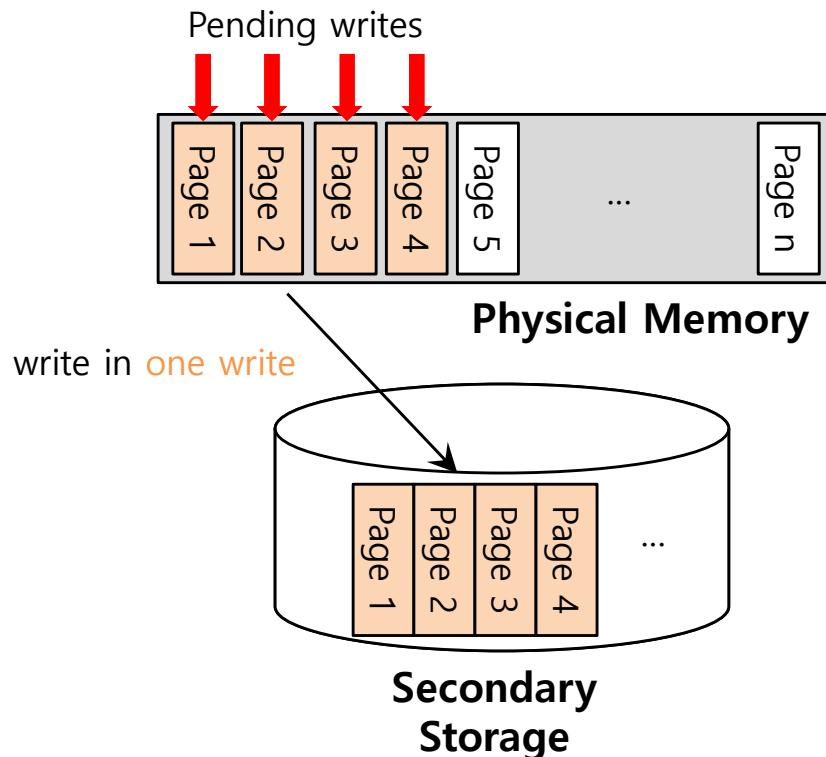
2개 Pages

가장 빠른

일정 ~?

Clustering, Grouping

- Collect a number of **pending writes** together in memory and write them to disk in **one write**
 - Perform a single large write more efficiently than many small ones



지정한 대로
그룹화해서 쓰기.

Thrashing

- If a process does not have “enough” page frames, page fault rate is very high
 - Thrashing

- A process is busy in swapping pages in and out

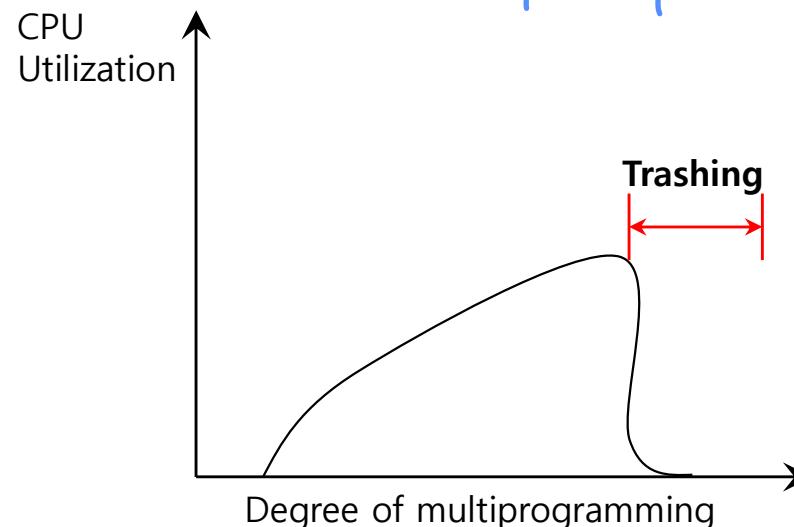
- Why does thrashing occur?

– Σ size of locality > total memory size

$$4+2=6$$

$$3+1=4$$

한번의 Swapping
한번의 CPU Utilization



ex) A process

e → 4 (1234) 1234 · · ·

B process

2 (56) 56 · · ·

Page

Working-set Model

특정시간동안 그 이전 구간의
Locality는 window 37개로는 놓기

- Working set model
 - is based on the assumption of **locality**
 - $\Delta \equiv$ **working-set window** \equiv a fixed number of page references
- WSS_i (working set of Process P_i)
 - total number of pages referenced in the most recent Δ
 - if Δ is too small, it will not encompass entire locality
 - if Δ is too large, it will encompass several localities
 - ✓ if $\Delta = \infty$, it will encompass entire program



- $D = \sum WSS_i \equiv$ total demand frames
 - If $D > m$, **Thrashing**