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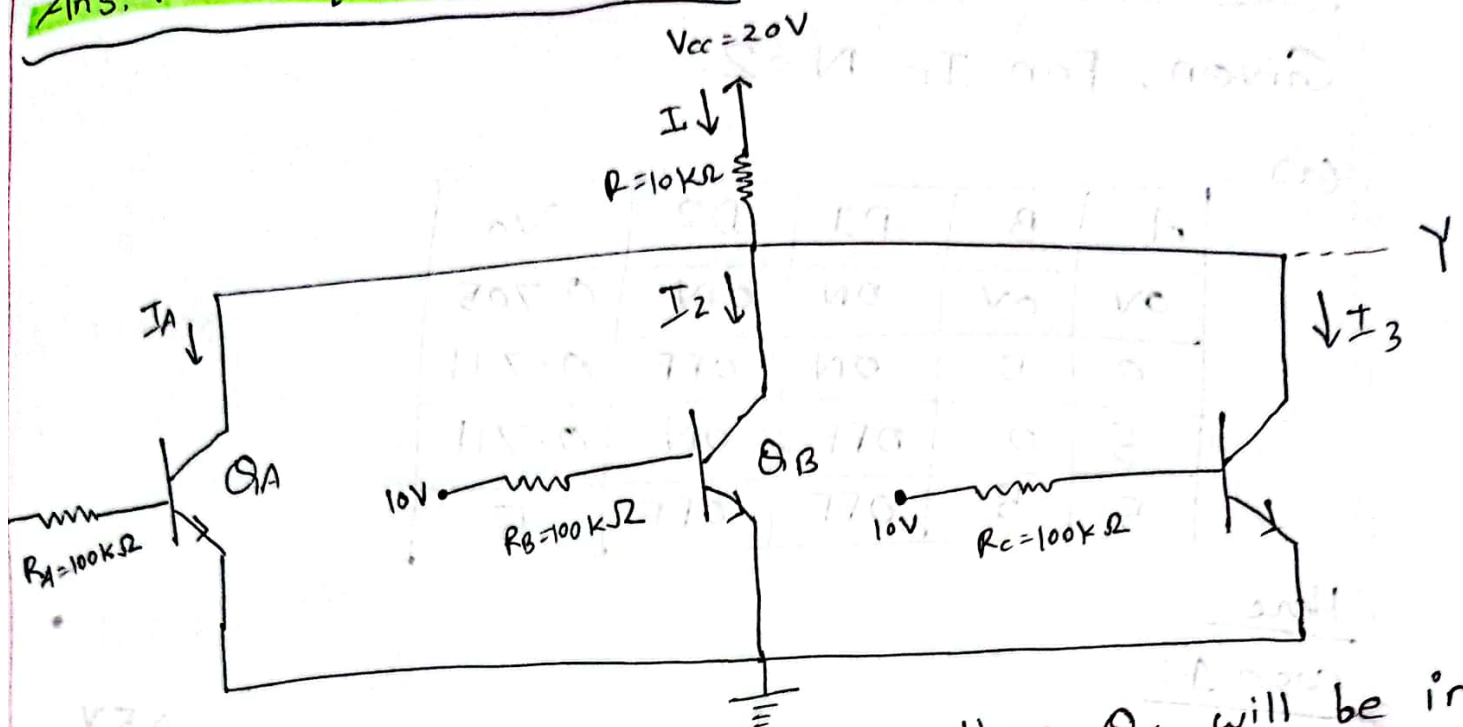
ID: 20101537

Sec: 11

CSE 350
HW2

20101537

Ans. to the question no. 01:



Since input voltage of QA is low then QA will be in cutoff. While on the other hand QB and QC will be in saturation. as of high input voltage. So, $V = 0.2V$

$$\text{So, } I = \frac{20 - 0.2}{10k} = 1.98 \text{ mA}$$

As QA is off then $I_A = 0 \text{ mA}$. Again other branches are identical. Therefore $I_B = I_C$

$$\therefore I_B = I_C = \frac{I}{2} = 0.99 \text{ mA}$$

Therefore,

$$V = 0.2V, I = 1.98 \text{ mA}, I_A = 0, I_B = I_C = 0.99 \text{ mA}.$$

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Ans. to the question no. 02:Given, Fan In $N=2$.

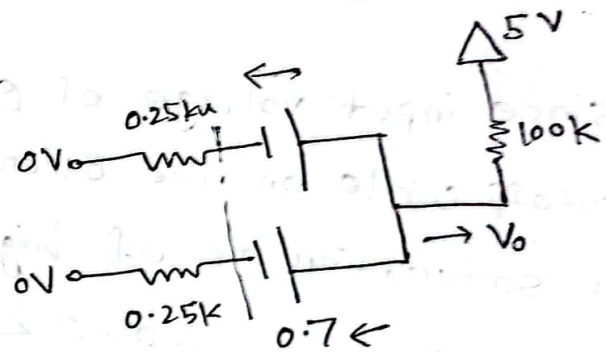
(a)

A	B	D1	D2	V_o
0V	0V	ON	ON	0.705
0	5	ON	OFF	0.711
5	0	OFF	ON	0.711
5	5	OFF	OFF	5

Herecase 1:

$$\frac{V_o - 5}{100k} + \frac{V_o - 0.7 - 0}{0.25k} \times 2 = 0$$

$$\therefore \boxed{V_o = 0.705V}$$



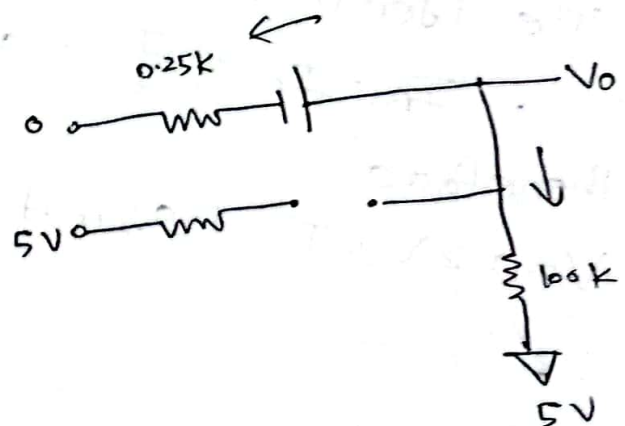
$$0.7 = V_o - V_{th}$$

$$\therefore V_{th} = V_o - 0.7$$

case 2+3:

$$\frac{V_o - 5}{100k} + \frac{V_o - 0.7}{0.25k} = 0$$

$$\therefore \boxed{V_o = 0.711V}$$

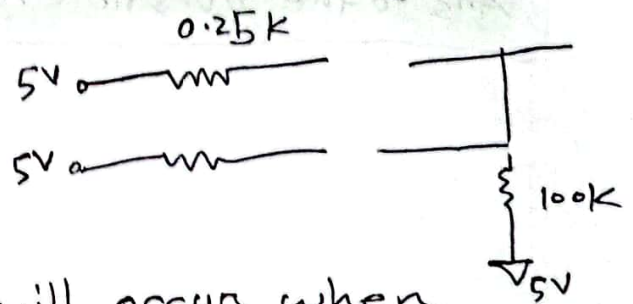


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case 04:

$$\frac{V_o - 5}{100k} = 0$$

$$\therefore V_o = 5V$$



(b) Maximum power dissipation will occur when both of the diodes are on. $V_o = 0.705V$

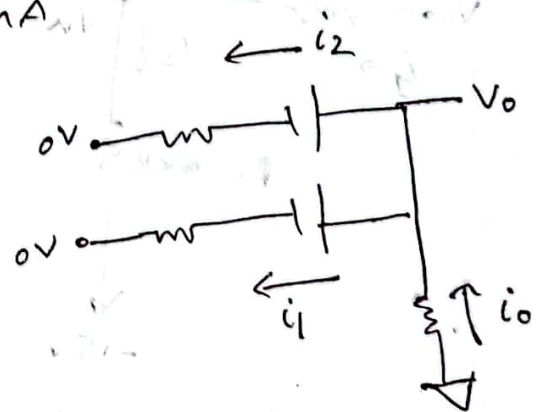
$$\therefore i_o = \frac{5 - 0.705}{100k} = 0.04295 \text{ mA}$$

$$i_1 = i_2 = \frac{i_o}{2} = 0.021475 \text{ mA}$$

$$\therefore P_1 = i_1 \times (5 - 0)$$

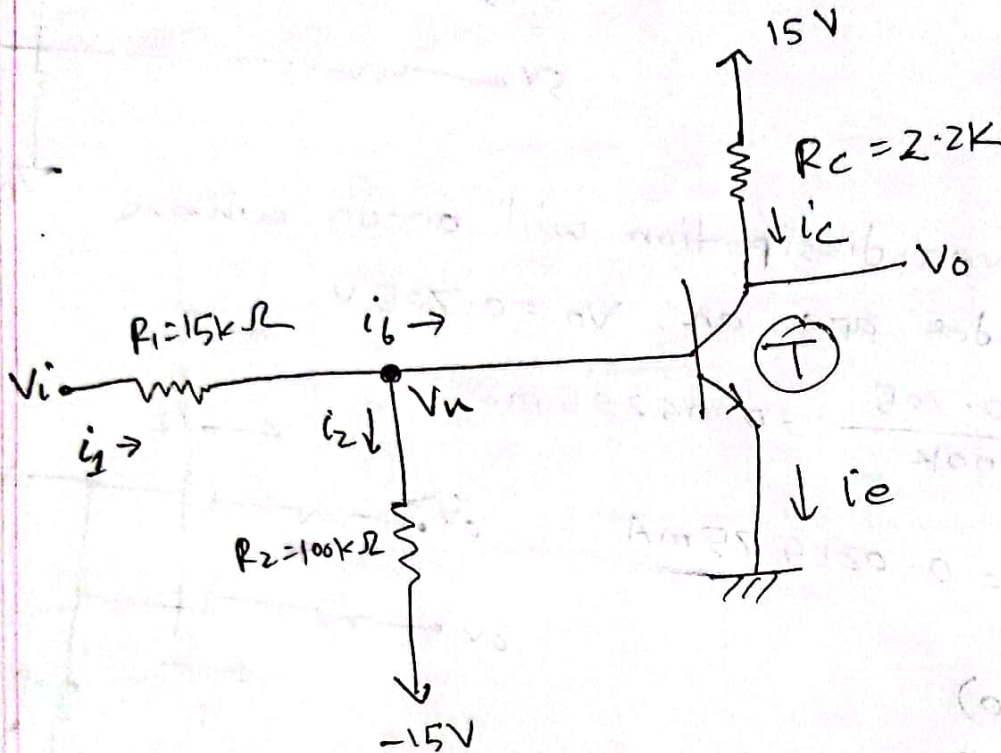
$$P_2 = i_2 \times (5 - 0)$$

$$\begin{aligned} \therefore P &= P_1 + P_2 = 5(i_1 + i_2) \\ &= 5 \times 0.04295 \\ &= 0.21475 \text{ mW} \end{aligned}$$



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Ans. to the question no. 03:



circuit will be in saturation. Therefore,

$$V_{BE} = 0.8V \text{ and } V_{CE} = 0.2V$$

$$\therefore V_B = 0.8V \text{ and } V_C = 0.2V \quad \Bigg| \quad \therefore V_O = 0.2V$$

$$I_C = \frac{15 - 0.2}{2.2K} = 6.73 \text{ mA}$$

$$I_1 = \frac{15 - 0.8}{15K} = 0.95 \text{ mA}$$

$$I_2 = \frac{0.8 - (-15)}{100K} = 0.16 \text{ mA}$$

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KCL at V_u ,

$$I_1 = I_2 + I_B$$

$$\Rightarrow I_B = I_1 - I_2 = (0.95 - 0.16) = 0.79 \text{ mA}$$

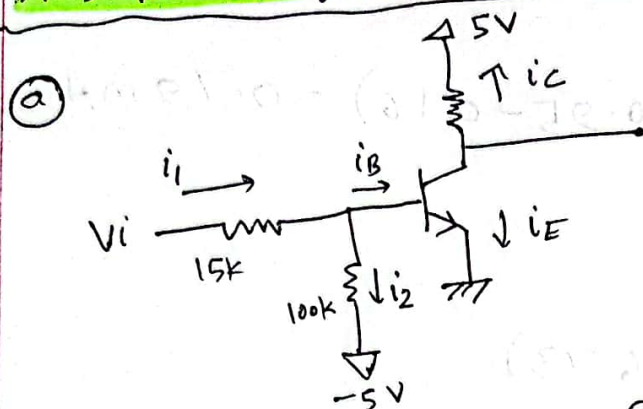
We know,

$$I_E = I_B + I_C$$

$$\therefore I_E = (0.79 + 6.73) \\ = 7.52 \text{ mA}$$

So, $V_o = 0.2 \text{ V}$, $I_1 = 0.95 \text{ mA}$, $I_2 = 0.16 \text{ mA}$,
 $I_C = 6.73 \text{ mA}$; $I_B = 0.79 \text{ mA}$ and $I_E = 7.52 \text{ mA}$.

Ans. to the question no. 04:



Given, $V_{OH} = 4V$, $V_{OL} = 0.2V$, $\beta_F = 30$, cut-in voltage $= 0.5V$

Finding V_{IL} :

Maximum voltage we can apply without turning on the transistor.

$$V_{BE} = V_B = 0.5V$$

$$\therefore i_2 = \frac{V_B - (-5)}{100k}$$

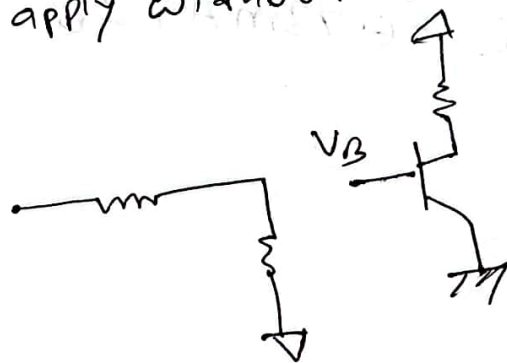
$$= \frac{0.5 + 5}{100k} = 0.055mA$$

Since, $i_1 = i_2$ (here)

$$\therefore \frac{V_i - V_B}{15k} = 0.055$$

$$\therefore V_i = (0.055 \times 15) + 0.5 = 1.33V$$

$$\therefore \boxed{V_{IL} = 1.33V}$$



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Finding V_{IH} : Minimum voltage we can apply so that the transistor remains in saturation.

$$\text{So, } V_{CE} = V_O = 0.2V$$

$$V_{BE} = V_B = 0.8V$$

$$\therefore I_C = \frac{5 - 0.2}{2.2K} = 2.182 \text{ mA}$$

we can write,

$$\frac{I_C}{I_B} = \beta_F$$

$$\therefore I_B = \frac{2.182}{30} = 0.073 \text{ mA}$$

$$\therefore I_2 = \frac{V_B - (-5)}{100K} = 0.058 \text{ mA}$$

KCL at V_B node —

$$I_1 = I_2 + I_B$$

$$\frac{V_i - V_B}{15K} = (0.058 + 0.073)$$

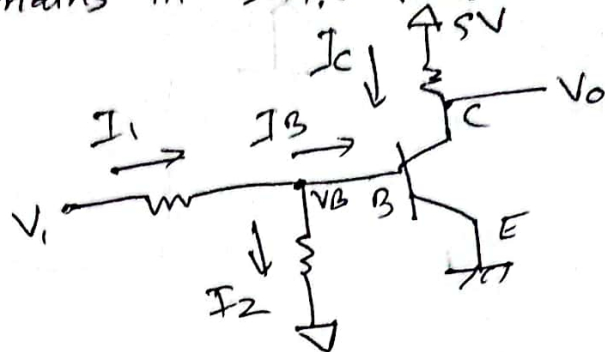
$$V_i = (0.131 \times 15) + 0.8 = 2.765V$$

$$\therefore V_{IH} = 2.765V$$

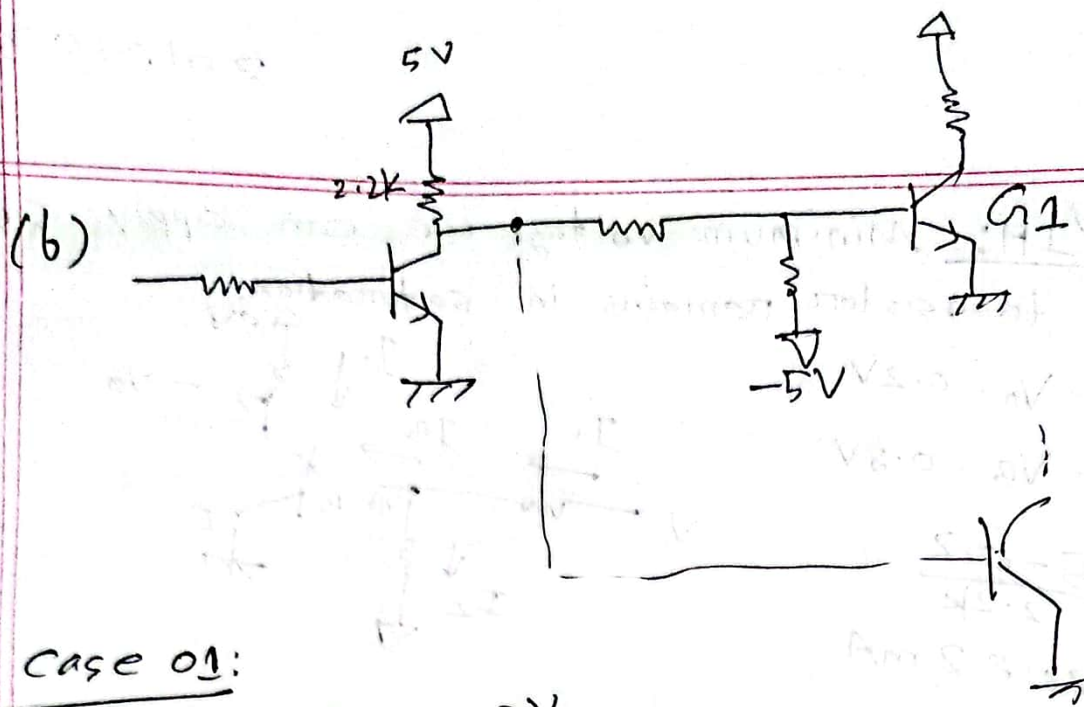
$$\therefore NM_H = V_{OH} - V_{IH} = (4 - 2.765) = 1.235V$$

$$\therefore NM_L = V_{IL} - V_{OL} = (1.33 - 0.2) = 1.13V$$

$$NM = \min \text{ of } (NM_H, NM_L) = 1.13V$$



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Case 01:

Let, $V_o = V_{OL} = 0.2V$

G_1 will be in cutoff

$$\therefore i_1 = i_2 = \frac{V_o - (-5)}{115K} = 0.045 \text{ mA}$$

Here, supply current, $I_s = \frac{5 - 0.2}{2.2K} = 2.182 \text{ mA}$

$$\text{Fanout} = \left\lfloor \frac{2.182}{0.045} \right\rfloor = \left\lfloor 48.48 \right\rfloor = 48$$

Case 02:

Let, $V_o = V_{OH} = 4V$. So, G_1 will be saturation

$$V_{BE} = V_B = 0.8V$$

$$I_1 = \frac{V_o - V_B}{15K} = \frac{4 - 0.8}{15K} = 0.213 \text{ mA}$$

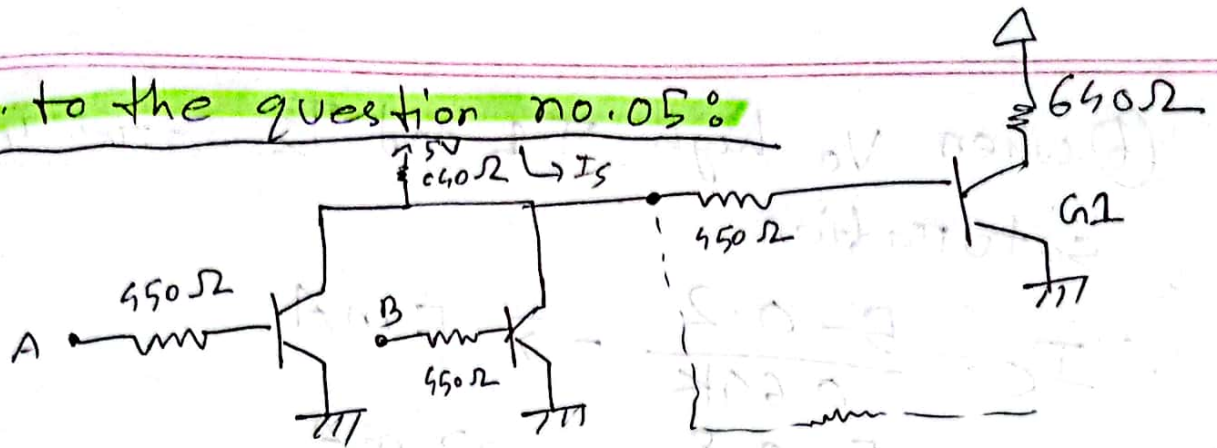
supply current, $I_s = \frac{5 - 4}{2.2K} = 0.455 \text{ mA}$

$$\text{Fanout} = \left\lfloor \frac{0.455}{0.213} \right\rfloor = \left\lfloor 2.136 \right\rfloor = 2$$

\therefore maximum fanout = 2

Ans. to the question no. 05:

(a)



case 01: A and B high $V_O = V_{OL} = 0.2V$

Therefore, G1 will be in cut off means $I_1 = 0$

$$\therefore \text{fan out} = \left\lfloor \frac{I_S}{I_1} \right\rfloor = \left\lfloor \frac{I_S}{0} \right\rfloor = \infty$$

case 02: A and B Low. $V_O = V_{OH} = 2.5V$

Therefore, G1 will be in saturation.

$$I_1 = \frac{2.5 - 0.8}{0.45k} = 3.78mA$$

$$I_S = \frac{5 - 2.5}{0.64} = 3.91mA$$

$$\therefore \text{fan out} = \left\lfloor \frac{3.91}{3.78} \right\rfloor = 1$$

$$\text{maximum fanout} = \min(\infty, 1) = 1.$$

⑥ when, $V_o = \text{high}$ T_1 and T_2 are in saturation.

$$\therefore I_C = \frac{5 - 0.2}{0.64K} = 7.5 \text{ mA}$$

$$I_B = \frac{5 - 0.8}{0.45K} = 9.33 \text{ mA}$$

$$\therefore \frac{I_C}{I_B} = \frac{7.5}{9.33} = 0.804 < \beta_{\text{forced}}$$

$$\therefore \beta_{\text{min}} = 0.804$$

(Ans)