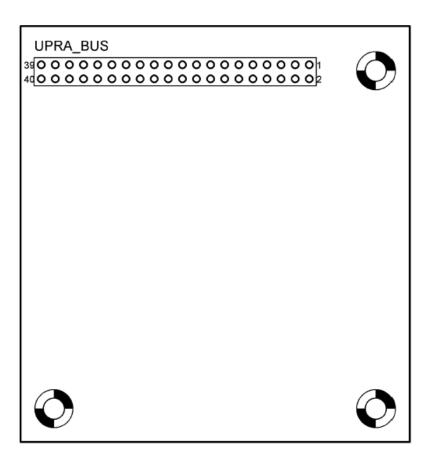
UPRA BUS - 40pin - DIP

RESERVED	RESERVED	EPS_DIAG	DAU_DIAG	COM_DIAG	EPS:GPIO4	EPS:GPIO3	EPS:GPI02	EPS:GPI01	DAU:GPI04	DAU:GPI03	DAU:GPI02	DAU:GPI01	COM:GPIO5	COM:GPIO4	COM:GPIO3	COM:GPI02	COM:GPIO1	GPS_TX	GPS_RX
39	37	35	33	31	29	27	25	23	21	19	17	15	13	11	9	7	5	3	1
40	38	36	34	32	30	28	26	24	22	20	18	16	14	12	10	8	6	4	2
RESERVED	RESERVED	PAYLOAD3+3V3	PAYLOAD2+3V3	PAYLOAD1+3V3	DAU+3V3	COM+3V3	GND	OBC:+3V3	OBC:SWO	OBC:NRST	OBC:SWDIO	GND	OBC:SWCLK	OBC:+3V3	BUS_BUSY	CANL	CANH	OBC:UART3_RX	OBC:UART3_TX

	Alternative Functions							
PIN	Main Function	Alternative/Recommended Function						
2	OBC:UART3_TX	SUB MODULE: TX	Dealuse DLIC ODC controlled					
4	OBC:UART3_RX	SUB MODULE: RX	Backup BUS, OBC controlled					
5	COM:GPIO1	COM:TMS/COM_SDWIO						
7	COM:GPIO2	COM:TCK/SWCLK	7					
9	COM:GPIO3	COM:TDO/SWO	COM program/debug port					
11	COM:GPIO4	COM:TDI						
13	COM:GPIO5	COM:RESET						
15	DAU:GPIO1	DAU:SDWIO						
17	DAU:GPIO2	DAU:SWCLK	DALL program (dahug part					
19	DAU:GPIO3	DAU:SWO	DAU program/debug port					
21	DAU:GPIO4	DAU:NRST	1					
23	EPS:GPIO1	EPS:SDWIO						
25	EPS:GPIO2	EPS:SWCLK	FDC managed /dah.com					
27	EPS:GPIO3	EPS:SWO	EPS program/debug port					
29	EPS:GPIO4	EPS:NRST						



MATeF-2 PINOUT

PIN	OBC	сом	APRS	EPS-GPS		
1	GPS_RX	NC	GPS_RX	GPS_RX		
2	SICL_TX	SICL_RX	NC	NC		
3	GPS_TX	NC	NC	GPS_TX		
4	SICL_RX	SICL_TX	NC	NC		
5	NC	NC	NC	NC		
6	NC	NC	NC	NC		
7	NC	NC	NC	NC		
8	NC	NC	NC	NC		
9	NC	NC	NC	NC		
10	BUS_BUSY	BUS_BUSY	NC	NC		
11	NC	NC	NC	NC		
12	Vcc	NC	NC	3V3		
13	NC	NC	NC	NC		
14	NC	NC	NC	NC		
15	NC	NC	NC	NC		
16	GND	GND	GND	GND		
17	NC	NC	NC	NC		
18	NC	NC	NC	NC		
19	NC	NC	NC	NC		
20	NC	NC	NC	NC		
21	NC	NC	NC	NC		
22	NC	NC	NC	NC		
23	NC	NC	NC	NC		
24	Vcc	NC	NC	3V3		
25	NC	NC	NC	NC		
26	GND	GND	GND	GND		
27	NC	NC	NC	NC		
28	NC	Vcc	NC	3V3		
29	NC	NC	NC	NC		
30	NC	NC	NC	3V3		
31	NC	NC	NC	NC		
32	NC	NC	NC	NC		
33	NC	NC	NC	NC		
34	NC	NC	NC	NC		
35	NC	NC	NC	NC		
36	NC	NC	NC	NC		
37	NC	NC	NC	NC		
38	NC	NC	NC	NC		
39	NC	NC	NC	NC		
40	NC	NC	NC	NC		