



EASTPROFIT LCM **PRODUCT SPECIFICATIONS**

ENGINEERING NO: CT028QVT026

For Customer: _____

Approved by: _____

Signature: _____

Date: _____

Please sign the cover page of the spec for your approval and return it to our local sales **within a month** after your receipt of the spec from EASTPROFIT. In the case EASTPROFIT does not receive the signed spec even after one month later, in general we will consider that the spec was already accepted by your company.

Prepared	Checked	Approved	Date

History of Versions and Modifications

Version	Modifications	Date
1	Generation first version	2011-03-29
2	Modify the name of production. Modify block diagram.	2011-04-10



PRODUCT SPECIFICATIONS

- ◆ LCD MODULE PHYSICAL DATA
- ◆ EXTERNAL DIMENSIONS
- ◆ BLOCK DIAGRAM
- ◆ ABSOLUTE MAXIMUM RATINGS
- ◆ ELECTRICAL CHARACTERISTICS
- ◆ ELECTRO-OPTICAL CHARACTERISTICS
- ◆ INTERFACE PIN CONNECTIONS
- ◆ RELIABILITY
- ◆ SPECIFICATION OF QUALITY ASSURANCE
- ◆ SUGGESTIONS FOR USING LCD MODULES
- ◆ PACKING

LCD MODULE PHYSICAL DATA

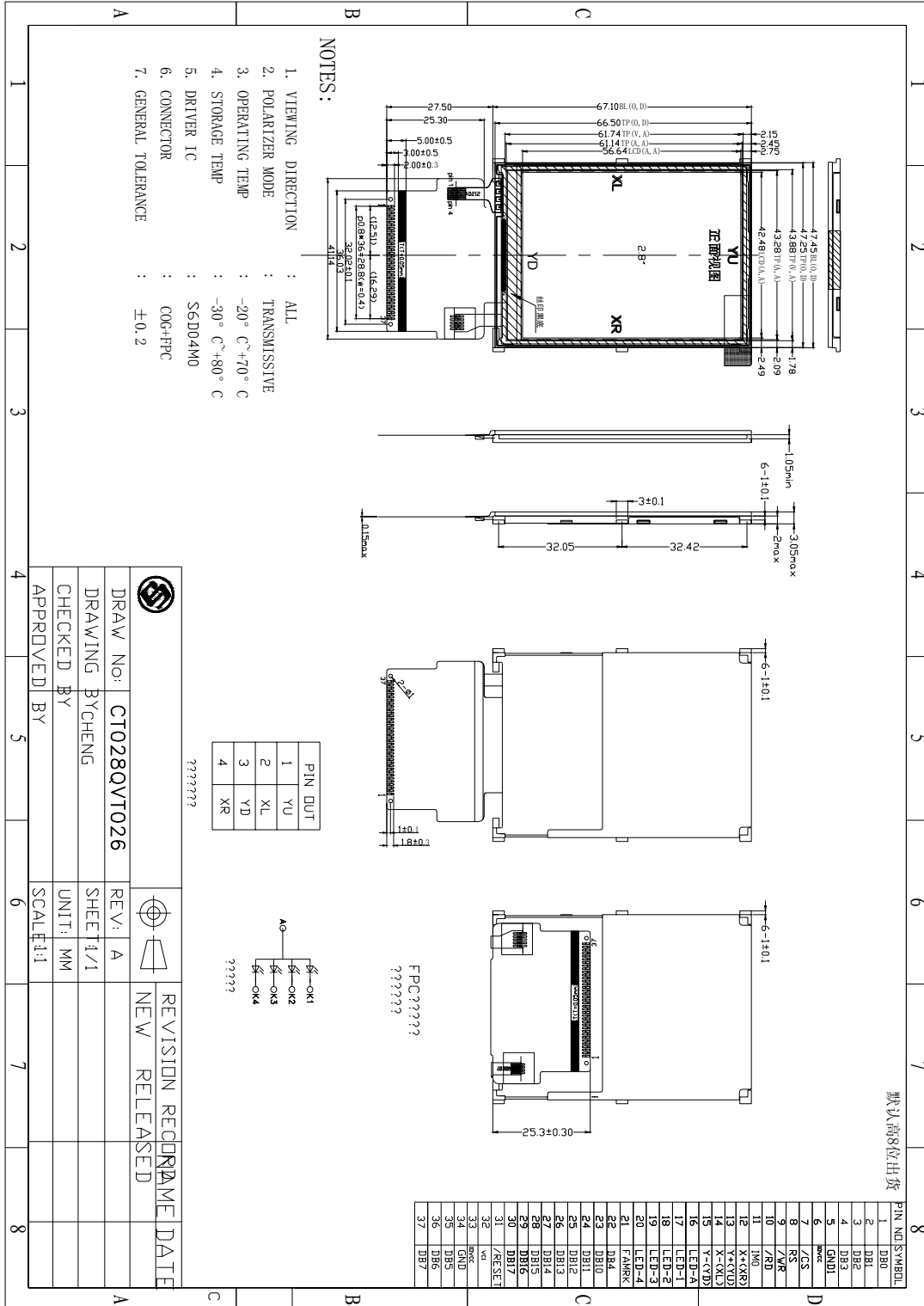
General Description

Item	Standard Value	Unit
LCD Type	TRANSMISSIVE	---
Number of Dots	240RGBX320	---
Viewing Direction	ALL	o'clock
LCM Dimension	67.10 (W) X47.45 (H) X 3.05(T) _(With TP)	mm
Viewing area	/	mm
Active area	42.48 (W) X56.64 (H)	mm
Operating temperature	-20~70	℃
Storage temperature	-30~80	℃
Driving IC	S6D04M0X21	---
Approx. weight	TBD	g

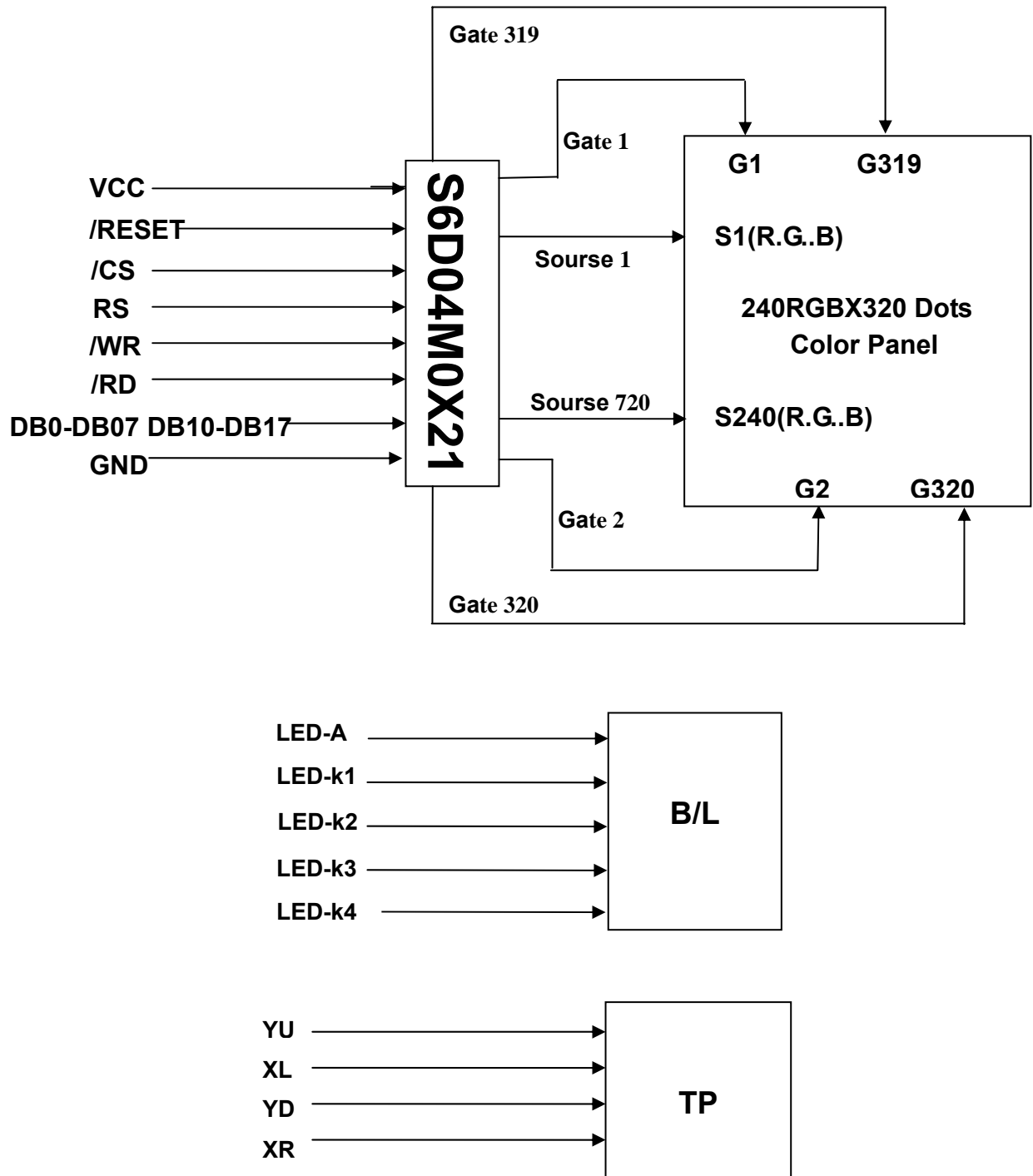
The backlight electrical-optical characteristics

Item	Symbol	Min	Typ	Max	Unit	Unit
Forward voltage	V _f	3.0	3.2	3.4	V	I _f =60mA/Ta=25℃
Uniformity	△B _p	80			%	
Luminance for LCD	L _v	3600	3800	4200	Cd/m ²	I _f =60mA/Ta=25℃

EXTERNAL DIMENSIONS



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Rating	Unit
Operating temperature	Top	-20-70	℃
Storage temperature	Tst	-30-80	℃
Input voltage	Vin	-0.3-4.6	V
Supply voltage for logic	VCC	-0.3-4.6	V
Driver supply voltage	VGH - VGL	11.0 - 30	V

NOTE:

1. If the module is used above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. $VCC > GND$ must be maintained.

ELECTRICAL CHARACTERISTICS

DC Characteristics

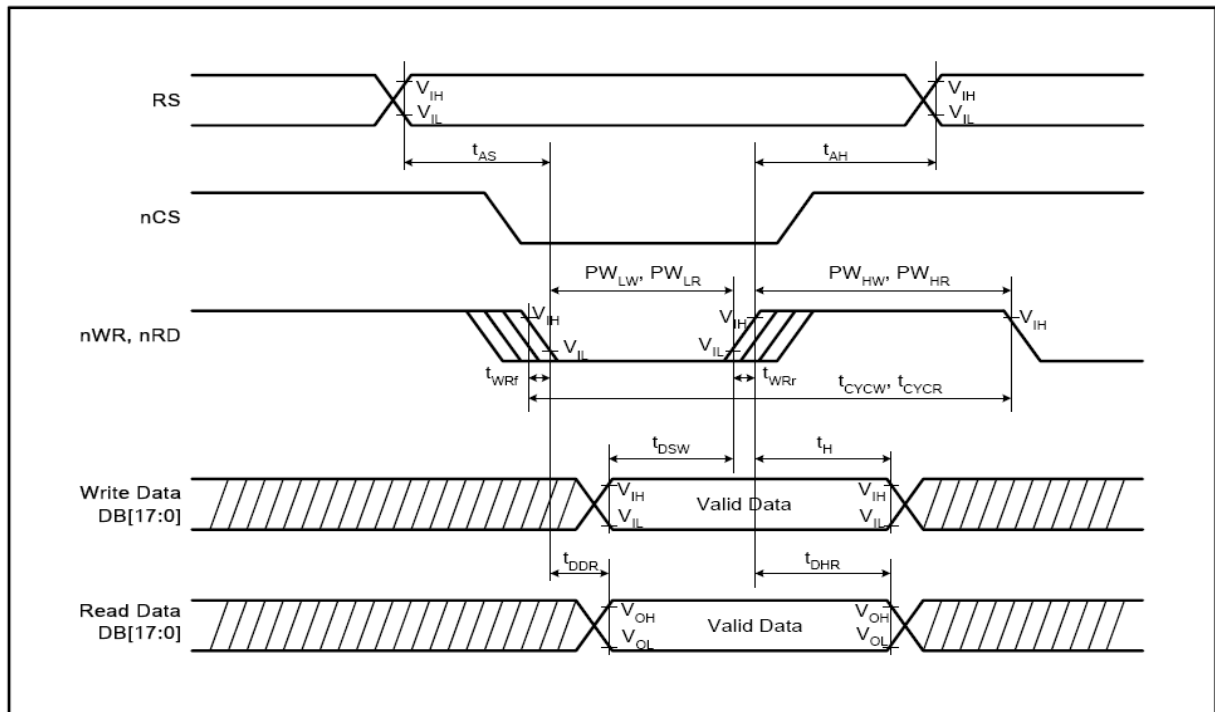
Item	Symbol	Condition	Min	Typ	Max	Unit
Input high voltage	V _{IH}	-	0.8 VCC	-	VCC	V
Input low voltage	V _{IL}	-	0	-	0.2VCC	V
Supply voltage for logic	VCC	Ta=25℃	2.4	3.0	3.3	V
Output high voltage	V _{OH}		0.8 VCC	-	VCC	V
Output low voltage	V _{OL}		0		0.2VCC	V
Current consumption for LCD normal operation	I _{cc}	V _{cc} = TBD	-	TBD	TBD	mA

AC Characteristics

i80-System Interface Timing Characteristic

Normal Write Mode (IOVCC = 1.65~3.3V, VCC=2.4~3.3V)

Item	Symbol	Unit	Min.	Typ.	Max.	Test Condition
Bus cycle time	Write	t_{CYCW}	ns	100	-	-
	Read	t_{CYCR}	ns	300	-	-
Write low-level pulse width	PW_{LW}	ns	50	-	500	-
Write high-level pulse width	PW_{HW}	ns	50	-	-	-
Read low-level pulse width	PW_{LR}	ns	150	-	-	-
Read high-level pulse width	PW_{HR}	ns	150	-	-	-
Write / Read rise / fall time	t_{WRr}/t_{WRf}	ns	-	-	25	-
Setup time	Write (RS to nCS, E/nWR)	t_{AS}	ns	10	-	-
	Read (RS to nCS, RW/nRD)		ns	5	-	-
Address hold time	t_{AH}	ns	5	-	-	-
Write data set up time	t_{DSW}	ns	10	-	-	-
Write data hold time	t_H	ns	15	-	-	-
Read data delay time	t_{DDR}	ns	-	-	100	-
Read data hold time	t_{DHR}	ns	5	-	-	-

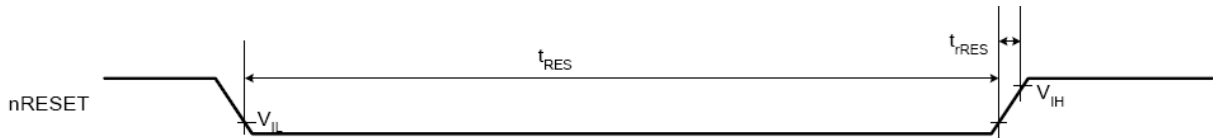


i80-System Bus Timing

Reset Timing Characteristics

Reset Timing Characteristics (VCC = 1.8 ~ 3.3 V, IOVCC = 1.65 ~ 3.3 V)

Item	Symbol	Unit	Min.	Typ.	Max.
Reset low-level width	t_{RES}	ms	1	-	-
Reset rise time	t_{rRES}	μ s	-	-	10



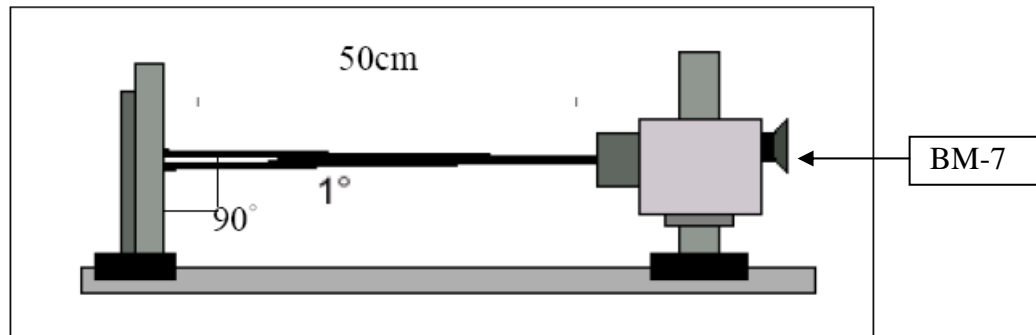
ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Response time	Tr+Tf	$\Theta=0^\circ$; $\Phi=0^\circ$;		TBD		ms	Note4
Contrast ratio	Cr			450		--	Note5
Luminance uniformity	δ WHITE			80		%	Note7
Surface luminance	Lv		156.0	183.9	200.6	cd/m ²	Note6
View angle range (with polarizer)	Top	CR \geq 10		TBD		Degree	Note9
	Bottom			TBD			
	Left			TBD			
	Right			TBD			
CIE (X, Y)	Rx	$\Theta=0^\circ$	0.599	0.619	0.639	--	Note8
	Ry		0.308	0.328	0.348		
	Gx		0.282	0.302	0.322		
	Gy		0.566	0.586	0.606		
	Bx		0.115	0.135	0.155		
	By		0.096	0.116	0.136		
	Wx		0.285	0.305	0.325		
	Wy		0.312	0.332	0.352		

Note 1: Ambient temperature = 25 \pm 2 $^\circ$ C;

Note 2: To be measured in the dark room;

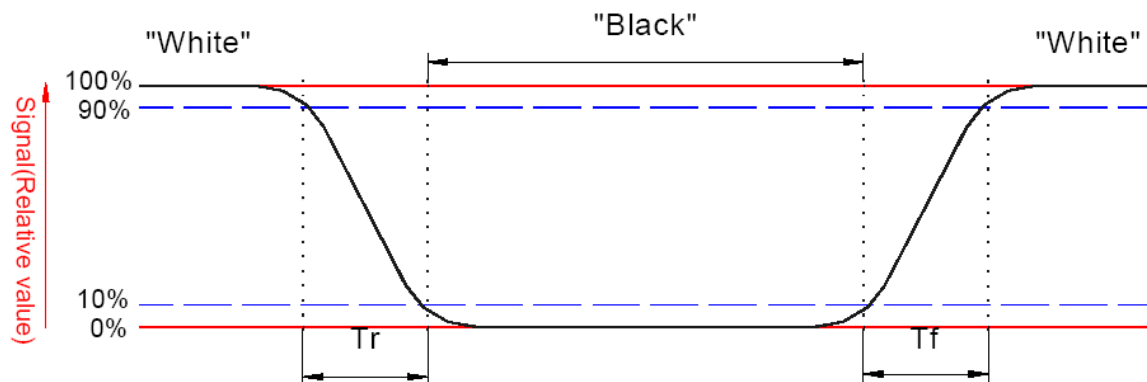
Note 3: To be measured at the center area of the panel with a view cone of 1° by BM-7, after 10 minutes operation (module).



FLG1

Note 4: Define the response time:

The output signals of photo detector are measured when the input signals are charged from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



FLG2

Note 5: Contrast Ratio (CR) is defined mathematically as For more information from FLG3

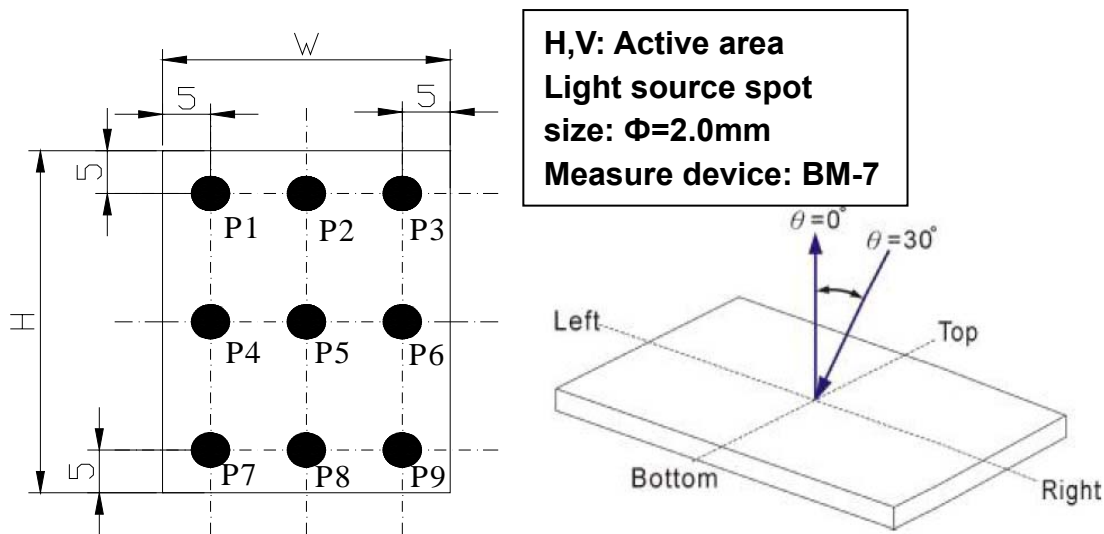
$$\text{Contrast Ratio} = \frac{\text{Average surface luminance with all white pixel (P1,P2,P3,P4,P5,P6,P7,P8,P9)}}{\text{Average surface luminance with all black pixel (P1,P2,P3,P4,P5,P6,P7,P8,P9)}}$$

Note 6: Surface luminance is the center point across the LCD surface 500mm from the surface with all pixel displaying white, For more information see the FLG3
 L_v = Average Surface luminance with all white pixel(P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 7: The uniformity in surface luminance, δ white is determined by measuring luminance at each test position 1 to 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FLG3.

δ WHITE = $\frac{\text{Minimum surface luminance with all white pixel(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}{\text{Maximum surface luminance with all white pixel(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}$

Note 8: CIE(X, Y), the X, Y value is determined by measuring luminance at each test position 1 to 5, and then make average value. For more information see FLG3.



FLG3

FLG4

Note 9: Viewing angle is the angle at which the contrast ratio is greater than 2, TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see the FLG 4.

INTERFACE PIN CONNECTIONS

NO.	Symbol	Function
1	DB0	Graphic Display Data
2	DB1	
3	DB2	
4	DB3	
5	GND1	Ground
6	VCC1	Power supply for I/O circuit
7	/CS	Chip select signal
8	RS	Command or data select signal
9	/WR	Write signal
10	/RD	Read signal
11	IM0	IM0=0,16bit IO;IM0=1,8bit IO
12	XR	TP CONNECT PIN
13	YD	TP CONNECT PIN
14	XL	TP CONNECT PIN
15	YU	TP CONNECT PIN
16	LEDA	The anode of the backlight LED
17	LEDK1	The cathode of the backlight LED
18	LEDK2	The cathode of the backlight LED
19	LEDK3	The cathode of the backlight LED
20	LEDK4	The cathode of the backlight LED
21	FMARK	FMARK signal output
22	DB4	Graphic Display Data
23	DB10	
24	DB11	
25	DB12	
26	DB13	
27	DB14	
28	DB15	
29	DB16	
30	DB17	
31	/RESET	Hardware reset pin
32	VCI	Power supply for analog circuit
33	VCC2	
34	GND	Ground
35	DB5	Graphic Display Data

36	DB6	Graphic Display Data
37	DB7	

RELIABILITY

NO..	Test Item	Description	Test Condition
1	High temperature storage	Endurance test applying the high storage temperature for a long time	80℃,240 H
2	Low temperature storage	Endurance test applying the low storage temperature for a long time	-30℃,240H
3	High temperature operation	Endurance test applying the electric stress under high temperature for a long time	70℃,96H
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time	-20℃,96H
5	High temperature /humidity storage	Endurance test applying the high temperature and high humidity storage for a long time	50℃, 90% R.H 240H
6	High temperature /humidity operation	Endurance test applying electric stress under high temperature and high humidity for a long time	40℃ 90% R.H 96H
7	Temperature Cycle	Endurance test applying the low and high temperature cycle $\begin{array}{ccccccc} -20^{\circ}\text{C} & \rightarrow & 25^{\circ}\text{C} & \rightarrow & 70^{\circ}\text{C} & \rightarrow & 25^{\circ}\text{C} \\ \text{30min} & & \text{5min} & & \text{30min} & & \text{5min} \end{array}$ <div style="text-align: center;"> \longleftrightarrow one cycle </div>	-20℃/70℃ 10 cycles
8	Vibration test	Endurance test applying the vibration during transportation and using	10Hz~50Hz Swing:0.75mm time:30min
9	Fall test	Endurance test dropping the LCM from a high place	600mm height

10	Static electricity test	Endurance test applying static electric stress to terminal	Contact discharge: ±2KV
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SPECIFICATION OF QUALITY ASSURANCE

Summary

The customer should check and accept the products of EASTPROFIT within one month after reception. This standard for Quality Assurance should affirm the quality of LCD products to supply to purchaser by EASTPROFIT. Entire process is controlled according to QS9000.

Standard for quality test

- 1、 Inspection
Before delivering, the supplier should take the following tests, and affirm the quality of product.
- 2、 Electro-Optical Characteristics
According to the individual specification to test the product.
- 3、 Test of Appearance Characteristics:
According to the individual specification to test the product.
- 4、 Test of Reliability Characteristics
According to the definition of reliability on specification for test product.
- 5、 Delivery Test
Before delivering, the supplier should take the delivery test
- 6、 Sampling Method: GB/T2828.1-2003, Level II
- 7、 The defects classify of AQL as following
Major defect: AQL=0.65
Minor defect: AQL=1.5

Nonconforming Analysis & Deal With Manners

✧ Nonconforming Analysis

- 1、 Purchaser should supply the detail data of nonconforming sample and the non-suitable state.

2、 After accepting the detail data from purchaser ,the analysis of nonconforming should be finished in two weeks.

3、 If supplier can not finish analysis on time ,must announce purchaser before two weeks.

✧ Disposition of nonconforming

1、 If find any supplier defect during assembly line, supplier must change the good product for every defect after recognition.

2、 Both supplier and customer should analysis the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

Agreement items.

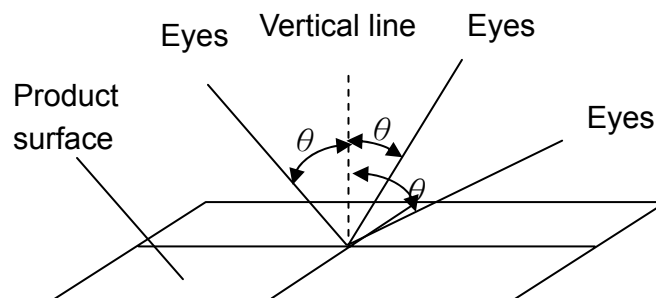
Both sides should discuss together when the following problems happen:

- 1、 There is any problem of standard of quality assurance ,and both sides think that must be modifier.
- 2、 There is any argument item which does not record in the quality assurance.
- 3、 Any other special problem.

Standard of the Product Appearance Test

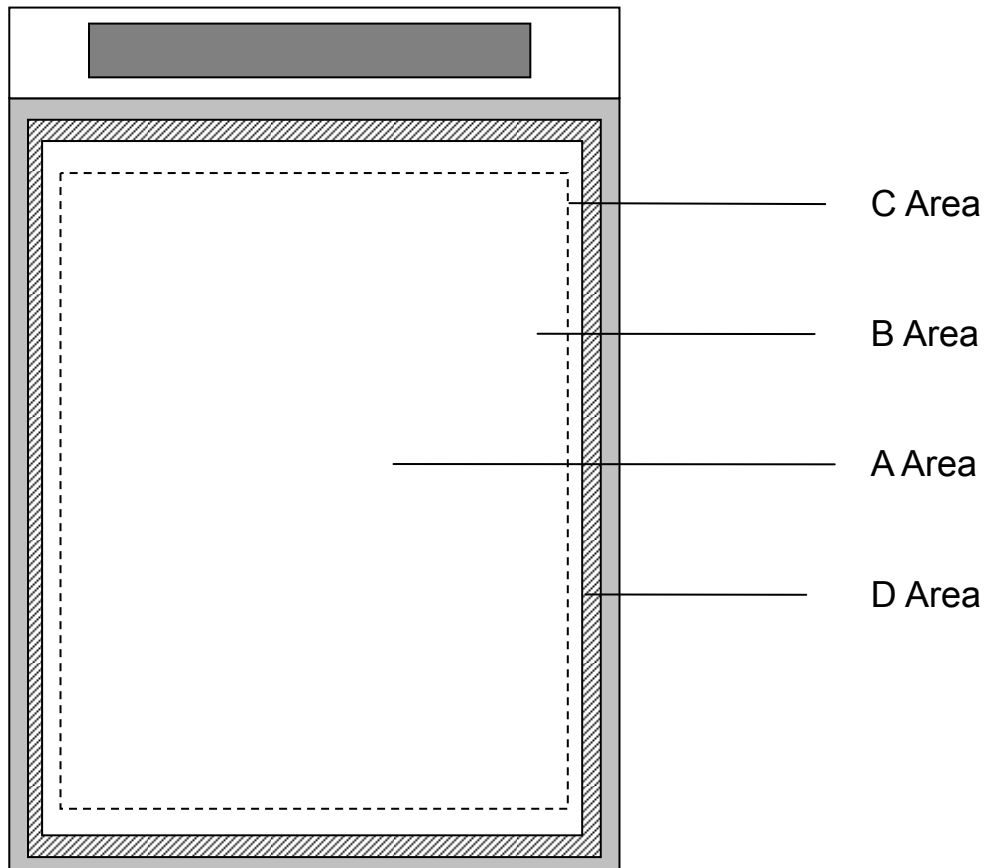
Manner of appearance test

- 1、 The test must be under 20W*2 or 40W fluorescent light ,and the distance of view must be at 30 ± 5 cm.
- 2、 When test the model of Transmissive product must add the reflective plate.
- 3、 The test direction is base on about around 30 degree(within θ range)of vertical line.



4、Definition of Area:

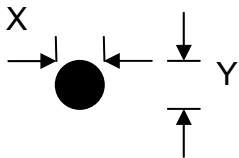
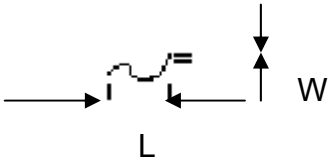
- A Area: Active area
- B Area: Viewing area
- C Area: Out of viewing area
- D Area: Seal area

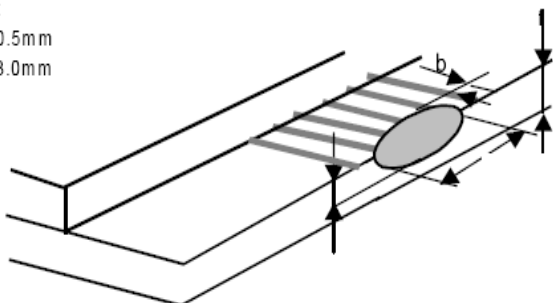


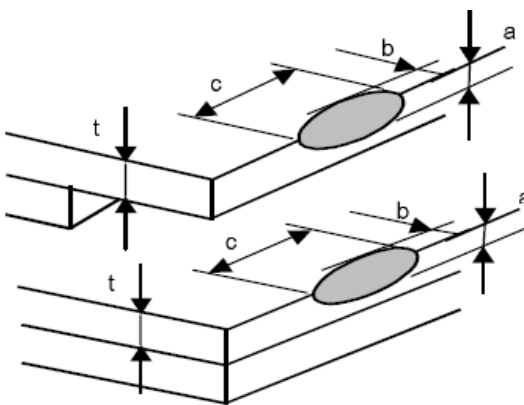
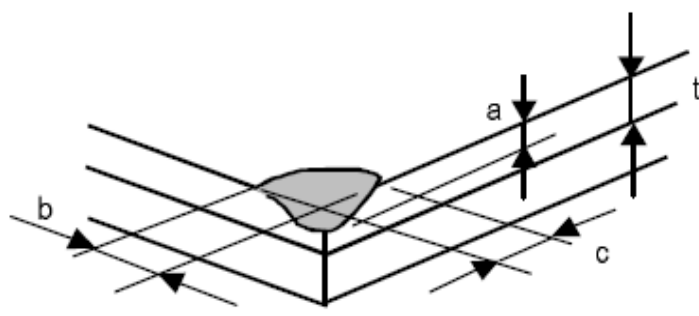
Basic principle:

- 1、 It will accord to the AQL when the standard can not be described.
- 2、 The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- 3、 Must add new item on time when it is necessary.

Inspection specification

NO	Item	Criterion	AQL																						
01	Electrical Testing	<p>1.1 Missing vertical, horizontal segment, segment contrast defect.</p> <p>1.2 Missing character, dot or icon.</p> <p>1.3 Display malfunction.</p> <p>1.4 No function or no display.</p> <p>1.5 Current consumption exceeds product specifications.</p> <p>1.6 LCD viewing angle defect.</p> <p>1.7 Contrast defect</p>	0.65																						
02	LCD black spots, white spots, color spots, contamination, scratches (display/non-display)	<p>2.1 Round type: As following drawing</p> $\varphi = (x+y)/2$  <table border="1"> <thead> <tr> <th rowspan="2">Size</th><th colspan="2">Acceptable QTY</th><th rowspan="2">Remark</th></tr> <tr> <th>A.A</th><th>V.A</th></tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.20$</td><td>Ignore</td><td>Ignore</td><td rowspan="5">No more than two spots within 5mm</td></tr> <tr> <td>$0.20 < \varphi \leq 0.25$</td><td>2</td><td>3</td></tr> <tr> <td>$0.25 \leq \varphi \leq 0.30$</td><td>1</td><td>2</td></tr> <tr> <td>$0.30 < \varphi$</td><td>0</td><td>0</td></tr> <tr> <td>Total</td><td>3</td><td>5</td></tr> </tbody> </table> <p>2.2 Line Type: (As following drawing)</p> 	Size	Acceptable QTY		Remark	A.A	V.A	$\varphi \leq 0.20$	Ignore	Ignore	No more than two spots within 5mm	$0.20 < \varphi \leq 0.25$	2	3	$0.25 \leq \varphi \leq 0.30$	1	2	$0.30 < \varphi$	0	0	Total	3	5	1.5
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Total	3	5																							

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---	$0.08 < W$	0	0																											
03	Polarizer bubbles	<p>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.</p> <table> <tr> <th rowspan="2">Size</th> <th colspan="2">Acceptable QTY</th> </tr> <tr> <th>A.A</th> <th>V.A</th> </tr> <tr> <td>$\phi \leq 0.30$</td> <td>Ignore</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \phi \leq 0.60$</td> <td>2</td> <td>3</td> </tr> <tr> <td></td> <td>0</td> <td>0</td> </tr> </table>	Size	Acceptable QTY		A.A	V.A	$\phi \leq 0.30$	Ignore	Ignore	$0.30 < \phi \leq 0.60$	2	3		0	0	1.5													
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04	<p>Symbols:</p> <p>a: Chip length b: Chip width c: Chip thickness</p> <p>t: Glass thickness</p> <p>4.1 ITO electrode</p> <p>a<=t b<=0.5mm c<=3.0mm</p> 																													

	Chipped glass	<p>4.2 General ,corner portion</p> <p> $a \leq t$ $b \leq 1.0\text{mm}$ $c \leq 5.0\text{mm}$ </p>  <p>*Effective width of seal area shall be more than 0.3mm.</p> 	1.5
05	Cracked glass	The LCD with extensive crack is not acceptable.	0.65
06	Backlight elements	6.1 Illumination source flickers when lit.	0.65
		6.2 Spots or scratches that appear when lit must be judged using LCD spot, lines and contamination standards.	1.5
		6.3 Backlight doesn't light or color is wrong	0.65
07	Soldering	7.1 No unmelted solder paste may be present on the PCB.	1.5
		7.2 No cold solder joints, missing solder connections, oxidation or icicle.	1.5
		7.3 No residue or solder balls on PCB.	1.5
		7.4 No short circuits in components on PCB.	0.65

08	General appearance	8.1 No oxidation, contamination, curves or, bends on interface pin (OLB) of TCP.	1.5
		8.2 No cracks on interface pin(OLB) of TCP	0
		8.3 NO contamination, solder residue or solder balls on product.	.
		8.4 The IC on the TCP may not be damaged, circuits.	5 1.5
		8.5 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	0.65 1.5
		8.6 Sealant on top of the ITO circuit has not hardened	
		8.7 Pin type must match type in specification sheet.	1.5
		8.8 LCD pin loose or missing pins.	0.65
		8.9 Product packaging must the same as specified on packaging specification sheet.	0.65 0.65
		8.10 Product dimension and structure must conform to product specification sheet.	0.65

SUGGESTIONS FOR USING LCD MODULES

Handling of LCM

- (1) The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
- (2) If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
- (3) Don't apply excessive force on the surface of the LCM.
- (4) If the surface is contaminated ,clean it with soft cloth. If the LCM is severely contaminated , use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer . The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
- (5) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (6) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (7) Don't disassemble the LCM.
- (8) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded.

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- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- (9) Do not alter, modify or change the the shape of the tab on the metal frame.
- (10) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (11) Do not damage or modify the pattern writing on the printed circuit board.
- (12) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
- (13) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- (14) Do not drop, bend or twist LCM.

Storage

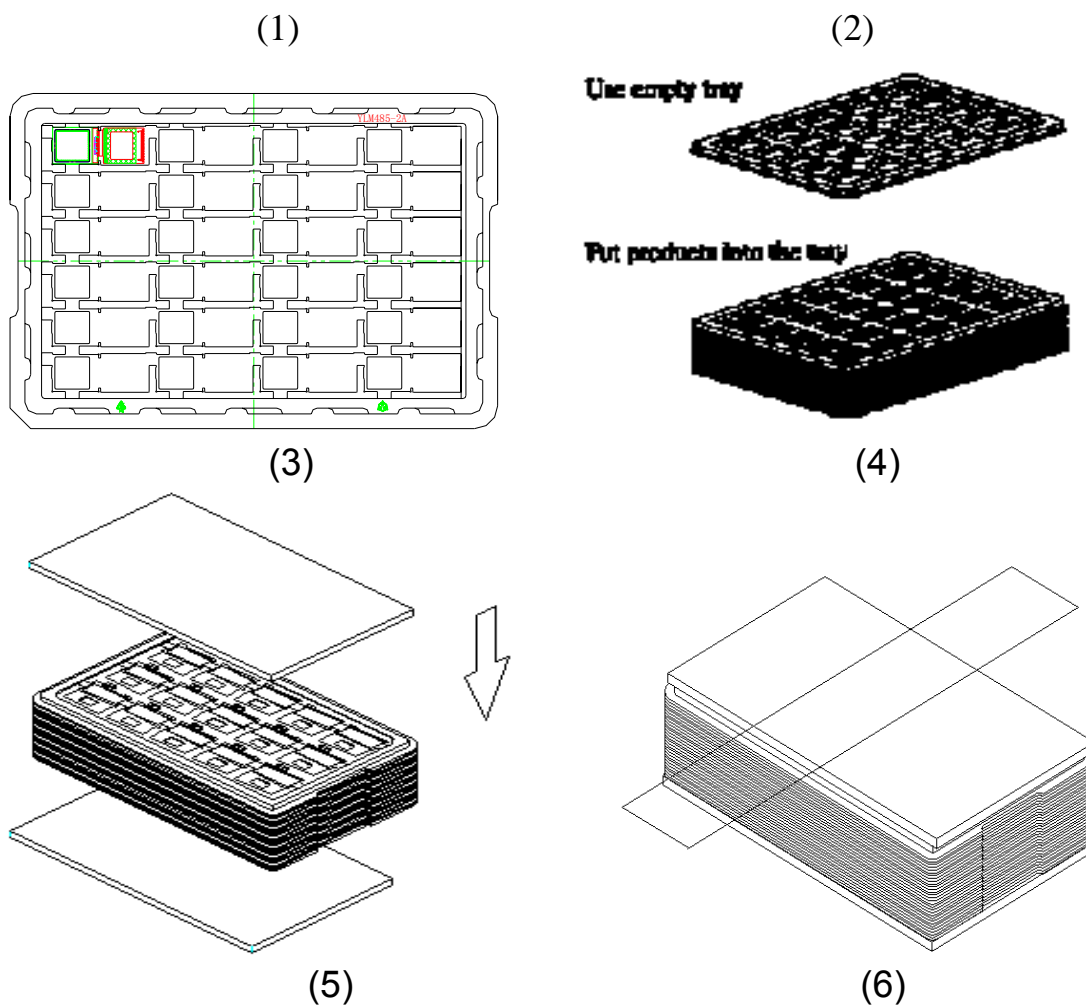
- (1) Store in an ambient temperature of 5 to 45 °C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- (2) Storage in a clean environment, free from dust, active gas, and solvent.
- (3) Store in antistatic container.

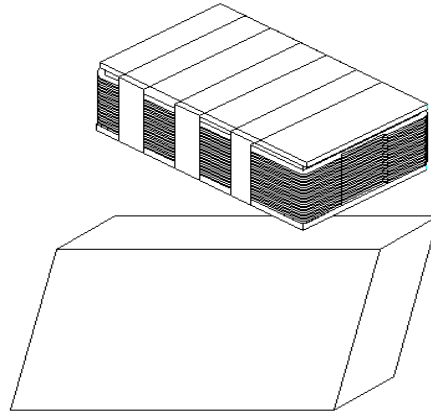
PACKING

Packing Materials

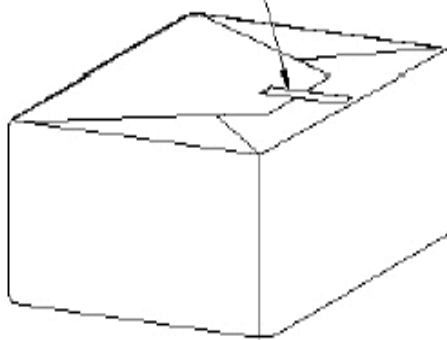
NO.	ITEM	Dimension(LXWXH) (mm)	Quantity
1	Tray	TBD	TBD
2	Carton	TBD	TBD

Packing Method



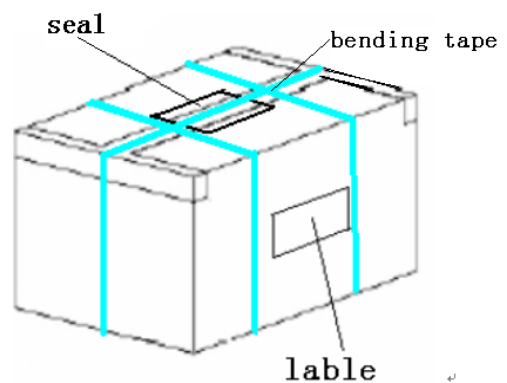
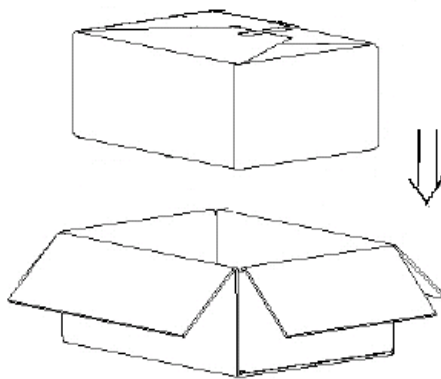


Fix by adhesion tape



(7)

(8)



- (1). Put module into tray cavity :
- (2). Tray stacking
- (3). Put 1 cardboard under the tray stack and 1 cardboard above:
- (4). Fix the cardboard to the tray stack with adhesive tape:
- (5). Put the tray stack and 4 pcs desiccant into the LDPE bag



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- (6). Fix the LDPE bag with adhesive tape
- (7). Put LDPE bag with tray stack into carton.:
- (8). Carton sealing with adhesive tape.