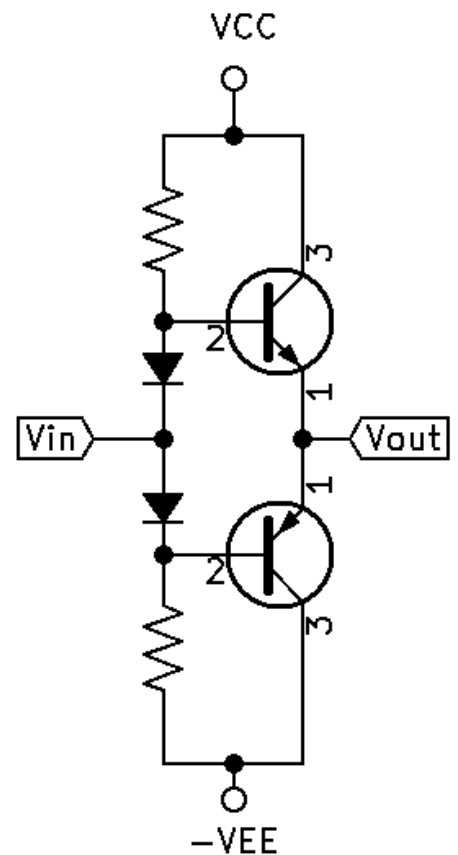
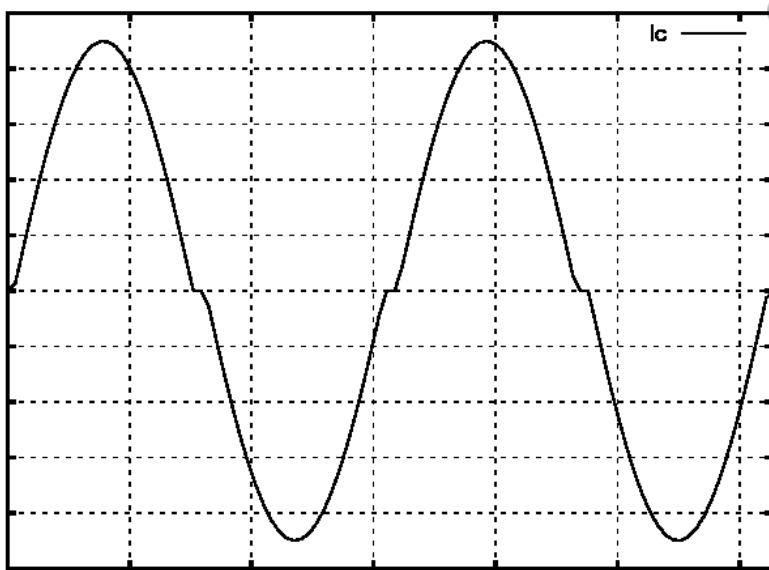


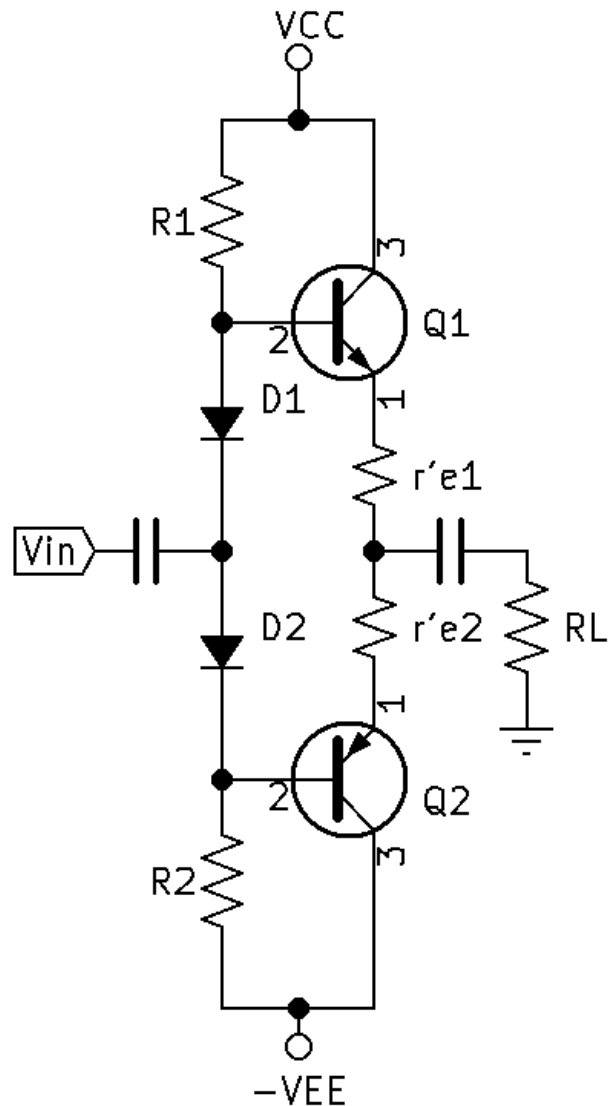
### Class AB amplification Characteristics:

- $\Delta v = 1, \Delta i = \text{high}$
- High  $Z_{in}$  and Low  $Z_{out}$
- Improved efficiency over Class A
- $MaxEfficiency_{classAB} \approx 50\%$
- Transistors are never on at the same time. Current path is through the load only.
- Transistor Amplification is active for each transistor nearly  $180^\circ$  of the input cycle (on nearly 50% of the time). This means that nearly  $360^\circ$  of the input signal is amplified.
- Achieves a high degree of linearity
- Crossover distortion occurs when both transistors are off.



**Design:**

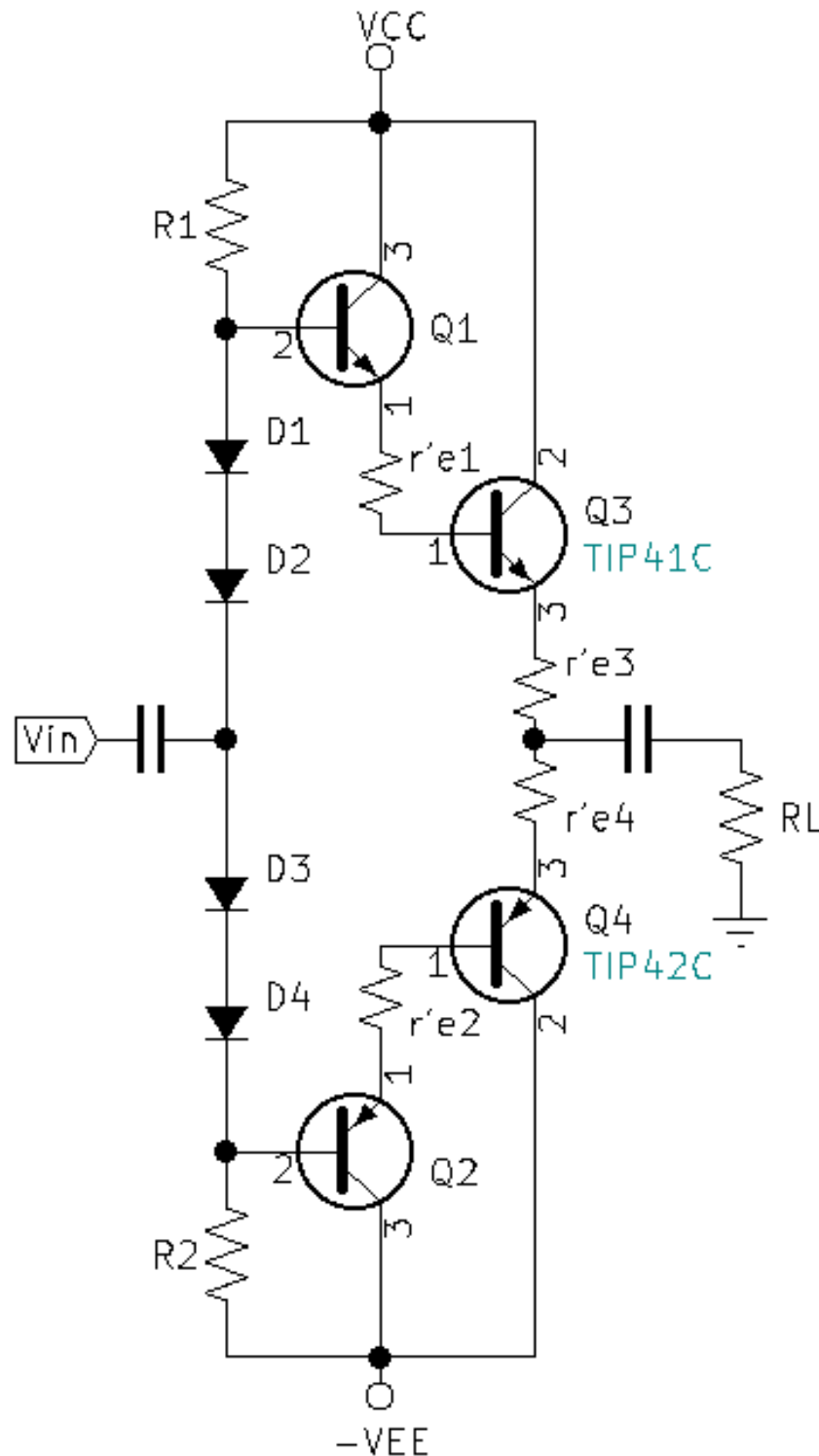
- $Q1 \text{ beta} \approx Q2 \text{ beta}$
- $V_{outmaxp} \approx \frac{V_{CC} + V_{EE}}{2}$
- $V_{inmaxp} \approx V_{outmaxp}$
- $I_{e_{max}} \approx \frac{V_{outmaxp}}{R_L}$
- $I_{b_{max}} \approx \frac{I_{e_{max}}}{B+1}$
- $I_{R1} \geq 10 \times I_{b_{max}}$
- $V_{R1} = \frac{V_{CC} + V_{EE}}{2} - V_{D1} - V_{D2}$
- $R1 = \frac{V_{R1}}{I_{R1}}$
- $R2 = R1$



- Treat as though Q1 is on and Q2 is off. Then find  $Z_{in}$  and  $Z_{out}$ .
  - $Z_{in} = [((R_L + r'e1)(B + 1) // R1) + r'd1] // [r'd2 + R2]$
  - $r'd = \frac{0.026}{I_F}$
  - $Z_{out} = \frac{[R_{gen} // (r'd2 + R2) + r'd1] // R1}{B + 1} + r'e1$

**High Power Push-Pull:**

- Follow the previous process, compensate for additional components.



## Shoot-Through:

- Shoot-Through is a non-desirable condition in which the transistors are conducting current at the same time.
- Shoot-Through can occur due to a voltage bias mismatch between the transistors and the diodes additionally Shoot-Through can be thermally or heat induced.
- Shoot-Through can be eliminated by using a small Swamping Resistor. The larger the Swamping resistor the greater the Cross-Over Distortion will be. Typically  $0.1\Omega$

