



LMbench-latency Benchmark Report

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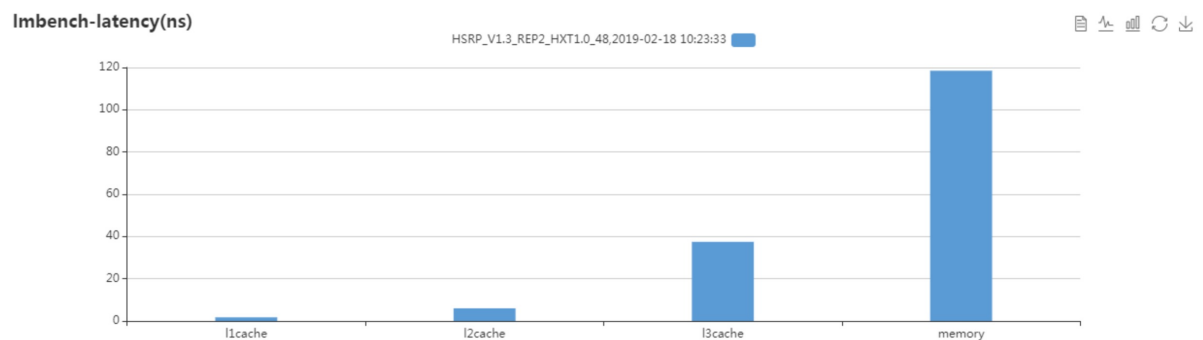
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1. Benchmark Data

Items	HSRP_V1.3_REP2_HXT1.0_48 2019-02-18 10:23:33
l1cache(ns)	1.74
l2cache(ns)	6.003
l3cache(ns)	37.472
memory(ns)	118.484

2. Benchmark Chart



3. SOC Configuration

NAME	HSRP_V1.3_REP2_HXT1.0_48 2019-02-18 10:23:33
CPU_Type	HXT1.0
Total_Socket	1
Cores_Per_Socket	48
Threads_Per_Core	1
Total_Threads	48
Cpu_Frequency	2600.00
Cpu_Max_Frequency	2600.00
Cpu_Min_Frequency	600.00
L1_Cache_Size	32K/64K
L2_Cache_Size	512K
L3_Cache_Size	61440K

4. Hardware Configuration

NAME	HSRP_V1.3_REP2_HXT1.0_48 2019-02-18 10:23:33
Platform	REP2
Memory_Total_Size	192G
Total_Memory	6
Memory_Speed	2666MHz
Memory_Configured_Clock	1333MHz
NIC_Info	enP3p1s0 speed: 100G driver: mlx5_core vendor: Mellanox bus: 0003:01:00.0
Disk_Info	sdb size: 480G type: SSD interface: SATA io_scheduler: noop vendor: samsung nvme0n1 size: 900G type: SSD interface: NVMe io_scheduler: noop vendor: toshiba
PCIe_Info	0000:00:00.0 PCI bridge: Guizhou Huaxintong Semiconductor Technology Co.,Ltd StarDragon4800 PCI Express Root Port 0000:01:00.0 Serial Attached SCSI controller: Adaptec Device 028f (rev 01) 0001:00:00.0 PCI bridge: Guizhou Huaxintong Semiconductor Technology Co.,Ltd StarDragon4800 PCI Express Root Port 0002:00:00.0 PCI bridge: Guizhou Huaxintong Semiconductor Technology Co.,Ltd StarDragon4800 PCI Express Root Port 0002:01:00.0 PCI bridge: ASPEED Technology, Inc. AST1150 PCI-to-PCI Bridge (rev 04) 0002:02:00.0 VGA compatible controller: ASPEED Technology, Inc. ASPEED Graphics Family (rev 41) 0003:00:00.0 PCI bridge: Guizhou Huaxintong Semiconductor Technology Co.,Ltd StarDragon4800 PCI Express Root Port 0003:01:00.0 Ethernet controller: Mellanox Technologies MT27800 Family [ConnectX-5]

5. Software Configuration

NAME	HSRP_V1.3_REP2_HXT1.0_48 2019-02-18 10:23:33
OS	Centos7.5
Linux_Kernel	4.14.36-5
BIOS	0ACJA541 07/03/2018
GCC	4.8.5_20150623
GLIBC	2.17-222.el7
JDK	openjdk_1.8.0.171
SPEC_CPU	2006
SPEC_JBB	2015
Lmbench	
Iperf	3
FIO	3.3

6. Tune

7. Customization

N/A