SC1005 Digital Logic Tutorial 3

Digital arithmetic

1. Perform the following <u>unsigned</u> binary addition and subtraction.

a. 1100110 +1111001 b. 11100011 - 1011101

2. Perform the following <u>two's complement</u> additions. Clearly indicate whether or not an overflow occurs.

a. 11010100 + 11101011 c. 01011101 + 00110001

b. 10111111 + 11011111 d. 01100001 + 00011111

3. Perform the following <u>two's complement</u> subtractions. Clearly indicate whether or not an overflow occurs. Check by converting to decimal values.

a. 00110110 - 01000101 c. 11010111 - 11101100

b. 01110101 - 11010110 d. 10000011 - 10001111

4. Perform the following <u>unsigned</u> binary multiplications. Verify with decimal values.

a. 110101 <u>x 1110</u> b. 010110 x 1101

5. Perform the following <u>signed 2's complement</u> binary multiplications. Verify with decimal values.

a. 110101 <u>x 1110</u> b. 010110 x 1101

Answers

- 1.
- a. 1101 1111
- b. 1000 0110
- 2.
- a. 1011 1111 (no overflow)
- b. 1001 1110 (no overflow)
- c. 1000 1110 (overflow)
- d. 1000 0000 (overflow)
- 3.
- a. 1111 0001 (no overflow)
- b. 1001 1111 (overflow)
- c. 1110 1011 (no overflow)
- d. 1111 0100 (no overflow)
- 4.
- a. 10 1110 0110
- b. 01 0001 1110
- 5.
- a. 00 0001 0110
- b. 11 1011 1110

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