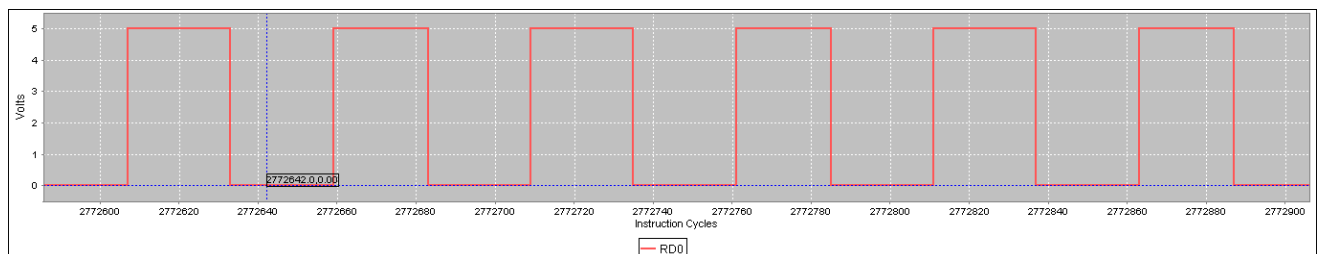


PWM on ADC pins using ASSEMBLY code

17 December 2021 23:08

```
1  .equ __30F6010, 1
2
3      .include "p30f6010.inc"
4      .global __reset
5
6      ;.....
7      ;Configuration bits:
8      ;.....
9
10     config __FWDIT, WDI_OFF           ;Turn off Watchdog Timer
11
12     config __FBORPOR, RST_IOPIN
13
14     .text
15     __reset:
16
17     mov #0b11111111111110000, w0
18
19     mov w0, TRISD
20
21     mov #0, w0
22     mov w0, OC1R
23
24     mov #25, w0
25     mov w0, OC1RS
26
27
28     mov #0, w0
29     mov w0, OC2R
30
31     mov #25, w0
32     mov w0, OC2RS
33
34     mov #0b00000000000000101, w0
35     mov w0, OC1CON
36
37     mov #0b00000000000000101, w0
38     mov w0, OC2CON
39
40     mov #50, w0
41     mov w0, PR2
42
43     bset T2CON #15
44
45     BRA LOOP
46
47     LOOP: BRA LOOP
```



PWM on ADC pins using STIMULUS (ASSEMBLY code)

17 December 2021 23:07

```
1  .equ __30F6010, 1
2
3  .include "p30f6010.inc"
4  .global __reset
5
6  .text
7  __reset:
8
9  mov #0b1111111111110000, w0
10 mov w0, TRISD
11
12 mov #0x01FF, w0
13 mov w0, TRISB
14
15 mov #0xFF00, w0
16 mov w0, ADPCFG
17
18 mov #0, w0
19 mov w0, ADCON1
20
21 mov #0, w0
22 mov w0, ADCON2
23
24 mov #0x0005, w0
25 mov w0, ADCON3
26
27 bset ADCON1, #15
28
29
30 LOOP:
31 CALL LOOP1
32 mov #0, w0
33 mov w0, OC1R
34
35 MOV ADCBUF0, W2
36 MOV W2, OC1RS
37
38
39 mov #0b00000000000000101, w0
40 mov w0, OC1CON
41
42 mov #0b00000000000000101, w0
43 mov w0, OC2CON
44
45 mov #1024, w0
46 mov w0, PR2
47
48 bset T2CON, #15
49 BRA LOOP
```

```

51      LOOP1:
52          mov #0, w1
53          mov w1, ADCHS
54          bset ADCON1, #SAMP
55          BRA DELAY1
56          DELAY1: DO #10000, ENDDelay1
57          NOP
58          NOP
59          ENDDelay1: NOP
60          BCLR ADCON1, #SAMP
61          BRA DELAY2
62          DELAY2: DO #10000, ENDDelay2
63          NOP
64          NOP
65          ENDDelay2: NOP
66          RETURN
67

```

