

پیکسل ۹۸۴۱۱۳۸۷ (ches, w)

①

R, G, B

$$7280 \times 720 = 921'600 \text{ pixels} \rightarrow 921'600 \times 3 = 2764'800 \text{ bytes frame}$$

$$8 = 2^3$$

$$\frac{2'764'800 \times 8}{10^8} \approx 0.22 \text{ (s)}$$

(2)

	A	B	C	D
IC	0.4×10^6	0.1×10^6	0.3×10^6	0.2×10^6
CPI ₁	1	2	3	3
CPI ₂	2	2	2	2

clock cycles = $\sum_{i=1}^n (\text{CPI}_i \times \text{Instruction Count})$ (Ans)

P₁:

$$\begin{aligned} \text{clockcycles}_1 &= 0.4 \times 10^6 + 0.2 \times 10^6 \\ &+ 0.9 \times 10^6 + 0.6 \times 10^6 = \underline{\underline{2.1 \times 10^6}} \end{aligned}$$

P₂:

$$\begin{aligned} \text{clockcycles}_2 &= 0.8 \times 10^6 + 0.2 \times 10^6 \\ &+ 0.6 \times 10^6 + 0.4 \times 10^6 = \underline{\underline{2 \times 10^6}} \end{aligned}$$

$$\text{Average CPI}_1 = \frac{2.1 \times 10^6}{10^6} = \underline{\underline{2.1}}$$

$$\text{Average CPI}_2 = \frac{2 \times 10^6}{10^6} = \underline{\underline{2}}$$

(3) Execution time = 100 (s)

$$\frac{t}{P} \text{ (s)} , \quad 8 \text{ (s)}$$

only 1

only P

n processors	P Ex. time	time	Normal speed up	Ideal speed up
2	50	58	$\frac{100}{58} = 1.72$	$\frac{1.72}{2} = 0.86$
4	25	33	$\frac{100}{33} = 3.03$	$\frac{3.03}{4} = 0.76$
8	12.5	20.5	$\frac{100}{20.5} = 4.88$	$\frac{4.88}{8} = 0.61$
16	6.25	14.25	$\frac{100}{14.25} = 7.02$	$\frac{7.02}{16} = 0.44$
32	3.125	11.125	$\frac{100}{11.125} = 8.99$	$\frac{8.99}{32} = 0.28$

(4)

(i)

$$CPI_1 = 1 \times 0.6 + 2 \times 0.3 + 4 \times 0.7 = 1.6$$

$$CPI_2 = 2 \times 0.6 + 3 \times 0.3 + 4 \times 0.7 = 2.5$$

(ii)

$$MIPS_1 = \frac{80 \times 10^6 \times 10^{-6}}{1.6} = 50$$

$$MIPS_2 = \frac{100 \times 10^6 \times 10^{-6}}{2.5} = 40$$

(iii)

Instruction count₁ > Instruction count₂ → performance₁ > performance₂

(5)

$$\text{Instruction Count} = 2.389 \times 10^{12}, \text{ Execution Time} = 750 \text{ (s)}$$

reference time = 9650 (s)

$$\text{clock cycle} = 1 \text{ (ns)} \quad \text{clock Rate} = \frac{1}{\text{clock cycle}} = 1 \text{ GHz (c/s)}$$

$$\text{CPI} = \frac{\text{CPU Time} \times \text{Clock Rate}}{\text{Instruction Count}} = \frac{750 \times 1 \times 10^9}{2.389 \times 10^{12}} = 0.31$$

$$\text{SPEC Ratio} = \frac{\text{reference time}}{\text{Execution time}} = \frac{9650}{750} = 12.86$$

$$\text{CPU Time} = \frac{\text{Instruction count} \times \text{CPI}}{\text{clock Rate}}$$

$$\frac{\text{CPU Time}'}{\text{CPU Time}} = \frac{\text{Instruction count}'}{\text{Instruction count}} = \frac{1.05}{1} \xrightarrow{\text{about 5\% of CPU Time}}$$

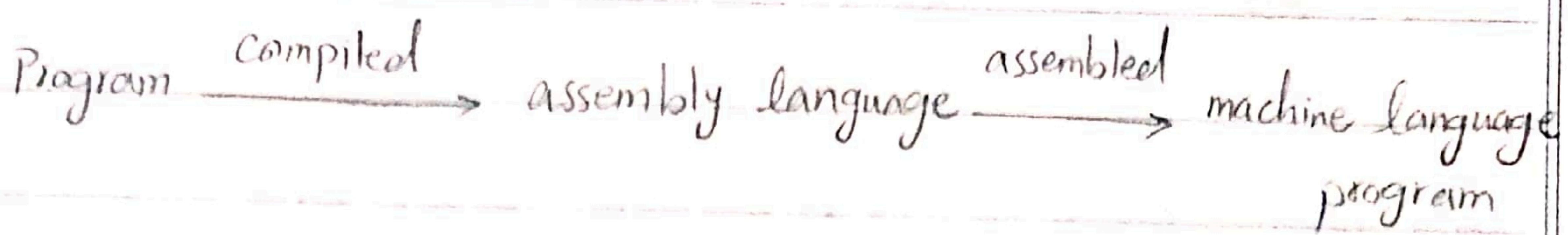
$$\text{clock Rate} = 1 \text{ GHz}, \text{ Execution Time} = 700 \text{ ns}$$

$$\text{Instruction count} = 0.85 \times 2.389 \times 10^{12} = 2.03 \times 10^{12}$$

$$\text{SPEC ratio} = 13.7 \quad \text{CPI} = \frac{\text{CPU Time} \times \text{clock Rate}}{\text{Instruction Count}}$$

$$\text{CPI} = \frac{700 \times 10^9 \times 4 \times 10^9}{2.03 \times 10^{12}} = 13.8 \times 10^{-10}$$

⑥



⑦

$$\text{Power} = \frac{1}{2} \text{ capacitive load} \times \text{voltage}^2 \times \text{frequency}$$

$$\frac{\text{Power}_2}{\text{Power}_1} = \frac{0.8 P_1}{P_1} = \left(\frac{V_2}{V_1} \right)^2 \rightarrow V_2 = 2\sqrt{0.2} V_1$$

⑧

P_1 : clock Rate = 2.5 GHz , CPI = 1.2

P_2 clock Rate = 2 GHz , CPI = 0.9

P_3 clock Rate = 3.5 GHz , CPI = 1.9

$$\text{Performance} = \frac{\text{clock Rate}}{\text{CPI}}$$

P_2 (Ans)

$$\text{Performance}_{P_1} = \frac{2.5 \times 10^9}{1.2} = 2.08 \times 10^9$$

$$\text{Performance}_{P_2} = \frac{2 \times 10^9}{0.9} = 2.22 \times 10^9$$

$$\text{Performance}_{P_3} = \frac{3.5 \times 10^9}{1.9} = 1.84 \times 10^9$$

CPU clock cycles = CPU Time × Clock Rate

(C)

$$\text{CPU clock cycles}_{P_1} = 10 \times 2.5 \times 10^9 = 25 \times 10^9 \text{ (s)}$$

$$\text{CPU clock cycles}_{P_2} = 10 \times 2.0 \times 10^9 = 20 \times 10^9 \text{ (s)}$$

$$\text{CPU clock cycles}_{P_3} = 10 \times 3.5 \times 10^9 = 35 \times 10^9 \text{ (s)}$$

$$\text{Instruction Count} = \frac{\text{clock cycles}}{\text{CPI}}$$

$$\text{Instruction count}_{P_1} = \frac{25 \times 10^9}{1.2} = 20.83 \times 10^9$$

$$\text{Instruction Count}_{P_2} = \frac{20 \times 10^9}{0.9} = 22.22 \times 10^9$$

$$\text{Instruction Count}_{P_3} = \frac{35 \times 10^9}{1.9} = 18.42 \times 10^9$$

$$\text{Clock Rate} = \frac{\text{Instruction count} \times \text{CPI}}{\text{CPU time}} \quad 0.70 \times 10 = 7 \text{ s (C)}$$

$$\text{Clock Rate}_{P_1} = \frac{20.83 \times 10^9 \times (1.2 \times 1.2)}{7} = 4.29 \times 10^9 = 4.29 \text{ GHz}$$

$$\text{Clock Rate}_{P_2} = \frac{22.22 \times 10^9 \times 0.9 \times 1.2}{7} = 3.43 \times 10^9 = 3.43 \text{ GHz}$$

$$\text{Clock Rate}_{P_3} = \frac{18.42 \times 10^9 \times 1.9 \times 1.2}{7} = 6 \times 10^9 = 6 \text{ GHz}$$

⑨

$$\text{CPU Time} = \frac{\text{Instruction Count} \times \text{CPI}}{\text{Clock Rate}}$$

CPI (cycles) - 0.06/20 & Instruction count not -
(clock Rate)