

1. مزایای مدار CVSL چیست ؟ حداقل دو مورد را توضیح دهید

1. Potential advantages include reduced circuit delay, higher layout density, lower power consumption and extended logic flexibility.
2. robust design nature which can implement reliable circuits with excellent noise margin.
3. CVSL seeks the benefits of ratioed circuits without the static power consumption.
- 4.

2. مدار XNOR/XOR سه ورودی را به روش CVSL ترسیم کنید.

