

Bachelor's Thesis in Computer Science

Efficient Symbolic Execution using CEGAR over Two Abstract Domains

Thomas Lemberger

Supervisor: Prof. Dr. Dirk Beyer

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Abstract

Symbolic execution is a powerful approach to automatic software verification we already applied to the domain of configurable software verification in previous work. Unfortunately, it suffers from bad runtime performance, mainly due to path explosion caused by its high precision. To mitigate this problem, we apply counterexample-guided abstraction refinement (CEGAR), an abstraction technique mostly used in model checking, to our configurable program analysis (CPA) for symbolic execution. We design two different refinement procedures for its compositional domain, considering two strongly intertwined domains at the same time. First, applying CEGAR to multiple domains is a novel approach compared to the existing single or combined refinement procedures, which only handle one domain at a time. Second, often seen as two opposites, we are, to our knowledge, the first to apply CEGAR to symbolic execution. Both refinement procedures were implemented in the verification framework CPACHECKER and evaluated with different configurations and optimizations to find the one yielding the best results. We are able to show a significant boost in runtime performance compared to symbolic execution without CEGAR for most programs. This concludes CEGAR as a valid mean to improve the runtime performance of symbolic execution and shows a valid way to apply CEGAR to multiple domains.

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1 Introduction

Software systems are prone to error due to multiple factors: The developers skills, humans' limited understanding of software principles, communication problems in development, missing or sparse documentation and unforeseen interdependencies between software components are just some of them.

Because of this testing has been an integral part of software development for quite some time, often claiming about 50% of development effort and more than 50% of the budget. *Software testing* describes the execution of a program with the intention of finding errors. The tester, either a person or another program, uses different inputs and checks that the proper output is produced. The nature of this approach determines that only a finite amount of inputs is possible in finite time. As a result, it is impossible to ensure the errorless execution of a program with arbitrary input. Additionally, human testing is a technique only shortly touched in most software engineering educations, resembling art more than science.[MSB11]

Along with these points, the rise of ubiquitious computing and reactive systems complicates the process of reliable verification by testing even more. Always running systems that gather, evaluate and adapt their behaviour to information of their environment pose an immense amount of possible inputs and interactions due to these. This makes it almost impossible to predict the behaviour of such systems through testing.

An alternative to testing is formal verification, which tries to produce formal statements that are true for all possible behaviours of a system, using mathematical methods. These statements are then used for proving that a specific specification is met. One area of formal verification is *automated software verification*. It tries to reach the above goal by only using software that works without the help or feedback of humans. CPACHECKER [BK11] is such a program that yielded excellent performance in the last iterations of the Competition on Software Verification (SV-COMP) [Bey13] [Bey14] [Bey15b]. CPACHECKER is a framework for *Configurable Software Verification* [BHT07] utilizing different *configurable program analyses* (CPAs) to locate possible property violations of a specification in a program. Three such CPAs are the value

```
extern VERIFIER nondet int();
  int main() {
    int a = ___VERIFIER_nondet_int();
    int b;
    if (a >= 0) {
                                                                    _VERIFIER_nondet_int()
      b = a;
                                                              Ø
                                                                    a \geq 0
                                                   |a| \ge 0
     } else {
10
                                                                     Ø
      b = a + 1;
11
                                              b = a + 1
                                                                       b = a
12
                                                           merge
    if (b < a) {
14
  ERROR:
15
      return -1;
16
                                                  |b| < a
                                                                   [b < a]
    }
17
  }
18
                                                                     return -1
```

Figure 1.1: Simple program and its execution by the value analysis CPA.

analysis CPA, which uses concrete variable assignments, the predicate CPA, which creates predicates for describing properties of program paths, and the symbolic execution CPA, which uses an extension of the value analysis CPA tracking non-deterministic values as symbolic ones in combination with the constraints CPA, which tracks constraints to symbolic values on program paths. While the value analysis CPA has high efficiency due to her simplicity, it can't handle complex program characteristics like pointers or non-deterministic values. The predicate CPA, in constrast, is very expressive, but has low efficiency since SAT checks are necessary for computing the feasibility of a program path. The symbolic execution CPA based on the concepts of symbolic execution [?] poses something in between these two, not being able to handle some complex program characteristics as it is partly based on the value analysis CPA, but being able to handle non-deterministic values. On the other hand, it uses SAT checks, too, though less often and over smaller formulas.

Figure 1.1 displays an example program that uses non-deterministic values and its analysis using the classic value analysis CPA. Since the CPA does not store any information about non-deterministic assignments, no information about the relation

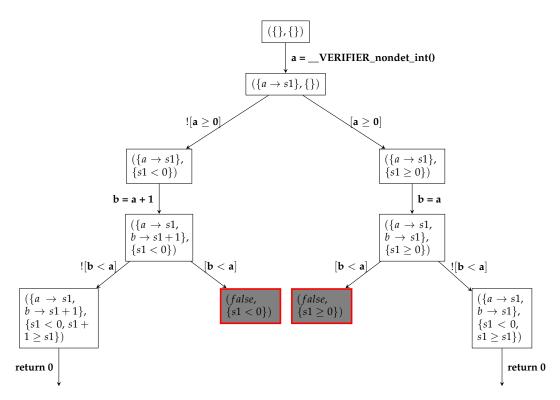


Figure 1.2: Analysis of the program in Listing ?? by the symbolic execution CPA.

between **a** and **b** exists and the property violation is reachable according to the analysis. This produces a *false alarm*. In constrast to this, the symbolic execution CPA is able to handle the non-deterministic assignment to **a** a and its later usage. It returns that the program is safe, correctly. Figure 1.2 shows this analysis.

Symbolic execution CPA's ability to track non-deterministic values is able to reduce the number of false alarms to a great extent, as we already showed in [Lem15]. Runtime wise, it performs poorly, though, when compared to the value analysis CPA. Since it considers all variable assignments, both deterministic and non-deterministic, and all occurring assumptions, its state space is exponential to the amount of occuring assumptions. If a infinite loop occurs, the state space is infinite, too. This problem is called *path explosion* and characteristic to symbolic execution.[AGT08] Obviously, an exponential amount of states does not scale to large programs. In addition, the cost for SAT checks, which are performed at every assumption, are exponential to the amount of non-deterministic values occuring in all encountered assumptions on the currently considered program path. Evaluation in [Lem15] showed that the symbolic execution CPA spends up to 95% of its runtime for SAT checks.

In this work we design, implement and evaluate different approaches to increasing the performance of the symbolic execution CPA. Our main contribution is, along with variations to the existing merge and less-or-equal operators of the CPA, the application of CEGAR [CGJ⁺03] to the composition of the two strongly intertwined CPAs symbolic value analysis CPA and constraints CPA with precision refinement for both CPAs with two different precision types.

This work is divided into four parts: Theoretical background and contributions, their implementation, their evaluation, and future work and a conclusion. First we will describe the concepts that are the basis for our work, such as Configurable Software Verification, used CPAs and CEGAR. Next, we will illustrate the theory behind our own contribution, before explaining details about the existing and newly added implementation and deviations from theory. We will evaluate all presented concepts and compare them to the performance of the value analysis CPA, predicate CPA, and symbolic execution CPA of our old work. Last, we will give a short outlook to possible future work in this field and close with a conclusion.

2 Related work

Symbolic execution was first introduced by [?] in 1976 for program testing and verification. In this classic symbolic execution, a programming language is extended to be able to handle symbolic values without changing its syntax. Then, a program in this language is executed using symbolic values as its input. If a fork in the program control flow occurs, for example because of an if-statement, for which both branches are possible, execution splits into two parallel executions, recording the particular branching condition (i.e. assume statement). Each such execution represents the execution of the program for a set of concrete input values, which can be derived based on all recorded branching conditions of an execution. This way, a lot less symbolic executions are necessary for reaching a certain test coverage than when using concrete executions. Verification at this stage was only possible by providing conditions the output of the program had to fulfill. Today, symbolic execution is used for testing, test case generation and verification.

Concolic testing

In the context of testing, concolic testing [?] [?] [?] evolved from symbolic execution. In concolic testing, a program is executed with some arbitrary, but concrete input values, while tracking the conditions created by the branches the concrete execution takes. When finished, the last encountered, not yet negated condition is negated, new input values are created based on the conjunction of this negated condition and all other encountered conditions, and the program is executed with these new values, forced to take the previously unexplored branch the negated condition stems from. While this technique alone still suffers from path explosion, single executions are very fast as only concrete values are used, which allow easy and precise reasoning about complex data structures [?] and allows the simplification of constraints unsolvable using symbolic values by concrete values. In addition, the used concrete input values can be used for easy test case generation. Nevertheless, concolic testing is obviously not suitable for verification, as program properties

can only be examined based on the current set of concrete input. It still deserves a mention because of its wide use and as most techniques we will present in the following are based on it.

There are four major areas improvements focus on for mitigating the pathexplosion problem: (1) Search heuristics for achieving a high level of branch or path coverage, (2) Compositional execution, this means creating summaries of functions or paths to reuse them instead of recomputing already explored states, (3) Handling of unbounded loops, which cause infinite path exploration when using symbolic execution, and (4) Using interpolants for tracking reasons why a certain path is infeasible.

While the concepts are presented in the context of testing, they can be applied to verification, too.

Search heuristics

[?] proposes three different heuristics for exploring a CFA in concolic testing to reach a target region or uncovered branches faster, instead of simply using a depth-first search. The first heuristic chooses branches to take based on their distance in the CFA to currently uncovered branches/the target region. The second heuristic, inspired by random testing¹, chooses random paths. For each branch at each execution, it is randomly decided whether to take it. The third heuristic chooses of all branches at the current path one it will not take in the next iteration, randomly. They were able to prove the increased effectiveness when using any of these three heuristics, with the first being the best in terms of coverage, quickly followed by the third. Klee [?], a tool for automatic test generation running one symbolic execution for each branch taken in parallel, uses two different heuristics in turn to decide at each program location which execution to continue first. The first heuristic, called random path selection, maintains a binary tree representing the program path followed by all active executions. The leaves of the tree are the executions, while each node represents a fork in the program control flow. The tree is traversed from the root and each branch is taken with a possibility of 1/2. This way, executions high in the tree, which have the most freedom to reach currently uncovered branches, are more likely to be chosen. In addition, starvation is impossible due to the randomness of the heuristic. 1. Using the first heuristic increases the chance to cover previously uncovered code as soon as possible. 2. Choosing each processes at a program location with the same

¹ Testing technique using input values randomly generated

probability, starting at the top of the execution tree, favors executions currently high up in the tree.

While heuristics can assist in speeding up the process of finding an error, they hardly mitigate the problem of path explosion when trying to prove that a program is error-free.

Compositional execution

The compositional symbolic execution proposed in [?] tests functions in isolation to create summaries of them. A summary of a function is a formula describing preconditions for its input and postconditions for its output. If the preconditions are met for the current used input, the function summary can be used instead of executing the function again. Summaries are computed for functions whenever no fitting summary exists, based on their call hierarchy. This use of summaries is implemented as an extension to the symbolic execution testing tool DART [?], called SMART. [AGT08] extends this notion by lazy and relevant exploration, computing new function summaries only if no conjunction of summaries can be used to reach a ceratin branch or program location and recognizing branches that are not able to reach a certain branch or program location. It uses uninterpreted functions and predicates describing a functions (possibly not fully known) semantics for this. Thanks to its flexibility, it can be combined with any search heuristic. CPACHECKER supports block- and function summaries for the predicate CPA and value analysis CPA, so it should be easy to adapt this for the symbolic execution CPA. The use of such summaries might prove useful for programs requiring a high precision, but we assume that, when analysing a program which only requires to track few program variables and constraint, just reducing the state space by using CEGAR and as such minimizing repeated computations is more useful. Both approaches are orthogonal though, and can be used in combination.

Handling of unbounded loops

In classic symbolic execution, unbounded loops result in infinite execution. [?] tackles this problem by computing inductive invariants for loops, unrolling loops up to the point interpolants which are also loop invariants can prove infeasiblity of an error path. A major downside to this approach is that it will only terminate if such invariants can be found. Inspired by this approach, [?] abstracts symbolic states at

loop headers to only consist of invariants that hold for one path. If these invariants are too coarse to prove the infeasiblity of a counterexample, a refinement procedure similar to CEGAR is used to refine them. This is a compromise between performing eager symbolic execution and lazy CEGAR when encountering unbounded loops. [?] analyses cyclic paths in the CFA and computes a so called *template* for each one, describing all possible program states that may leave the cycle after any number of iterations. In the following symbolic execution, these templates are then used when encountering loops to directly jump to the loop exits, resulting in symbolic states based on the path to the cycle, a parameter k of iterations along the cycle, and the execution step leading to the exit. This mitigates the path explosion problem considerably, as no more loops exist in execution, but deepens the complexity of formulas to solve, due to the parameter k.

Interpolation

CEGAR with predicate CPA/model checking and with valueAnalysis

3 Theoretical background

3.1 General Overview of configurable program analysis

For the sake of simplicity, all theoretical concepts are based on a fictional programming language that only consists of variable assignments (e.g. x := 5 or y := x) and assumptions (e.g. [x > 5] or [y < x]). All values are integers of arbitrary magnitude. The implementation of our presented concepts is performed in CPACHECKER a verification tool for C programs, though.

We represent a program by a control flow automaton (CFA) [BGS][BW12]. A CFA $A = (L, l_0, G)$ is a directed graph whose nodes L represent the program locations of the program. The initial node $l_0 \in L$ represents the program entry. An edge $g \in G \subseteq L \times Ops \times L$ exists between two nodes if a program statement exists that transfers control between the program locations represented by the nodes. Each edge is labeled with the operation that transfers the control. If a node has no leaving edges it is a final node. Final nodes represent the program exit. A CFA for the previously mentioned example program can be seen in Figure 3.1. A path σ [BLW15] is a sequence $\langle (op_1, l_1), ..., (op_n, l_n) \rangle$ of program locations and their corresponding operations. A path σ is a *program path*, if σ represents a syntactic walk through the CFA, that means for every $1 \le i \le n$ a CFA edge $g = (l_{i-1}, op_i, l_i)$ exists and l_0 is the initial program location. Every path $\sigma = \langle (op_1, l_1), ..., (op_n, l_n) \rangle$ defines a constraint sequence $\gamma_{\sigma} = \langle op_1, ..., op_n \rangle$. The conjunction of two constraint sequences $\gamma = \langle op_1, ..., op_n \rangle$ and $\gamma' = \langle op'_1, ..., op'_n \rangle$ is defined as their concatenation, that means $\gamma \wedge \gamma' = \langle op_1, ..., op_n, op'_1, ..., op'_n \rangle$. The set X is the set of all program variables occurring in a program.

```
extern ___VERIFIER_nondet_int();
   int main() {
     int a = ___VERIFIER_nondet_int();
                                                                       a := __VERIFIER_nondet_int()
     if (a >= 0) {
       b = a;
                                                        [!(a \geq 0)]
                                                                            [\mathsf{a} \geq \mathsf{0}]
     } else {
10
                                                                              8
                                                             11
       b = a + 1;
11
                                                                               b := a
                                                   b := a + 1
12
                                                                             10
                                                             12
     if (b < a) {
14
   ERROR:
                                                                     14
       return -1;
16
                                                        [!(b < a)]
                                                                            |\mathbf{b} < \mathbf{a}|
     }
17
   }
                                                             17
                                                                             16
18
```

Figure 3.1: An example CFA.

3.1.1 Concrete state definition

A concrete state c is a total function $c: X \cup \{pc\} \to \mathbb{Z}$ that assigns a specific value of \mathbb{Z} to every program variable $x \in X$ and to the program counter pc. The program counter pc represents the current location in the program. The set of all concrete states of a program is C. A set $r \subseteq C$ is called a region. For the reachability problem the region of concrete states that violate a given specification is called the target targ

3.1.2 Abstract state definition

An abstract domain [BHT07] $D = (C, \mathcal{E}, [\![\cdot]\!])$ consists of a set of possible concrete states C, a semi-lattice \mathcal{E} that describes the abstract states and their possible relation to each other and a concretization function $[\![\cdot]\!]: \mathcal{E} \to 2^C$ which maps each element of \mathcal{E} to a subset of C.

A semi-lattice $\mathcal{E} = (E, \top, \bot, \sqsubseteq, \sqcup)$ consists of a set E of elements, a top element $\top \in E$, a partial order $\sqsubseteq \subseteq (E \times E)$ and the total function $\sqcup : (E \times E) \to E$ called join operator. The elements $e \in E$ of an abstract domain are called *abstract states*.

The basis of automatic software verification is the reachability problem: For a given specification it is derived whether a program state is reachable that violates this specification. Traditionally, two major approaches exist, both working on a reachability tree: Model checking and program analysis, also called data flow analysis. While model checking is mostly concerned with finding a program abstraction with a precision high enough to eliminate false alarms, which in turn only allows it to handle small programs because of poor performance, program analysis tries to reach high efficiency by looking at only a few chosen characteristics of a program. It does so by using abstract states of a specific, chosen abstract domain to abstract from concrete program states.

Program analysis starts with an initial abstract state, usually \top , and uses a transfer relation to derive new abstract states from old abstract states and program statements. Customization of program analysis usually means to choose one or more abstract interpreters, that is the abstract domains, transfer functions and widening operators to use.[BHT07]

Configurable software verification tries to bridge the gap of precision finding of model checking and the efficiency focus of program analysis to allow for arbitrary algorithms between these two extremes by providing the possibility to control the precision and efficiency of the algorithm by choosing all of the following:

- a) one or more abstract interpretations, that is the abstract domains to work in and the transfer functions that describe the possible transfers between abstract states,
- b) a precision type,
- c) a merge operator which controls when two nodes of the reachability tree are merged, i.e. when two abstract states are merged,
- d) a stop operator that controls when the exploration of a path is stopped, i.e. when a state is already covered by the existing reached states (this is also called termination check), and
- e) a precision adjustment operator that can weaken or strengthen an abstract state based on a precision.

These elements are encapsulated in a *configurable program analysis* (CPA) [?], which is used by the CPA algorithm.

3.1.3 Configurable program analysis definition

A CPA with dynamic precision adjustment $\mathbb{D} = (D, \Pi, \leadsto, \mathsf{merge}, \mathsf{stop}, \mathsf{prec})$ consists of:

- 1. The abstract domain *D*, as described above. *E* is the set of its semi-lattice's elements. For soundness (i.e. if a property violation exists, it is always found) and progress of the program analysis, the following requirements have to be fulfilled:[BHT07][?]
 - a) the top element of abstract states has to represent all possible concrete states and the bottom element must represent none, formally put $[\![\top]\!] = C$ and $[\![\bot]\!] = \emptyset$,
 - b) the join operator has to be precise or over-approximating. That means the join of two abstractes states always has to represent the same or more concrete states than the union of the concrete states both abstract states represent. This can be formally expressed as $\forall e, e' \in E : [e \sqcup e'] \supseteq [e] \cup [e']$,
 - c) $\forall e, e' \in E : e \sqsubseteq e' \Rightarrow \llbracket e \rrbracket \subseteq \llbracket e' \rrbracket$
- 2. The precision type Π .
- 3. The transfer relation $\leadsto \subseteq (E \times G \times E \times \Pi)$. It assigns to each abstract state $e \in E$ possible abstract successors $e' \in E$ with a precision $\pi \in \Pi$. For every program statement $g \in G$ we write $e \stackrel{g}{\leadsto} (e', \pi)$ if $(e, g, e', \pi) \in \leadsto$ and $e \leadsto (e', \pi)$ if a program statement g with $e \stackrel{g}{\leadsto} (e', \pi)$ exists.

The transfer relation \leadsto has to be total, that is $\forall e \in E : \exists e' \in E : e \leadsto e'$, and it has to be precise or over-approximating. That means the union of all concrete states represented by all possible abstract successors of an abstract state e and a program statement g have to be the same or more than the union of all concrete successors of statement g and all concrete states represented by e. This can be formally expressed as $\forall e \in E, g \in G : \bigcup_{e \in g} g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ and $g \in F$ are $g \in F$ are $g \in F$ are $g \in F$ are $g \in F$

4. The merge operator merge : $E \times E \times \Pi \to E$, which weakens the information of the second given state based on the first state. It returns an abstract state of the given precision. The result of $merge(e,e',\pi)$ can be anywhere between e' and \top . Two characteristic merge operators are

$$merge^{sep}(e, e', \pi) = e'$$

and

$$merge^{join}(e, e', \pi) = e \sqcup e'.$$

5. The stop operator stop : $E \times 2^E \times \Pi \to \mathbb{B}$, which checks if the given abstract state with the given precision is covered by the set of abstract states given as second parameter. $stop(e, R, \pi) = true$ always has to imply $[\![e]\!] \subseteq \bigcup_{e' \in R} [\![e']\!]$ to ensure soundness. Two characteristic stop operators are

$$stop^{sep}(e, R, \pi) = \exists e' \in R : e \sqsubseteq e'$$

and

$$\mathsf{stop}^{join}(e,R,\pi) = e \sqsubseteq \bigsqcup_{e' \in R} e'.$$

For stop^{join}, the abstract domain has to be a powerset domain, that means $e \sqsubseteq e' \Rightarrow e \supseteq e'$ for abstract states e, e'.

6. The precision adjustment prec : $E \times \Pi \times 2^{E \times \Pi} \to E \times \Pi$. It computes a new abstract state and precision for a given abstract state based on its precision and a set of abstract states with precision. It can both strengthen and weaken an abstract state. The following condition has to hold: $\forall e, \hat{e} \in E, \pi, \hat{\pi} \in \Pi, R \subseteq (E \times \Pi)$: $(\hat{e}, \hat{p}) = \operatorname{prec}(e, \pi, R) \Rightarrow \llbracket e \rrbracket \subseteq \llbracket \hat{e} \rrbracket$.

3.1.4 CPA algorithm

The CPA algorithm described in Algorithm 1 uses an arbitrary CPA of this form to solve the reachability problem. Given a CPA \mathbb{D} , an initial abstract state e_0 to start its computation at and an initial precision π_0 , the algorithm computes the set reached of reachable abstract states. As long as the set of abstract states that still have to be processed (waitlist) is not empty, an abstract state $e \in \text{waitlist}$ is removed from the waitlist and each possible abstract successor e' and its precision π is examined:

First, precision adjustment is performed on e' based on π and reached. This produces a new abstract state \hat{e} and a new precision $\hat{\pi}$.

Next, it is checked whether the adjusted state \hat{e} represents any concrete state that violates a property. This is done by isTargetState : $E \to \mathbb{B}$ in Line 8, whose behaviour can be defined arbitrarily. In classic reachability computation, the complete reachable set would be computed first.

Algorithm 1 *CPA*(\mathbb{D} , e_0 , π_0), adapted from [?]

Input: a CPA $\mathbb{D} = (D, \Pi, \leadsto)$, merge, stop, prec), an initial abstract state $e_0 \in E$ with E being the set of elements of D, and an initial precision $\pi_0 \in \Pi$.

Output: a set of abstract states reachable from e_0

```
Variables: reached and waitlist, both subsets of E \times \Pi
 1: reached := \{(e_0, \pi_0)\}
 2: waitlist := \{(e_0, \pi_0)\}
 3: while waitlist \neq \emptyset do
          choose (e, \pi) from waitlist
 5:
          remove (e, \pi) from waitlist
          for all e' with e \rightsquigarrow (e', \pi) do
 6:
              (\hat{e}, \hat{\pi}) := \operatorname{prec}(e', \pi, \operatorname{reached})
                                                                                   ▶ Precision adjustment
 7:
              if isTargetState(\hat{e}) then
 8:
                   return (reached \cup {(\hat{e}, \hat{\pi})}, waitlist)
 9:
              for all (e'', \pi'') \in \text{reached do}
10:
                   e_{new} := \text{merge}(\hat{e}, e'', \hat{\pi})
11:

    Combine with existing state

                   if e_{new} \neq e'' then
12:
                        waitlist := (waitlist \cup {(e_{new}, \hat{\pi})}) \setminus {(e'', \pi'')}
13:
                        reached := (reached \cup \{(e_{new}, \hat{\pi})\}) \setminus \{(e'', \pi'')\}
14:
              if \neg \operatorname{stop}(\hat{e}, \{e \mid (e, \cdot) \in \operatorname{reached}\}, \hat{\pi}) then
15:
                   waitlist := waitlist \cup \{(\hat{e}, \hat{\pi})\}
16:
17:
                   reached := reached \cup \{(\hat{e}, \hat{\pi})\}
18: return reached
```

Next, each already reached abstract state $e'' \in \texttt{reached}$ is individually merged with the new state \hat{e} with precision $\hat{\pi}$. If the merge weakened e'', it and its precision are replaced with the weakened state and the new precision $\hat{\pi}$ in reached and waitlist (Lines 11 - 14). Next, the termination check checks whether the new abstract successor \hat{e} is already covered by the current reached set. If it is not, it is added to waitlist and reached. After this it is continued with the next element in the waitlist. If the waitlist is empty, there are no more reachable states and the reached set is returned.

3.2 Basic definition of CPAs used in this paper

A definition of all CPAs important to this paper follows.

3.2.1 Composite CPA

It is often useful to combine multiple CPAs to use their individual strengths and mitigate their weaknesses. A composition of two CPAs [?] can be expressed as $(\mathbb{D}_1, \mathbb{D}_2, \Pi_\times, \leadsto_\times, \mathsf{merge}_\times, \mathsf{stop}_\times, \mathsf{prec}_\times)$. We will extend this definition to allow the composition of an arbitrary number of CPAs. It consists of:

- 1. Two CPAs \mathbb{D}_1 and \mathbb{D}_2 . The CPAs have to share the same set of concrete states C, but can differ in any other way.
- 2. A composite set of precisions Π_{\times} .
- 3. A composite transfer relation $\leadsto_{\times} \subseteq (E_1 \times E_2) \times G \times (E_1 \times E_2) \times \Pi_{\times}$.
- 4. A composite merge operator $\mathsf{merge}_{\times} : (E_1 \times E_2) \times (E_1 \times E_2) \times \Pi_{\times} \to (E_1 \times E_2)$.
- 5. A composite termination check $stop_{\times} : (E_1 \times E_2) \times 2^{E_1 \times E_2} \times \Pi_{\times} \to \mathbb{B}$.
- 6. A composite precision adjustment

$$\operatorname{prec}_{\times}: (E_1 \times E_2) \times \Pi_{\times} \times 2^{(E_1 \times E_2) \times \Pi_{\times}} \to (E_1 \times E_2) \times \Pi_{\times}.$$

The three composite operators 3. - 5. use the corresponding operators of the contained CPAs \mathbb{D}_1 to \mathbb{D}_i as well as multiple *strenghtening operators* \downarrow_j and *compare relations* \preceq_j . They only alter lattice elements through these components.

The strengthening operator \downarrow : $E_k \times E_l \to E_k$ computes a stronger abstract state of the type E_k by using the information of an abstract state of the type E_l , with $1 \le k, l \le i$ and $k \ne l$ for i being the number of CPAs in the composition. It has to meet the requirement $\downarrow (e, e') \sqsubseteq e$. The use of strengthening operators in the transfer relation \leadsto_{\times} allows the use of a transfer relation that is stronger than the simple combination of the transfer relations of \mathbb{D}_1 and \mathbb{D}_2 .

A compare relation $\leq E_k \times E_l$ allows the comparison of two abstract states of different types.

The composition of CPAs can be used to construct a composite CPA $\mathcal{C} = (D_{\times}, \Pi_{\times}, \leadsto_{\times}, \mathsf{merge}_{\times}, \mathsf{stop}_{\times}, \mathsf{prec}_{\times})$ with abstract domain $D_{\times} = D_1 \times D_2 = (C, \mathcal{E}_{\times}, \llbracket \cdot \rrbracket_{\times})$ and semi-lattice $\mathcal{E}_{\times} = \mathcal{E}_1 \times \mathcal{E}_2 = (E_1 \times E_2, (\top_1, \top_2), (\bot_1, \bot_2), \sqsubseteq_{\times}, \sqcup_{\times})$. The semi-lattice uses the less-or-equal operator \sqsubseteq_{\times} defined as $(e_1, e_2) \sqsubseteq_{\times} (e'_1, e'_2)$ if $e_1 \sqsubseteq_1 e'_1$ and $e_2 \sqsubseteq_2 e'_2$ and the join operator defined as $(e_1, e_2) \sqcup_{\times} (e'_1, e'_2) = (e_1 \sqcup_1 e'_1, e_2 \sqcup_2 e'_2)$. The concretization function $\llbracket \cdot \rrbracket_{\times}$ is defined as $\llbracket (e_1, e_2) \rrbracket_{\times} = \llbracket e_1 \rrbracket_1 \cap \llbracket e_2 \rrbracket_2$.

A special merge operator in this context is

$$\mathsf{merge}^{\mathsf{agree}} : (E_1 \times E_2) \times (E_1 \times E_2) \times (\Pi_1 \times \Pi_2) \to (E_1 \times E_2)$$

. It uses the merge operators of each CPA on the corresponding abstract states individually, if, after the merge, every component's state is less or equal the both given states. Otherwise it behaves like $merge^{sep}$, i.e. no merge is performed.

$$\begin{aligned} & \operatorname{merge}^{\operatorname{agree}}(e_1, e_2, e_1', e_2', \pi_1, \pi_2) = \\ & \begin{cases} (\operatorname{merge}_1(e_1, e_1', \pi_1), \operatorname{merge}_2(e_2, e_2', \pi_2)) & \text{if } \operatorname{merge}_1(e_1, e_1', \pi_1) \sqsubseteq e_1, e_1' \text{ and} \\ (e_1', e_2') & \operatorname{otherwise} \end{cases} \end{aligned}$$

3.2.2 Location CPA

The location CPA [BGS] [?] $\mathbb{L} = (D_{\mathbb{L}}, \widetilde{\Pi}, \leadsto_{\mathbb{L}}, \mathsf{merge}^{sep}, \mathsf{stop}^{sep}, \widetilde{\mathsf{prec}})$ is a CPA that analyses the syntactical reachability of program locations. It does not consider any semantics and is mostly used to track the program location for other CPAs by using a composite CPA. This allows for simpler CPAs since they do not have to care about location tracking individually. The location CPA contains:

- 1. The abstract domain $D_{\mathbb{L}} = (C, \mathcal{L}, \llbracket \cdot \rrbracket)$. It consists of the set C of possible concrete states, the semi-lattice \mathcal{L} and the concretization function $\llbracket \cdot \rrbracket$. $\mathcal{L} = (L \cup \{\top\}, \top_{\mathbb{L}}, \bot, \sqsubseteq, \sqcup)$ is defined by its less-or-equal operator \sqsubseteq , which has the following properties: $l \sqsubseteq \top_{\mathbb{L}}, l \neq l' \Rightarrow l \not\sqsubseteq l'$ and $\bot \sqsubseteq l$ for all $l, l' \in L$ (this implies $\top_{\mathbb{L}} \sqcup l = \top_{\mathbb{L}}$ and $l \sqcup l' = \top_{\mathbb{L}}$ for all $l, l' \in L$ with $l \neq l'$) A semi-lattice with these properties is also called *flat semi-lattice*. The concretization function is defined as $\llbracket \top_{\mathbb{L}} \rrbracket = C$, $\llbracket l \rrbracket = \{c \in C \mid c(pc) = l\}$ for all $l \in L$.
- 2. The set $\widetilde{\Pi} = \{\widetilde{\pi} \text{ of precisions that only contains a single precision. This means that all abstract states have the same precision all the time.$
- 3. The transfer relation $\leadsto_{\mathbb{L}}$, which has the transfer $l \stackrel{g}{\leadsto}_{\mathbb{L}} (l', \widetilde{\pi})$ if g = (l, op, l') for any operation op and the transfer $\top_{\mathbb{L}} \stackrel{g}{\leadsto}_{\mathbb{L}} (\top_{\mathbb{L}}, \widetilde{\pi})$ for all $g \in G$.
- 4. The already mentioned merge operator merge^{sep}, defined as $\mathsf{merge}^{\mathsf{sep}}(l,l',\pi) = l'$ for all $l,l' \in \mathcal{L}$.

- 5. The already mentioned termination check stop^{sep}, defined as stop^{sep}(l, R, π) = $\exists l' \in R : l \sqsubseteq l'$.
- 6. The precision adjustment prec that does not change anything: $\widetilde{\text{prec}}(l, \pi, R) = (l, \pi)$.

3.2.3 Predicate CPA

A predicate is a boolean formula using linear-arithmetic expressions and equality with uninterpreted functions. The predicate CPA [BGS] [?] uses predicate abstraction [BPR01] to compute abstract states from a formula ϕ and a set π of predicates (the precision). Two different kinds of predicate abstraction exist: The cartesian predicate abstraction $(\phi)_C^{\pi}$ is the strongest conjunction of predicates from π that is implied by ϕ . The boolean predicate abstraction $(\phi)_B^{\pi}$ is the strongest boolean combination of predicates from π that is implied by ϕ . In this work, we will only look at cartesian predicate abstraction because of its greater simplicity. For a set $r \subseteq \pi$, φ_r denotes the conjunction of all predicates in r, with $\varphi_{\{\}} = true$.

The predicate CPA $\mathbb{P} = (D_{\mathbb{P}}, \Pi_{\mathbb{P}}, \leadsto_{\mathbb{P}}, \mathsf{merge}^{sep}, \mathsf{stop}^{sep}, \widetilde{\mathsf{prec}})$ consists of:

1. The abstract domain $D_{\mathbb{P}} = (C, \mathcal{P}, [\![\cdot]\!])$ with concrete states C, the semi-lattice \mathcal{P} and the concretization function $[\![\cdot]\!]$. The semi-lattice is defined by $\mathcal{P} = (2^P, \top_{\mathbb{P}}, \bot, \sqsubseteq, \sqcup)$. Each abstract state is a finite subset $r \in P$ of predicates, with P denoting the set of quantifier-free predicates over program variables X. An abstract state can be interpreted as the conjunction of all its predicates. $\top_{\mathbb{P}} = \emptyset$ is an abstract state without any constraints (true) and represents all possible concrete states. The bottom element $\bot = \{false\}$ represents no concrete state. A state r is less or equal another state r', if r contains all predicates of r', formally $r \sqsubseteq r'$ if $r \supseteq r'$. The join of two states r, r' is defined by $r \sqcup r' = r \cap r'$.

The concretization function $\llbracket \cdot \rrbracket$ is defined by $\llbracket r \rrbracket = \{ c \in C | c \vDash \varphi_r \}$.

- 2. The precision type $\Pi_{\mathbb{P}} = 2^{p}$ describes the precision of an abstract state as a set of predicates. If predicate $p \in P$ is in a precision π , p is tracked by the analysis when π is used.
- 3. The transfer relation $\leadsto_{\mathbb{P}}$ has the transfer $r \overset{g}{\leadsto_{\mathbb{P}}} (r', \pi)$ if $post(\varphi_r, g)$ is satisfiable and r' is the largest set of predicates from π so that $\varphi_r \Rightarrow pre(p, g)$ for each $p \in r'$. The operations $post(\varphi, g)$ and $pre(\varphi, g)$ describe the strongest post-condition

and the weakest pre-condition for a formula φ and an operation g. They are defined such that

$$[post(\varphi,g)] = \{c' \in C \mid \exists c \in C : c \stackrel{g}{\leadsto} c' \land c \vDash \varphi\}$$

and

$$[pre(\varphi,g)] = \{c \in C | \exists c' \in C : c \stackrel{g}{\leadsto} c' \land c' \models \varphi\}.$$

- 4. The already mentioned merge operator merge^{sep}, defined as $\mathsf{merge}^{\mathsf{sep}}(r,r',\pi) = r'$ for all $r,r' \in \mathcal{P}$.
- 5. The already mentioned termination check stop^{sep}, defined as stop^{sep}(r, R, π) = $\exists r' \in R : r \sqsubseteq r'$.
- 6. The precision adjustment prec that does not change anything: $\widetilde{\text{prec}}(r, \pi, R) = (r, \pi)$.

3.2.4 Value analysis CPA

The value analysis CPA [BL13] tracks integer values for all program variables explicitly. It does so by using abstract variable assignments [BL13]. An abstract variable assignment $v: X \rightarrow \mathbb{Z} \cup \{\bot\}$ is a partial function that maps program variables $x \in X$ to integer values - if their assignment is known - or to \bot , if no possible value assignment exists. An abstract variable assignment v is contradicting if $v(x) = \bot$ for any $x \in \text{def}(v)$. For two abstract variable assignments v and v', v is implied by v', that is $v' \Rightarrow v$, if v' is contradicting or if $\text{def}(v') \subseteq \text{def}(v)$ and for each variable $x \in \text{def}(v) \cap \text{def}(v') : v(x) = v'(x)$. The conjunction $v \wedge v'$ is defined as

$$(v \wedge v')(x) = \begin{cases} \bot & \text{if } x \in \text{def}(v) \cap \text{def}(v') \text{ and } v(x) \neq v'(x) \\ v(x) & \text{if } x \in \text{def}(v) \\ v'(x) & \text{if } x \in \text{def}(v') \end{cases}$$

We define the *definition range* of a partial function f as $def(f) = \{x | \exists y : (x,y) \in f\}$ and the *restriction* of a partial function f to a new definition range Y as $f_{|Y} = f \cap (Y \times (\mathcal{Z} \cup \{\bot\}))$.

The value analysis CPA $\mathbb{C} = (D_{\mathbb{C}}, \Pi_{\mathbb{C}}, \leadsto_{\mathbb{C}}, \mathsf{merge}^{\mathit{sep}}, \mathsf{stop}^{\mathit{sep}}, \widetilde{\mathsf{prec}})$ consists of the following components:

1. The abstract domain $D_{\mathbb{C}} = (C, \mathcal{V}, \llbracket \cdot \rrbracket)$ contains the set C of possible concrete states, the semi-lattice \mathcal{V} and the concretization function $\llbracket \cdot \rrbracket$. The semi-lattice $\mathcal{V} = (V, \top_{\mathbb{C}}, \bot, \sqsubseteq, \sqcup)$ consists of the set $V = X \Rightarrow \mathbb{Z}$ of abstract variable assignments, with X being the set of program variables and $\mathcal{Z} = \mathbb{Z} \cup \{\bot_{\mathcal{Z}}\}$ the set of integer values and the bottom element. The top element $\top_{\mathbb{C}}$ of the abstract domain is defined as $\top_{\mathbb{C}} = \emptyset$. It represents an unknown assignment for all program variables. The bottom element \bot is defined as $\bot(x) = \bot_{\mathcal{Z}}$ for all $x \in X$. It represents an impossible variable assignment, that is a state that cannot be reached in the program execution. The less-or-equal operator $\sqsubseteq \subseteq V \times V$ defines $v \sqsubseteq v'$ if $def(v') \subseteq def(v)$ and for all $x \in def(v') : v(x) = v'(x)$ or $v(x) = \bot_{\mathcal{Z}}$. This means that a state v is less or equal a state v' if v contains all variable assignments of v' or restricts them even further.

The join operator \sqcup defines the least upper bound of two abstract variable assignments, but is never used in our configuration.

The concretization function $\llbracket \cdot \rrbracket$ assigns to each abstract state v the concrete states it represents, $\llbracket v \rrbracket = \{c \in C \mid c(x) = v(x) \text{ for all } x \in \text{def}(v)\}$. If an abstract state v contains an impossible variable assignment, that is $v(x) = \bot_{\mathbb{Z}}$ for any $x \in \text{def}(v)$, then it represents no concrete state: $\llbracket v \rrbracket = \varnothing$.

- 2. The set of precisions $\Pi_{\mathbb{C}} = L \to 2^X$. A precision $\pi \in \Pi_{\mathbb{C}}$ specifies for each program location $l \in L$ a subset of program variables of X that are tracked at this location.
- 3. The transfer relation $\leadsto_{\mathbb{C}}$ has the transfer $v \overset{g}{\leadsto}_{\mathbb{C}} (v', \pi)$ if one of the following is true:
 - a) $g = (\cdot, assume(p), \cdot)$ and for all $x \in def(v')$:

$$v'(x) = \begin{cases} \bot_{\mathcal{Z}} & \text{if } \exists y \in X : v(y) = \bot_{\mathcal{Z}} \text{ or } p_{/v} \text{ unsatisfiable} \\ c & \text{if } c \text{ is the only satisfying assignment of } p_{/v} \text{ for } x \\ v(x) & \text{if none of the above and } x \in \text{def}(v) \end{cases}$$

 $p_{/v}$ is the interpretation of a predicate p using the known variable assignments of v, that is $p_{/v} = p \land \bigwedge_{x \in \text{def}(v), v(x) \in \mathbb{Z}} x = v(x) \land \neg \exists x \in \text{def}(v) : v(x) = \bot_{\mathcal{Z}}.$

b)
$$g = (\cdot, w := exp, \cdot)$$
 and

$$v'(x) = \begin{cases} exp_{/v} & \text{if } x = w \text{ and } exp_{/v} \neq \top_{\mathcal{Z}} \\ v(x) & \text{if } x \neq w \text{ and } x \in \text{def}(v) \end{cases}$$

 $exp_{/v}$ denotes the interpretation of an expression exp using the values of abstract variable assignment v, that is

$$exp_{/v} = \begin{cases} \bot_{\mathcal{Z}} & \text{if } \exists y : v(y) = \bot_{\mathcal{Z}} \\ \top_{\mathcal{Z}} & \text{if } y \not\in \text{def}(v) \text{ for some } y \in X \text{ that occurs in } exp \\ c & \text{otherwise, where expression } exp \text{ is evaluated to} \\ c & \text{after replacing each occurrence of variable } x \in \\ \text{def}(v) \text{ in } exp \text{ with } v(x) \end{cases}$$

with $\top_{\mathcal{I}}$ denoting an unknown value.

- 4. The merge operator merge^{sep}. That means that no merge is performed. This is the only aspect in which constant propagation CPA [BGS] and value analysis CPA differ.
- 5. The termination check stop^{sep}, which checks every abstract state individually.
- 6. The precision adjustment prec that does not change anything: $\widetilde{\text{prec}}(v, \pi, R) = (v, \pi)$. Since we only track the program location in the location CPA, a composite precision adjustment has to handle the correct adjustment of abstract states to precisions of $\Pi_{\mathbb{C}}$.

3.2.5 Symbolic value analysis CPA

The symbolic value analysis CPA (introduced in [Lem15] without dynamic precision adjustment and using a different less-or-equal operator) is an extension to the value analysis CPA. It introduces *symbolic values* to handle non-deterministic values and expressions of unknown value.

The symbolic value analysis CPA $\mathbb{C}_S = (D_{\mathbb{C}_S}, \Pi_{\mathbb{C}_S}, \leadsto_{\mathbb{C}_S}, \mathsf{merge}_{\mathbb{C}_S}, \mathsf{stop}^\mathit{sep}, \widetilde{\mathsf{prec}})$ consists of:

1. The abstract domain $D_{\mathbb{C}_S} = (C, \mathcal{E}, \llbracket \cdot \rrbracket)$ with the set C of concrete states, the semilattice \mathcal{E} and the concretization function $\llbracket \cdot \rrbracket$. The semi-lattice $\mathcal{E} = (V_{\mathbb{C}_S}, \top_{\mathbb{C}_S}, \bot, \sqsubseteq, \sqcup)$ is defined by the set of abstract symbolic value assignments $V_{\mathbb{C}_S} = X \oplus \mathcal{Z}_{\mathbb{C}_S}$

mapping program variables in its definition range to values of $\mathbb{Z}_{\mathbb{C}_S} = \mathbb{Z} \cup S \cup \{\bot_{\mathbb{Z}}\}$. The value range of an abstract variable assignment of the type $V_{\mathbb{C}_S}$ consists of the set \mathbb{Z} of concrete integer values, the set S of symbolic values and the bottom element $\bot_{\mathbb{Z}}$, which represents an impossible variable assignment. $S = S_I \cup S_E$ consists of *symbolic identifiers* S_I and *symbolic expressions* S_E . Each expression that contains at least one symbolic identifier is a symbolic expression. The definition range of an abstract variable assignment of $V_{\mathbb{C}_S}$ consists of all program variables whose value is known as either a concrete value (of \mathbb{Z}), a symbolic value (of S) or as invalid (\bot_Z). The top element $\top_{\mathbb{C}_S} = \emptyset$ represents no known assignment.

The less-or-equal operator is equal to the one of the value analysis CPA, but implicitly considers symbolic values: For two abstract states $v, v' \in V_{\mathbb{C}_S}$, v is less or equal to v', i.e. $v \sqsubseteq v'$, if $def(v') \subseteq def(v)$ and for all $x \in def(v') : v(x) = v'(x)$ or $v(x) = \bot_{\mathbb{Z}}$. Note that with this operator $v' \sqsubseteq v \Rightarrow \llbracket v' \rrbracket \subseteq \llbracket v \rrbracket$, but in general $\llbracket v' \rrbracket \subseteq \llbracket v \rrbracket \not\Rightarrow v' \sqsubseteq v$.

In our previous work [Lem15] we used a more complex less-or-equal operator, using the semantics of the value analysis CPA for concrete values only, and defining a new behaviour for symbolic values. We defined it as follows: $v \sqsubseteq' v'$, if all of the following conditions hold: (a) v' must only contain value assignments also present in v, that is $def(v') \subseteq def(v)$, (b) for every concrete or invalid assignment of v, v' must contain the same or a weaker one, that means

$$\forall x \in \text{def}(v') : v(x) \in \mathbb{Z} \cup \{\bot_{\mathcal{Z}}\} \Rightarrow v'(x) = v(x) \lor v(x) = \bot_{\mathcal{Z}}$$

and (c) a bijective function alias : $S_I \to S_I$ exists that maps each symbolic identifier of S_I to another symbolic identifier, so that $\forall x \in \text{def}(v') : v'(x) \in S \Rightarrow v(x)$ results from v'(x) by replacing all $i \in S_I$ occurring in v'(x) with alias(i).

This operator can result in wrong behaviour when used in conjunction with the constraints CPA, so we will not use it in this work. An example for its wrong behaviour will be presented in Section 4.2.

The join $\sqcup : V_{\mathbb{C}_S} \times V_{\mathbb{C}_S}$ is defined as

$$(v \sqcup v')(x) = \begin{cases} v(x) & \text{if } v(x) = v'(x) \\ \bot_{\mathcal{Z}} & \text{if } v(x) = \bot_{\mathcal{Z}} \text{ or } v'(x) = \bot_{\mathcal{Z}} \end{cases}$$

for all $x \in def(v \sqcup v')$.

- 2. The precision type $\Pi_{\mathbb{C}_S} = L \to 2^X$. Just like $\Pi_{\mathbb{C}}$, a precision $\pi \in \Pi$ contains for each program location all program variables of X that are tracked at this location.
- 3. The transfer relation $\leadsto_{\mathbb{C}_S}$ contains the transfer $v \overset{g}{\leadsto}_{\mathbb{C}_S} v''$, if one of the following conditions is true:

a)
$$g = (\cdot, assume(p), \cdot), p_{/v}$$
 is satisfiable and for all $x \in def(v'')$

$$v''(x) = \begin{cases} c & \text{if } c \text{ is the only satisfying assignment for } p_{/v} \text{ and } \\ x \not\in \operatorname{def}(v) \\ y & \text{if } x \not\in \operatorname{def}(v) \text{ and } x \text{ appears in } p. \ y \in S_I \text{ is a new symbolic values that has not been used in any other state before } \\ v(x) & \text{if none of the above and } x \in \operatorname{def}(v) \end{cases}$$

 $p_{/v}$ performs an over-approximation in this case, as variables with a symbolic assignment are not considered. Reminder:

$$p_{/v} = p \land \bigwedge_{x \in \text{def}(v), v(x) \in \mathbb{Z}} x = v(x) \land \neg \exists x \in \text{def}(v) : v(x) = \bot_{\mathcal{Z}}$$

b)
$$g = (\cdot, w := exp, \cdot)$$
 and

$$v''(x) = \begin{cases} exp_{/v'} & \text{if } x = w \\ v'(x) & \text{if } x \in \text{def}(v) \text{ and } x \neq w \end{cases}$$

with

$$v'(x) = \begin{cases} y & \text{if } x \not\in \text{def}(v) \text{ and } x \text{ appears in } exp. \ y \in S_I \text{ is a} \\ & \text{new symbolic identifier that has not been used in} \\ & \text{any other state before} \\ v(x) & \text{if } x \in \text{def}(v) \end{cases}$$

and $exp_{/v'}$ defined as before. If any symbolic value occurs in exp after replacing all occurences $x \in X$ in exp with v'(x), the expression is only partially evaluated. In this case $exp_{/v'} \in S$.

c)
$$v'' = \top_{\mathbb{C}_{\mathbb{S}}}$$
.

- 4. The use of $merge_{\mathbb{C}_S} = merge^{sep}$ means that no merge of abstract states is performed.
- 5. The termination check stop^{sep} already mentioned considers every state independently when checking for coverage.
- 6. The precision adjustment prec that does not change anything: $\widetilde{\operatorname{prec}}(v,\pi,R) = (v,\pi)$. We rely on a composite CPA to perform precision adjustment, as a location is needed.

3.2.6 Constraints CPA

The constraints CPA (introduced in [Lem15] without dynamic precision adjustment and with a less-or-equal operator using an alias function) tracks constraints (i.e. boolean formulas) on symbolic identifiers created by assume edges. For this, it relies on the values provided by the symbolic value analysis CPA to partially evaluate assume edges and create constraints out of them. The constraints CPA $\mathbb{A} = (D_{\mathbb{A}}, \Pi_{\mathbb{A}}, \leadsto_{\mathbb{A}}, \mathsf{merge}_{\mathbb{A}}, \mathsf{merge}_{\mathbb{A}}, \mathsf{prec})$ is defined by:

1. The abstract domain $D_{\mathbb{A}} = (C, \mathcal{A}, [\![\cdot]\!])$, which consists of concrete states C, the semi-lattice \mathcal{A} and the concretization function $[\![\cdot]\!]$.

The abstract states described by $\mathcal{A}=(2^{\gamma},\top_{\mathbb{A}},\bot,\sqsubseteq_{sub},\sqcup)$ are subsets of the set γ of all possible boolean expressions over the possible values of the symbolic value analysis CPA, $\mathcal{Z}_{\mathbb{C}_S}$, and program variables, X. This includes concrete and symbolic values. An abstract state $a\subseteq\gamma$ can be interpreted as the conjunction of all its constraints, where each symbolic identifier $i\in S_I$ is handled as a variable. The top element $\top_{\mathbb{A}}=\varnothing$ contains no constraints (it represents true). The bottom element \bot represents a program state that can never be reached. It represents false. \sqsubseteq_{sub} is defined in the following way: For two given states $a, a'\subseteq\gamma$, a is less or equal a', that is $a\sqsubseteq_{sub}a'$, if a contains all constraints of a', that is $a\supset a'$.

In [Lem15] we used a less-or-equal operator using an alias function like the symbolic value analysis CPA did, but we will not do so in this work because it does not always work with CEGAR for the same reasons as the symbolic value analysis CPA's aliasing less-or-equal operator. The join □ computes the least upper bound of two abstract states, but is never used.

The concretization function $[\cdot]$ maps an abstract state to all concrete states that satisfy its constraints:

$$[a] = \{c \in C | c \models \varphi_a\}$$

with φ_a denoting the conjunction of all predicates in a, $\varphi_a = \bigwedge_{v \in a} p$.

2. The set $\Pi_{\mathbb{A}} = L \to 2^{\gamma^+}$ of precisions. Each precision $\pi \in \Pi_{\mathbb{A}}$ contains for each program location $l \in L$ all tracked constraints, with $\gamma^+ \subseteq \gamma$ being the set of all possible boolean expressions over $\mathfrak{Z}_{\mathbb{C}_S}$. The constraints of γ^+ do not contain any program variables, but the only variables occurring in them are symbolic identifiers of S_I .

A second option is to use the precision $\Pi_{\mathbb{C}_S} = L \to 2^X$ of the symbolic value analysis CPA. A constraint p is tracked by $\pi \in \Pi_{\mathbb{C}_S}$ at a location l, if $\pi(l)$ contains all program variables p originated from.

Example: If p=s1>s2+5 with $s1,s2\in S_I$ was created from an edge assume(a>b) by using an abstract variable assignment $v=\{(a,s1),(b,s2+5)\}$ and $\pi(l)=\{a,b\}$, then π contains all program variables p originated from and it is tracked by π .

- 3. The transfer relation $\leadsto_{\mathbb{A}}$ contains the transfer $a \overset{g}{\leadsto}_{\mathbb{A}} a'$ if one of the following is true:
 - a) $g = (\cdot, \operatorname{assume}(p), \cdot)$, $a' = a \cup p$ and a does not contain any variable $x \in X$. We just add the condition of the assume as a new constraint to the abstract state. Since a may not contain any program variables we enforce that variables must always be replaced by concrete or symbolic values through strengthening before the next assume edge occurs. As two assume edges might follow each other, we even enforce immediate strengthening. Or,
 - b) $g = (\cdot, w := e, \cdot)$ and a' = a. Constraints CPA only cares about assume edges.
- 4. The merge operator $\mathsf{merge}_{\mathbb{A}} = \mathsf{merge}^{\mathsf{sep}}$. No merge is performed when the control flow meets. We will introduce an alternative merge operator later on.
- 5. The termination check $\operatorname{stop}^{sep}(e,R) = \exists e' \in R : e \sqsubseteq e' \text{ considers every reached abstract state individually.}$

6. The precision adjustment $\widetilde{\text{prec}}$ that does not change anything: $\widetilde{\text{prec}}(r,\pi,R) = (r,\pi)$. Since we only track the program location in the location CPA, a composite precision adjustment has to handle the correct adjustment of abstract states to precisions of $\Pi_{\mathbb{A}}$.

3.2.7 Symbolic execution CPA: Composition of Location CPA, Symbolic Value Analysis CPA and Constraints CPA

The symbolic execution CPA [Lem15] is the composition of location CPA, symbolic value analysis CPA and constraints CPA. Besides connecting the location CPA to the other CPAs so abstract states can be mapped to a program location, its most important task is the definition of a strengthening operator that creates new constraints in the constraints CPA and checks their satisfiability.

The symbolic execution CPA $\mathbb S$ is the composite CPA implied by the composition ($\mathbb L$, $\mathbb C_S$, $\mathbb A$, Π_S , \leadsto_S , merge_S, stop_S, prec_S). It consists of:

- 1. The three CPAs \mathbb{L} , \mathbb{C}_S and \mathbb{A} and their abstract domains defined above.
- 2. The precision type $\Pi_S = \Pi_{C_S} \times \Pi_{\mathbb{A}}$ that contains the individual precisions of the symbolic value analysis CPA and constraints CPA. We do not need the precision of the location CPA because it never changes.
- 3. The transfer relation \leadsto_S . It contains the transfer $(l, v, a) \overset{g}{\leadsto}_S (l', v', a', \pi)$ if $l \overset{g}{\leadsto}_{\mathbb{L}} (l', \widetilde{\pi}), v \overset{g}{\leadsto}_{\mathbb{C}_S} (v', \pi_{\mathbb{C}_S})$, if:

a)
$$g = (\cdot, \operatorname{assume}(p), \cdot)$$
 and $\downarrow_{\mathbb{A},\mathbb{C}_S} (a'', v') = a'$ is defined, with $a \overset{g}{\leadsto}_{\mathbb{A}} (a'', \pi_{\mathbb{A}})$, or

b)
$$g = (\cdot, w := e, \cdot)$$
 and $a \stackrel{g}{\leadsto} (a, \pi_{\mathbb{A}})$, and if $\pi = (\widetilde{\pi}, \pi_{\mathbb{C}_s}, \pi_{\mathbb{A}})$.

It uses the strengthening operator $\downarrow_{\mathbb{A},\mathbb{C}_S}$: $2^{\gamma} \times V_{\mathbb{C}_S} \to \gamma^+$ that strengthens an abstract state of the constraints CPA by using an abstract state of the symbolic value analysis CPA. $\downarrow_{\mathbb{A},\mathbb{C}_S} (a'',v) = a'$ is defined if the following conditions are true:

a) a' results from a'' by first replacing all program variables x occurring in the constraints of a'' by their abstract value assignment v(x) (denoted as $a''_{/v}$) and then removing all constraints that still contain program variables:

$$a' = a''_{v} \cap \gamma^+$$
.

- v(x) can be a concrete or symbolic value as well as $\perp_{\mathcal{Z}}$. We define a constraint containing $\perp_{\mathcal{Z}}$ as false, though, and as such, the strengthen operator is not defined if $\perp_{\mathcal{Z}}$ is occurs.
- b) $\varphi_{a'}$ is satisfiable.
- 4. The merge operator merge_S = merge^{agree} uses the merge operators of each CPA on the corresponding abstract states individually, if, after the merge, every component's state is less or equal the both previous states. Otherwise no merge is performed.
- 5. The stop operator stop_S uses the stop operators of each CPA on the corresponding abstract state and reached set individually. It only returns *true* if all of them return *true*.
- 6. The precision adjustment operator prec_S performs precision adjustment on the abstract state of the symbolic value analysis CPA and the constraints CPA. It uses the abstract state of the location CPA in both cases to get the tracked program variables/constraints for the current location.

$$\operatorname{prec}_{\mathbb{S}}(l,v,a,\pi_{\times},R) = (l,\operatorname{prec}_{\mathbb{C}_{\mathbb{S}}}(v,l,\pi_{\mathbb{C}_{\mathbb{S}}}),\operatorname{prec}_{\mathbb{A}}(a,l,\pi_{\mathbb{A}}),\pi_{\times})$$

with $\pi_{\times} = (\pi_{\mathbb{C}_S}, \pi_{\mathbb{A}})$. The precision adjustment of the symbolic value state removes the abstract variable assignments of all program variables that are not tracked by $\pi \in \Pi_{\mathbb{C}_S}$ at the current location.

$$\mathrm{prec}_{\mathbb{C}_{\mathbb{S}}}(v,l,\pi) = v_{|\pi(l)}.$$

The precision adjustment of the constraints state depends on the type of precision used for the constraints CPA: The adjustment $\operatorname{prec}_{\mathbb{A}}$ removes all constraints that are not tracked at the current location. Its concrete implementation depends on

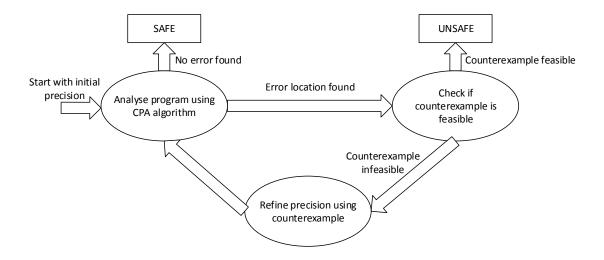


Figure 3.2: The general idea of CEGAR

the used precision type of the constraints CPA: If Π_A is used, which stores all tracked constraints explicitely, the adjustment is defined as

$$\operatorname{prec}_{\mathbb{A}}(a,l,\pi) = a \cap \pi(l).$$

If Π_{C_S} is used, which only stores the program variables constraints may originate from, the adjustment deletes all constraints that originate from at least one program variable that does not occur in $\pi(l)$ with π being the current precision.

3.3 Basic CEGAR and its algorithm

3.3.1 CEGAR and interpolation in general

Counterexample-guided abstraction refinement (CEGAR) [CGJ⁺03] is a technique to find an abstraction that contains as few information as possible while retaining the possibility to prove or disprove a program's correctness. This technique can greatly reduce the number of abstract states in a program's analysis and is considered "the most general and flexible for handling the state explosion problem," [CGJ⁺03] the major problem we are facing with our symbolic execution CPA.

The technique starts analysis with a coarse abstraction and refines it based on counterexamples. A counterexample is a witness of a property violation.[BL13] If no error path is found by the analysis, it terminates and reports that no property

Algorithm 2 $CPA(\mathbb{D}, R_0, W_0)$, adapted from [BL13]

Input: a CPA $\mathbb{D} = (D, \Pi, \leadsto)$, merge, stop, prec), a set $R_0 \subseteq (E \times \Pi)$ of initial states with their precision and a subset $W_0 \subseteq R_0$ of frontier abstract states with their precision, with E being the set of elements of D

Output: a set of abstract states reachable from R_0 with their precision and a subset of frontier abstract states with their precision

Variables: reached and waitlist, both subsets of $E \times \Pi$

```
1: reached := R_0
2: waitlist := W_0
3: while waitlist \neq \emptyset do
4: ...
```

violation exists. If an error path is found, it is checked whether the path is feasible (i.e. a possible program execution) by repeating the analysis with full precision. If the path is feasible, the analysis terminates and reports the found property violation. If the error path is infeasible it was only found because the abstraction is too coarse. As a consequence, the abstraction is refined using the error path. After this, the analysis starts again, using the new abstraction.

Since the problem of finding the coarsest possible refinement of an abstraction based on an error path is NP-hard, [CGJ⁺03] good heuristics have to be used to find good refinements. Interpolation [?] is one such technique in a boolean context that is used for refinement of both the predicate CPA and value analysis CPA.

3.3.2 CEGAR and interpolation in the context of configurable software verification

To apply CEGAR and interpolation to configurable software verification, a simple modification has to be made to the CPA algorithm. Instead of passing it an initial state e_0 and an initial precision π_0 , we use an initial reached set R_0 and initial waitlist W_0 (Alg. 2). This way we can control at which point the analysis continues after a refinement was performed.

Now that the CPA algorithm is able to use precisions created in a refinement procedure, we use it as a part of our complete CEGAR algorithm. Algorithm 3 uses a CPA using dynamic precision adjustment \mathbb{D} , an initial state e_0 and an initial precision π_0 to compute whether a property violation exists.

First, the *CPA* algorithm is used to compute a set of reached abstract states (reached) and a subset of this set that contains all reached abstract states that

Algorithm 3 *CEGAR*(\mathbb{D} , e_0 , π_0), adapted from [BL13]

Input: a CPA $\mathbb{D} = (D, \Pi, \leadsto, \mathsf{merge}, \mathsf{stop}, \mathsf{prec})$ with dynamic precision adjustment, an initial abstract state $e_0 \in E$ with precision $\pi_0 \in \Pi$, with E denoting the set of elements of the semi-lattice of D

Output: the verification result *safe* or *unsafe*

```
Variables: the sets reached and waitlist of elements of E \times \Pi, an error path
    \sigma = \langle (op_1, l_1), ..., (op_n, l_n) \rangle
 2: reached := \{(e_0, \pi_0)\}
 3: waitlist := \{e_0, \pi_0\}
 4: \pi := \pi_0
 5: while true do
        (reached, waitlist) := CPA(\mathbb{D}, reached, waitlist)
        if waitlist = \emptyset then return safe
 7:
 8:
            \sigma := \text{extractErrorPath(reached)}
 9:
10:
           if is Feasible (\sigma) then
                                                          ⊳ error path feasible: report bug
                return unsafe
11:
            else
                         ▶ error path infeasible: refine and restart from the beginning
12:
                \pi := \pi \cup \text{refine}(\sigma)
13:
                reached := (e_0, \pi)
14:
15:
                waitlist := (e_0, \pi)
```

have not been handled yet (waitlist). If waitlist is empty, the *CPA* algorithm has handled all reachable states without encountering any target state. If this is the case, no property violation was found and the algorithm can return safe. Otherwise, an error path is extracted from the reached set. If the error path is reported as feasible, a property violation exists or the algorithm is not able to prove that none exists. It returns unsafe. If the error path is infeasible, the current precision is too abstract. It is refined based on the infeasible error path by using $refine : \Sigma \to \Pi$ with Σ being the set of all error paths, so that it can prove its infeasibility. After this, the reached set and waitlist are reset to their initial values and the algorithm repeats analysis with the refined precision. It is important to notice that the return type of refine has to be equal to the precision type Π used in $\mathbb D$. Because of this, CPAs are not exchangeable without changing refinement, too, in general.

For refinement, the priorly mentioned technique of interpolation is used to determine a location-specific precision that is strong enough for the CPA algorithm with precision adjustment to prove that a given error path is infeasible. A boolean

formula ψ is a Craig interpolant [?] for two boolean formulas γ^- (called prefix) and γ^+ (called suffix), if the following three conditions are fulfilled:

- a) The prefix implies ψ , that is $\gamma^- \Rightarrow \psi$.
- b) ψ contradicts the suffix, that means $\psi \wedge \gamma^+$ is contradicting.
- c) ψ only contains variables occurring in *both* prefix and suffix.

It is proven that such an interpolant always exists in the domain of abstract variable assignments [BL13] as well as in the theory of linear arithmetics [?].

Our work is strongly based on the refinement technique for abstract variable assignments. The strongest-post operator SP_{op} describes the semantics of an operation $op \in Ops$. It is the analogy to the transfer relation in the domain of CPAs. It maps a region of concrete states, implied by an abstract variable assignment, to the region of all concrete states that can be reached by executing op. The semantics of a path $\sigma = \langle (l_1, op_1), ..., (l_n, op_n) \rangle$ is defined as the consecutive application of the strongest-post operator to its constraint sequence $\gamma_{\sigma} = \langle op_1, ..., op_n \rangle$: $\mathsf{SP}_{\gamma_{\sigma}}(v) = \mathsf{SP}_{op_n}(\mathsf{SP}_{op_{n-1}}(... \mathsf{SP}_{op_1}(v)...))$. We use strongest-post operators during interpolation and refinement to evaluate paths.

The strongest-post operator $\mathsf{SP}_{\mathit{op}}$ is defined in the following way: For an assignment operation s := exp, $\mathsf{SP}_{s := exp}(v) = v_{|X \setminus \{s\}} \land v_{s := exp}$ with $v_{s := exp} = \{(s, exp_{/v})\}$ and $exp_{/v}$ denoting the evaluation of exp using the abstract variable assignment v, as defined in Section 3.2.4. For an assume operation $\mathsf{assume}(p)$, $\mathsf{SP}_{\mathsf{assume}(p)}(v) = v'$ with

$$v'(x) = \begin{cases} \bot & \text{if } \exists y \in \text{def}(v) : v(y) = \bot \text{ or } p_{/v} \text{ is unsatisfiable} \\ c & \text{if } c \text{ is the only satisfying assignment of } p_{/v} \text{ for } x \\ v(x) & \text{if none of the above and } x \in \text{def}(v) \end{cases}$$

with $p_{/v}$ as defined in Section 3.2.4.

The algorithm for interpolation in the domain of abstract variable assignments is shown in Algorithm 4. For a prefix γ^- and a suffix γ^- , the abstract variable assignment v, that results from applying γ^- to the initial abstract variable assignment \varnothing is computed. Next, for each variable assignment in v it is checked whether the assignment is necessary to prove that γ^+ is contradicting. If it is not, it can be removed from v. After all variable assignments are checked, v only contains variable assignments that are necessary to prove that γ^+ is contradicting. From these, the interpolant is built (Lines 6 - 8).

Algorithm 4 interpolate(γ^-, γ^+), adapted from [BL13]

```
Input: two constraint sequences \gamma^- and \gamma^+, with \gamma^- \wedge \gamma^+ contradicting Output: a constraint sequence \Gamma, which is an interpolant for \gamma^- and \gamma^+ Variables: an abstract variable assignment v

1: v := \mathsf{SP}_{\gamma^-}(\varnothing)

2: \mathbf{for\ all}\ x \in \mathsf{def}(v)\ \mathbf{do}

3: \mathbf{if}\ \mathsf{SP}_{\gamma^+}(v_{|\mathsf{def}(v)\setminus\{x\}}) is contradicting \mathbf{then}

4: v := v_{|\mathsf{def}(v)\setminus\{x\}} \triangleright x not relevant, should not occur in interpolant

5: \Gamma := \langle \rangle

6: \mathbf{for\ all}\ x \in \mathsf{def}(v)\ \mathbf{do}

7: \Gamma := \Gamma \wedge \langle \mathsf{assume}(x = v(x)) \rangle

8: \mathbf{return}\ \Gamma
```

Algorithm 5 refine(σ), adapted from [BLW15]

```
Input: infeasible error path \sigma = \langle (op_1, l_1), ..., (op_n, l_n) \rangle
Output: precision \pi
Variables: interpolating constraint sequence \Gamma

1: \Gamma := \langle \rangle
2: \pi(l) := \emptyset for all program locations l
3: for i := 1 to n-1 do
4: \gamma^+ := \langle op_{i+1}, ..., op_n \rangle
5: \Gamma := \text{interpolate}(\Gamma \wedge \langle op_i \rangle, \gamma^+) \triangleright inductive interpolation 6: \pi(l_i) := \text{extractPrecision}(\Gamma)
7: return \pi
```

The interpolants produced are used in the refinement of the precision (Alg. 5). We use a location-specific precision $\pi:L\to 2^X$ that returns for a program location $l\in L$ all program variables of X which are relevant for the analysis at this location. This approach realizes the lazy abstraction technique [?]. The algorithm starts with an initial, empty interpolant Γ and empty precision π with $\pi(l)=\varnothing$ for all $l\in L$. For each location (l_i,op_i) on the error path, the suffix γ^+ of this location are set and the interpolant is computed inductively from the existing interpolant in conjunction with the current operation op_i and the suffix (Line 5). A precision for the current program location is then extracted from the interpolant. One straightforward way to do this is by using all program variables with a valid assignment in the abstract variable assignment resulting from the application of the strongest-post operator to our interpolant:

$$\operatorname{extractPrecision}(\Gamma) = \{x | (x, z) \in \operatorname{SP}_{\Gamma}(\varnothing) \text{ and } z \neq \bot_{\mathcal{Z}} \}.$$

It is not only sufficient, but also required to use $\Gamma \wedge \langle op_i \rangle$ instead of the full prefix $\gamma^- = \langle op_1, ..., op_1 \rangle$ for interpolation. The full prefix cannot be used as it has to be assured that the precision resulting from these consecutive interpolations proves the error path infeasible. All information necessary for proving the infeasibility of the remaining error path is present in the current interpolant and operation.

This refinement procedure can be used in CEGAR (Alg. 3) in combination with a CPA with precision adjustment that expects these precision types, like the value analysis CPA in combination with refinement for abstract variable assignments.

Refinement in the domain of linear arithmetics, as used for the predicate CPA, uses a standard approach to refinement based on lazy abstraction and Craig interpolation. The task of interpolation is delegated to an off-the-shelf SMT solver.

In this chapter, we gave an overview of all theoretical concepts that are necessary to describe our own work. We introduced the concept of configurable software verification and configurable program analyses (CPAs), a very versatile approach to automated software verification. We introduced different CPAs we use in this work and CEGAR with precision refinement for both linear arithmetics and abstract variable assignments, which we will use when applying CEGAR to the symbolic execution CPA.

4 Definitions of newly introduced concepts

To increase the performance of the symbolic execution CPA, multiple approaches are designed and evaluated. Symbolic execution suffers from two major issues: Path explosion due to its high precision and the bad performance of SAT checks. Since we use off-the-shelf SMT solvers for checking satisfiability we can not influence the performance of SAT checks. Instead almost all of our approaches focus on decreasing the state space.

We will first look at some optimizations to the existing symbolic execution CPA without using CEGAR before adapting this algorithm.

4.1 A different merge operator for constraints CPA

For every operation assume(p) at a location l that transfers the control flow to a location l' there exists another operation $assume(\neg p)$ at the same location transfering the control flow to a location $l'' \neq l'$. In most programs it is probable that the two different program branches starting at l' and l'' meet again, that means that for a later program location l''' two abstract states a, a' of the constraints CPA (in the following called *constraints states*) exist with a containing p and a' containing p.

If a constraint p is part of an abstract state a, p is true in all concrete states represented by a (just like a predicate in an abstract state of the predicate CPA [?]). If for one program location l two constraints states a, a' exist with $p \in a$ and $\neg p \in a'$ and $a \setminus \{p\} = a' \setminus \{\neg p\}$, then a represents all concrete states for which $p \land a \setminus \{p\}$ is true and a' represents all concrete states for which $\neg p \land a \setminus \{p\}$ is true. At this point, the analysis will never be able to prove a program location as infeasible because of p or $\neg p$. If a' reaches a program location and computes it as infeasible by using p, the abstract state a will compute the same program location as feasible, if it reaches it. Because of this, it seems legit to delete these obsolete constraints and only continue

with one more abstract state instead of two more concrete ones by using the merge operator

$$\mathsf{merge}(a, a', \pi) = \begin{cases} a' \setminus \neg Q & \text{if } a \sqsubseteq a' \setminus \neg Q \\ a' & \text{otherwise} \end{cases}$$

with $\neg Q = \{ \neg p \mid p \in a \land \neg p \in a' \}$ and $Q = \{ p \mid p \in a \land \neg p \in a' \}$. It is not necessary that $a' \setminus \neg Q = a \setminus Q$. If $a' \setminus \neg Q$ represents a super set of the concrete states represented by $a \setminus Q$, that is $a \setminus Q \sqsubseteq a' \setminus \neg Q$, then the above condition is true, and $a \sqsubseteq a \setminus Q$.

This condition is automatically checked by the merge^{agree} operator, so we can simply use $merge(a, a', \pi) = a' \setminus \neg Q$. Unfortunately, we can't use this merge operator for the constraints CPA in combination with CEGAR, as our design of CEGAR does not consider merges. Only $merge^{sep}$ is possible.

4.2 Different less-or-equal operators

The less-or-equal operator is the operator executed the most often during analyses as stop^{sep} uses it once for every state in the reached set, at every iteration of the CPA algorithm. In addition, it is responsible for determining whether a new state is already covered and analysis can be stopped at this point. Although the implementation framework CPACHECKER only performs a termination check for reached states at the same location, its speed and precision can make a great difference for the performance of our analysis.

The less-or-equal operators we used for symbolic value analysis CPA and constraints CPA in [Lem15] using an alias function try to be more precise than a simple subset check. Unfortunately, they can result in false behaviour because of their independent behaviour. Consider the two pairs of value state and constraint state e = (v, a) with $v = \{x \to s1, y \to s2\}$, $a = \{s1 > 0\}$) and e' = (v', a') with $v' = \{x \to s2, y \to s1\}$, $a' = \{s1 > 0\}$). When using the aliasing less-or-equal operators of the symbolic value analysis CPA and of the constraints CPA, the symbolic value analysis CPA states $v \sqsubseteq v'$ for alias function alias (s1) = s2, alias (s2) = s1 and the constraints CPA states $a \sqsubseteq a'$ for alias (s1) = s1. Because of this, $e \sqsubseteq e'$, although the concrete states $[e] = \{c \in C \mid c(x) > 0\}$ and $[e'] = \{c \in C \mid c(y) > 0\}$ represented by e and e' are two different sets. This violates the definition of the less-or-equal operator for abstract domains (Section 3.1.2). For this example, the less-or-equal operator of the constraints CPA actually behaves like

the subset operator, since alias represents the identity. This shows that the less-or-equal operator of the symbolic value analysis CPA cannot be used, regardless of the operator used by the constraints CPA. Besides the default less-or-equal operator for the constraints CPA presented in Section 3.2.6, another operator might prove useful.

Since a constraints CPA's abstract state a is interpreted as the conjunction of its constraints φ_a , it seems fit to use implication as the less-or-equal operator. Remember that $\llbracket a \rrbracket = \{c \in C \mid c \models \varphi_a\}$. If a formula φ_a implies a formula $\varphi_{a'}$ and c satisfies φ_a , then c also satisfies $\varphi_{a'}$. Because of this

$$\llbracket a \rrbracket = \{ c \in C \mid c \vDash \varphi_a \} \subseteq \{ c \in C \mid c \vDash \varphi_{a'} \} = \llbracket a' \rrbracket \text{ if } \varphi_a \Rightarrow \varphi_{a'}.$$

The less-or-equal operator for the constraints CPA using implication is defined as $a \sqsubseteq_{impl} a'$ if $\varphi_a \Rightarrow \varphi_{a'}$. This operator has a higher precision than \sqsubseteq_{sub} but requires SAT checks, which are definitely worse in performance than merely checking whether one set is the subset of another.

4.3 CEGAR for Symbolic Value Analysis + ConstraintsCPA

For using the symbolic execution CPA with CEGAR, we have to define a refinement procedure that returns a precision of type Π_S that fits the precision of the symbolic execution CPA. We designed two such refinement procedures: The first uses adjusted versions of the refinement and interpolation algorithms as used for abstract variable assignments, with an adjusted strongest-post operator and a precision type that fits Π_S . The second refinement procedure extracts a precision for the symbolic execution CPA from the precision created by the refinement of the predicate CPA, which is based on interpolation in the domain of linear arithmetics.

4.3.1 Refinement based on refinement for abstract variable assignments

We adjust the refinement algorithm for abstract variable assignments (Alg. 5). The feasibility check of an error path σ is performed by executing the symbolic execution CPA with full precision for all program locations l. If the error location of σ is reached by the analysis, the path is feasible. It is infeasible, otherwise.

We use a strongest-post operator that reflects the semantics of our symbolic execution CPA by defining a composite operator $SP^S_{op}: V_{C_S} \times 2^{\gamma^+} \to V_{C_S} \times 2^{\gamma^+}$. It is the composition of the transfer relations of the symbolic value analysis CPA and the constraints CPA, as well as the strengthen operator \downarrow_{A,C_S} to create useful constraints states. The result of SP^S_{op} is contradicting if \downarrow_{A,C_S} is not defined (that means that the conjunction of constraints are contradicting) or the transfer relation of the symbolic value analysis CPA produces a contradicting abstract variable assignment. Formally:

$$\mathsf{SP^S}_{op}(v,a) = \begin{cases} (v',a'') & \text{if } v \overset{g}{\leadsto}_{\mathbb{C}_S} v', a \overset{g}{\leadsto}_{\mathbb{A}} a', \downarrow_{\mathbb{A},\mathbb{C}_S} (a',v') \text{ is defined with} \\ \downarrow_{\mathbb{A},\mathbb{C}_S} (a',v') = a'' \text{ and } g = (\cdot,op,\cdot) \\ \bot & \text{otherwise} \end{cases}$$

The contradiction \bot represents the bottom element for both the symbolic value analysis CPA as well as for the constraints CPA. Both the transfer relation of the symbolic value analysis CPA \leadsto_{C_S} and the transfer relation of the constraints CPA $\leadsto_{\mathbb{A}}$ always produce only one successor, so we can use them in our definition while keeping $\mathsf{SP}^S_{\mathit{op}}$ unambigious. The performance of this strongest-post operator can be significantly worse than when only using abstract variable assignments since SAT checks have to be performed in the strengthen operation. Because of this, the amount of calls of the strongest-post operator can make a notable difference in performance.

Using this strongest-post operator for interpolation (Alg. 6) allows the computation of an interpolant for a prefix γ^- and a suffix γ^+ at a specific program location based on the semantics of the symbolic execution CPA. Since we want to create an interpolant Γ that contains all information necessary for proving that $\mathsf{SP}^{\mathsf{S}}_{\Gamma \wedge \gamma^+}$ is contradicting, not only abstract variable assignments but also constraints have to be considered. First, we compute the strongest-post condition (v, a) for the prefix γ^- . Similar to interpolation for abstract variable assignments, we then eliminate all constraints from a that are not necessary for proving that γ^+ is contradicting. Next, we remove all assignments from v that are not required. This way we try to get the weakest interpolant possible. We then build the interpolant from all left constraints in a and all left assignments of v. Contrary to Algorithm 6, we build the interpolant Γ not by using assume operations for each $x \in def(v)$, but assignment operations. By using assume operations only for the constraints of a we can easily distinguish between both domains. It is not wrong to only use assume operations, but it would unnecessarily increase the precision for the constraints CPA and the constraints sets used in the inductive interpolations, as all assumptions are added here. This could

Algorithm 6 interpolates (γ^-, γ^+) , a modified version of Alg. 4

```
Input: two constraint sequences \gamma^- and \gamma^+, with \gamma^- \wedge \gamma^+ contradicting
Output: a constraint sequence \Gamma, which is an interpolant for \gamma^- and \gamma^+
Variables: an abstract variable assignment v and a constraints state a
 1: (v, a) := \mathsf{SP}^{\mathbb{S}}_{\gamma^{-}}(\varnothing)
 2: for all p \in a do
          if SP^{\mathbb{S}}_{\gamma^+}(v, a \setminus \{p\}) is contradicting then
 3:
               a := a \setminus \{p\}
                                                   ▷ p not relevant, should not occur in interpolant
 4:
 5: for all x \in def(v) do
          if SP^{S}_{\gamma^{+}}(v_{|\text{def}(v)\setminus\{x\}}, a) is contradicting then
               v := v_{|\text{def}(v) \setminus \{x\}}
                                                   ▷ x not relevant, should not occur in interpolant
 7:
 8: \Gamma := \langle \rangle
 9: for all p \in a do
          \Gamma := \Gamma \wedge \langle assume(p) \rangle
11: for all x \in \text{def}(v) do
          \Gamma := \Gamma \wedge \langle x := v(x) \rangle
12:
13: return \Gamma
```

Algorithm 7 refines(σ), a modified version of Alg. 5.

```
Input: infeasible error path \sigma = \langle (op_1, l_1), ..., (op_n, l_n) \rangle
Output: precision (\pi_{\mathbb{C}_S}, \pi_{\mathbb{A}}) \in \Pi_S
Variables: interpolating constraint sequence \Gamma

1: \Gamma := \langle \rangle
2: (\pi_{\mathbb{C}_S}(l), \pi_{\mathbb{A}}(l)) := (\varnothing, \varnothing) for all program locations l
3: for i := 1 to n-1 do
4: \gamma^+ := \langle op_{i+1}, ..., op_n \rangle
5: \Gamma := \text{interpolate}_S(\Gamma \wedge \langle op_i \rangle, \gamma^+) \triangleright inductive interpolation 6: (\pi_{\mathbb{C}_S}(l_i), \pi_{\mathbb{A}}(l_i)) := \text{extractPrecision}_S(\Gamma)
7: return (\pi_{\mathbb{C}_S}, \pi_{\mathbb{A}})
```

significantly decrease performance during interpolation due to the for-loop over all constraints in Line 9 and the strongest-post operators bad performance. It might even be more effective to just use all constraints and not perform these additional strongest-post computations by omitting Lines 2 - 4 of the algorithm. Additionally, it is also possible to eliminate variable assignments first, and constraints second. We will examine all three possibilities later in detail.

After interpolation is done, a precision of type Π_S must be extracted from this interpolant so that future executions of the CPA algorithm with the symbolic execution CPA can prove the examined error path as infeasible. To get a precision

that consists of the two individual precisions of symbolic value analysis CPA and constraints CPA from an interpolant Γ , we define the extractPrecision function as the composition of two new functions, each of which extracts the precision for one of these CPAs based on Γ :

$$\texttt{extractPrecision}_S(\Gamma) = (\texttt{extractPrecision}_{\mathbb{C}_S}(\Gamma), \texttt{extractPrecision}_{\mathbb{A}}(\Gamma))$$
 with

extractPrecision_{Cs}(
$$\Gamma$$
) = { $x | (x,z) \in v, z \neq \bot_{\mathbb{Z}} \text{ and SP}^{\mathbb{S}}_{\Gamma}(\varnothing) = (v,a)$ }

and

extractPrecision_A(
$$\Gamma$$
) = a with $SP^{S}_{\Gamma}(\emptyset) = (v, a)$

if the constraints CPA uses the precision $L o 2^{\gamma^+}$, or

extractPrecision $\mathbb{A}(\Gamma) = \{x \mid \exists p \in a : p \text{ originates from an expression with } x\}$

if the constraints CPA uses the precision $L \to 2^X$. The symbolic value analysis CPA is based on the value analysis CPA, so it also works with the default refinement procedure for abstract variable assignments described in Section 3.3.2. We simply use the existing extractPrecision for this CPA. The precision of the constraints CPA is the set of all tracked constraints, so the constraints that result from applying the strongest-post operator to the interpolant provide the precision needed at the current location for proving the current error path as infeasible in future analysis. The adjusted refinement procedure is shown in Algorithm 7.

4.3.2 Refinement based on refinement for predicate CPA

Another possible way of refinement is to delegate the procedure to the refinement of the predicate CPA and extract a precision of type Π_S from the created predicate precision. This might not always work as the predicate CPA is able to handle more complex operations than the symbolic execution CPA, for example non-deterministic arrays. Even if an error path σ is infeasible by using the symbolic execution CPA with full precision, the interpolant (and resulting precision) computed by the predicate CPA's refinement could rely on unsupported operations.

Algorithm 8 refine $_{\mathbb{S}}'(\sigma)$

```
Input: infeasible error path \sigma = \langle (op_1, l_1), ..., (op_n, l_n) \rangle
Output: precision (\pi_{\mathbb{C}_S}, \pi_{\mathbb{A}}) \in \Pi_{\mathbb{S}} = \Pi_{\mathbb{C}_S} \times \Pi_{\mathbb{C}_S}
Variables: predicate precision \pi' \in \Pi_{\mathbb{P}} = 2^P

1: (\pi_{\mathbb{C}_S}(l), \pi_{\mathbb{A}}(l)) := (\varnothing, \varnothing) for all program locations l

2: \pi' = \text{refine}_{\mathbb{P}}(\gamma)

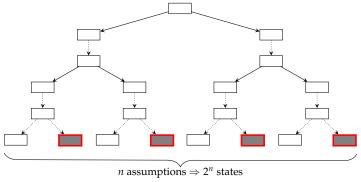
3: \pi_{\mathbb{C}_S}(l) := \text{extractPrecision}_{\mathbb{S}}'(\pi') for all l

4: return (\pi_{\mathbb{C}_S}, \pi_{\mathbb{C}_S})
```

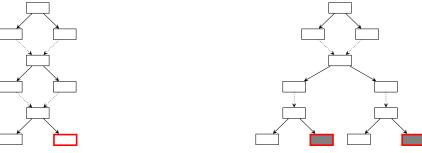
Two other drawbacks exist due to the predicate CPA's precision type: In the current implementation of CPACHECKER, we are not able to create constraints out of the predicates of the precision created by the predicate CPA's refinement, but can only extract program variables' names. Due to this it is not possible to use the precision type $\Pi_{\mathbb{A}} = L \to 2^{\gamma^+}$ as part of $\Pi_{\mathbb{S}}$, but only $\Pi_{\mathbb{C}_{\mathbb{S}}}$ (see Section 3.2.6). Secondly, the precision type $\Pi_{\mathbb{P}} = 2^P$ of the predicate CPA is not location-based per definition. Because of this, we have to assign the same set of tracked program variables for each location. This is not a problem in the implementation though, because a more detailed adjustment of the predicate CPA is possible there.

Despite these problems this approach might still yield better performance then refines. Though both have to rely on SMT solvers, our own procedure performs one SAT check for every constraint, while the predicate CPA's refinement utilizes a SMT solver to handle the complete interpolation process, which is presumably more performant due to its specialization. Algorithm 8 shows the refinement procedure delegating to predicate CPA's refinement. After computing the precision π' of the predicate CPA, the variables used in the predicates of π' are extracted and assigned as the precision $\pi_{C_S}(l)$ for every location l. The precision π_{C_S} is then returned as precision for both symbolic value analysis CPA and constraints CPA.

Figure 4.1 shows the benefit of using CEGAR with the symbolic execution CPA. The figure shows how analysis without CEGAR creates a lot of abstract states with information not necessary for proving that, increasing exponentially with the amount of assumptions in the program. In contrast, analysis with CEGAR consists of two iterations: While the target state is reachable in the analysis with empty precision, it is infeasible in the analysis with refined precision. Both runs themselves need far lesser abstract states than analysis without CEGAR, as only necessary information is tracked.



(a) Analysis without CEGAR



- (b) Analysis with CEGAR, first iteration: Empty precision
- (c) Analysis with CEGAR, second iteration: Refined precision

Figure 4.1: Symbolic execution analysis of a program with and without CEGAR. The red rectangles are abstract states at an infeasible target location. If they are filled grey, the analysis sees them as infeasible.

5 Implementations of used CPAs in CPAchecker

After defining the theoretical background of our work, deviations of the implementation from the theory and optimizations are documented next.

5.1 Basic implementation

We implemented our algorithm in the framework for configurable program verification CPACHECKER[BK11]. CPACHECKER is a command-line tool that is able to handle C programs without recursive function calls or multi-threading. It parses a C program, creates a CFA representing the program, and executes the CPA or CEGAR algorithm on it. The CPAs and, in the case of using CEGAR, the refinement procedure to use have to be defined in a configuration file that can be specified on the command line using the parameter <code>-config <FILE></code>. The specification to check must be defined as a temporal logic formula and is represented by an own CPA. This CPA represents the <code>isTargetState</code> function of the CPA algorithm.

A wide array of CPA implementations already exists, which we can utilize. The CPA interface equals the theoretical definition, but is extended with an initial state and initial precision, which are used as initial parameters of the CPA algorithm. The interface of the transfer relation is extended to include strengthening for the designated CPA, as almost always a composite CPA is used. Instead of defining one strengthen operator for each combination of two states, as done in theory ,the strengthen method gets all states of the current composite state, so that it can choose which to use for strengthening its own state.

One implementation for the composite CPA exists, called CompositeCPA. It allows arbitrary composition of CPAs by using their transfer relations, strengthen operators, the merge-agree operator merge^{agree}, a stop operator that only returns *true* if all subordinate stop operators return *true*, and by delegating the precision adjustment

to each individual CPA's precision adjustment operator not only with the precision to adjust to, but also *all* abstract states of the composite state, not only the one of the CPA delegated to. We used this CPA to create our symbolic execution CPA.

The way the precision adjustment of CompositeCPA works, it is possible to implement a precision adjustment function directly for a CPA whose precision uses location-specific tracking, like Π_{C_S} . As such we implemented the two auxiliary precision functions $\operatorname{prec}_{C_S}$ and prec_A as the precision adjustment of the symbolic value analysis CPA and constraints CPA. By just specifying the wanted CPAs as components of the composite CPA in a configuration file we composed the symbolic execution CPA. We use the existing location CPA without any modifications. It was already implemented in CPACHECKER.

Both the symbolic value analysis CPA, a direct extension of the existing value analysis CPA, and the constraints CPA, a completely new CPA, were mostly used as they were implemented in our work for [Lem15]. The constraints CPA transfer relation's complete syntax is in the strengthening by the symbolic value analysis CPA to forgo the need for constraints made of program variables which are then instantly replaced with constraints made of symbolic values. This way, a constraints state always only contains constraints over explicit and symbolic values. This means that all constraints are of γ^+ .

SAT checks over the constraints of a constraints state are performed by creating a conjunction of boolean formulas, each of which represents one constraint of the state, with symbolic identifiers being transformed to variables in the formulas. This conjunction is then given to a SMT solver.

To be able to handle conditions like "each constraint that originates from program variable x" (for example as used when using the precision type Π_{C_S} for the constraints CPA, see Section 3.2.6 and 4.3.1), we also store for each symbolic value that is assigned to an variable the variable in the symbolic value. This way it is always possible to transform a constraint of γ^+ back to its original representation.

5.2 Existing options/optimizations

To use symbolic values in the value analysis CPA, the configuration option cpa.value.symbolic has to be set. Since structures and arrays in C may have a lot of entries, which might not even be important for the analysis, it can prove useful not to track them at all, if

their assignments are non-deterministic. This increases the probability of coverage of a state, for example

results in two different abstract states when the control flow meets and b < 1 is checked twice, if non-deterministic structure assignments are tracked. If they are not tracked, analysis will terminate for one abstract state when the control flow meets and unnecessary computation is avoided. The options <code>cpa.value.symbolic.handleArrays</code> and <code>cpa.value.symbolic.handleStructs</code> can be used to disable the tracking of non-deterministic assignments to structs and arrays. This will only disable the tracking of non-deterministic assignments of the type struct or arrays, i.e. assignments to members of structs and array elements are always tracked, despite the value of those two options. When analysing

```
int[] b = new int[5];
b[0] = __VERIFIER_nondet_int();
```

with the symbolic execution CPA, *b* is always tracked. Arrays of unknown length are never tracked, though.

Multiple optimizations are applied to the constraints CPA implementation in CPAchecker. First, we use the SMT solver to create a model (a mapping of variables to concrete values) that satisfies the conjunction *F* of all constraints of a state. This model is used to compute all *definite assignments*, i.e. the variables for which only one valid assignment exists.

For each variable assignment s = n of the model, we check whether n is the only assignment for variable s that satisfies F by checking whether $F \land s \neq n$ is unsatisfiable. If it is, s = n is necessary for the formula to be true and we can store it as a definite assignment. (Alg. 9)

In addition, we only store constraints that contain at least one symbolic identifier. If a constraint does not contain a symbolic identifier, we call it *trivial*. Its representing

Algorithm 9 GetDefiniteAssignments(*F*, *M*)

```
Input: A boolean formula F and a model M = S_I \to \mathbb{Z} that satisfies F Output: A map D \subseteq M of definite assignments for all (s,n) \in M do

if F \land s \neq n is unsatisfiable then
D := D \cup (s,n)
return D
```

boolean formula then does not contain any variables and it can be checked whether it is satisifiable or not independently of all other constraints, as it can't influence any symbolic identifier's possible concrete values. If the constraint is unsatisfiable, the path using this assumption is infeasible and no valid transfer to a new state exists. Otherwise, the old state is used without adding the trivial constraint. In our basic implementation, if a constraint that already is in the current abstract state becomes trivial because all symbolic identifiers occurring in it have definite assignments, the constraint was removed, too, while the definite assignments were preserved. This was done for the same reason as not adding trivial constraints in the first place, but resulted in more complex code, as the definite assignments had to be considered every time a constraints state was examined. For simplicity and less error-prone code, this is changed in our current implementation (see Section 5.3).

As a third optimization, we periodically delete all constraints that do not hold any information, directly or indirectly, about a variable with symbolic value. This is the case for a constraint if none of the symbolic identifiers it contains are present in the current value analysis state and if either none of them occur in another constraint that holds information about a program variable with symbolic value, or if at least one of them only occurs in this one constraint and does not have a definite value. By doing this, we reduce the number of constraints to the minimum without losing any information and accelerate the SAT checks at every assumption edge due to fewer variables in boolean formulas.

Last, we do not create boolean formulas for each constraint every time we want to perform a SAT check, but store them and only create formulas for constraints for which none exist yet. This way we have to synchronize the set of constraints with an additional set of formulas, but save lots of redundant object creations.

We perform strenghtening of the value analysis CPA by using constraints states. If an abstract assignment of a symbolic identifier with a definite assignment to a program variable exists in the value analysis state, the symbolic identifier is replaced by the definite assignment's value. This way we reduce the number of existing

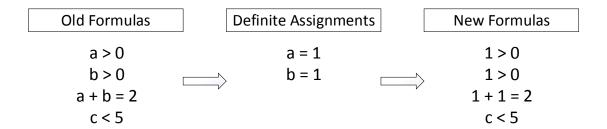


Figure 5.1: Illustration of the simplification of a constraints state's formulas by replacing variables with definite assignments

symbolic identifiers to the necessary minimum and create constraints with fewer symbolic identifiers.

5.3 New options/optimizations

As already mentioned, we do not remove constraints that only consist of symbolic identifiers with definite assignments, anymore. Instead, we update stored boolean formulas after computing new definite assignments by replacing all formulas that contain a variable with a new definite assignment with a new version using this definite assignment. This has two advantages: First, we do not have to consider definite assignments when examining constraints states, because all information concerning symbolic identifiers is present in a state's constraints. Second, we still get the performance benefit of minimizing the amount of symbolic identifiers we wanted to achieve by deleting trivial constraints, as the formulas representing the constraints use the definite assignments. As a consequence, a boolean formula representing a trivial constraint does not contain any variable and its satisfiability can be checked easily. Figure 5.1 illustrates this procedure using some example formulas. After checking that the set of formulas (in the figure called "old formulas") is satisfiable, new definite assignments are computed. For a > 0, b > 0 and a + b = 2, a and b both have to be 1 to fullfil the conjunction of these formulas. Variable c can be any value below 5, so it does not have a definite assignment. Using these two new definite assignments, all three formulas a and b occur in are replaced with their new versions.

By using configuration option cpa.constraints.mergeType = SEP or JOIN, either merge sep , as used in [Lem15], or the new merge operator merge as defined in Section 4.1 can be used in the constraints CPA.

For choosing the less-or-equal operator to use with the constraints CPA, the property cpa.constraints.lessOrEqualType with possible values SUBSET, ALIASED_SUBSET and IMPLICATION exists. Each less-or-equal operator behaves as described in Section 4.2.

6 Implementation of CEGAR

For applying CEGAR to the symbolic execution CPA, the CEGAR algorithm implementation already present in CPACHECKER is used. To use it, the configuration option analysis.algorithm.CEGAR = true has to be set. In addition, the refinement procedure has to be set with property cegar.refiner. Its value has to be the name of a class containing a method

public static Refiner create (ConfigurableProgramAnalysis)

which is called before starting the CEGAR algorithm to get the refinement procedure. The class name has to be given with its package description starting at org.sosy_lab.cpachecker. If the class to use were org.sosy_lab.cpachecker.cpa.val the option cegar.refiner = cpa.value.refiner.ValueAnalysisRefiner would have to be set.

In CPACHECKER, refinement for multiple CPA's precisions is already implemented. Since our precision refinement for the symbolic execution CPA based on abstract variable assignments (Section 4.3.1) is very similar to the refinement of the value analysis CPA, we refactored it to be able to reuse most code.

6.1 Refactoring of ValueAnalysis CEGAR into general form

The refinement procedures for value analysis CPA and symbolic execution CPA differ in the following ways, in theory:

- 1. Feasibility check isFeasible for error paths. The feasibility check of the value analysis refinement uses the value analysis CPA with full precision, the check of the symbolic execution refinement uses the symbolic execution CPA with full precision.
- 2. Precision type. Symbolic execution CPA's precision is a pair of the symbolic value analysis CPA's precision, which is the same as the value analysis CPA's

one, and the constraints CPA's precision. This changes the expected return type of the extractPrecision function and the refine algorithm. Since the CEGAR implemention in CPACHECKERexpects the refinement procedure to also update the precision in the CPAs, a different extractPrecision function and a different precision update procedure are needed.

3. Interpolation algorithm with strongest-post operator and structure of the produced interpolant. Symbolic execution uses an interpolation algorithm with another behaviour and a different structure of the returned interpolant. This has to be considered in the extractPrecision, also.

Keeping these points in mind, we first take a look at the old structure of the value analysis CPA's refinement implementation.

6.1.1 Structure of Value analysis CPA Refinement

Figure 6.1 shows the structure of the default refinement procedure for the value analysis CPA. The class ValueAnalysisRefiner is acting as interface for the refinement procedure. A deviation from the CEGAR algorithm (Alg. 3) is that the refinement procedure does not get the error path extracted from the reached set, but the reached set itself. The refinement procedure is responsible for extracting the error path, checking whether it is feasible, updating the precision if it is not and resetting the reached set and waitlist. (Lines 9 – 15 in Alg. 3).

Resetting the reached set and waitlist to their initial values after every refinement results in the CPA algorithm starting at the first state, always. Most of the time, this is not actually necessary though, because precision only changed for a few program locations. Because of this, CPACHECKERprovides the option to resume the CPA algorithm not at the beginning of the CFA, but at the first location that has to be revisited with its new precision so that the current error path is computed as infeasible. This location l_{i-1} is the one before the first pair (op_i, l_i) whose corresponding interpolant is not the empty constraint sequence (i.e. the location before the first location with a new precision). The states of all locations reachable from l_{i-1} are then removed from the reached set and added to the waitlist. This restart strategy is called pivot. The option cpa.value.refinement.restartStrategy = PIVOT activates this behaviour instead of a full reset.[?]

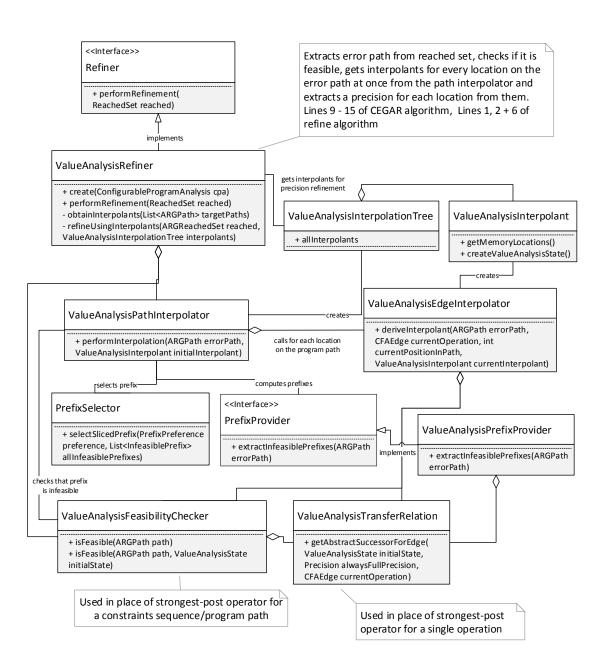


Figure 6.1: Structure of value analysis CPA refinement before refactoring

Independent of the way the reached set and waitlist are reset, the precision is updated by getting the existing precisions of all locations removed from the reached set and joining them with the newly extracted precision.

A deviation from the refine algorithm (Alg. 5) is that the interpolants for all program locations on the error path are created by the ValueAnalysisPath—Interpolator in one go, stored as a ValueAnalysisInterpolationTree. It basically represents Lines 3 – 5 of the refine algorithm, while ValueAnalysis—EdgeInterpolator is used for concrete interpolation. The interpolation algorithm (Alg. 4) gets a prefix and a suffix as parameters, with the prefix being combined from the interpolant computed for the last location and the current location's operation. It then applies the strongest-post operator to the complete prefix with an initial abstract variable assignment to get the abstract variable assignment for the current location. In the implementation, ValueAnalysisEdgeInterpolator receives the interpolant and the current operation in form of a CFA edge, separately. It is possible to recreate a ValueAnalysisState from the interpolant class ValueAnalysisInterpolant, so the strongest-post operator only has to be applyed to the current operation with the reconstructed state as initial one. This way, no redundant computations happen.

ValueAnalysisEdgeInterpolator uses the ValueAnalysisTransferRelation with full precision as strongest-post operator SP for single operations, as it represents the same semantics. The ValueAnalysisState also used in the abstract domain of the value analysis CPA is used to represent abstract variable assignments. A program variable is represented as a MemoryLocation. The class Value—AnalysisFeasibilityChecker is used for the feasibility check isFeasible (Alg. 3, Line 10). Since it applies the ValueAnalysisTransferRelation also representing SP sequentially to a program path to check whether it is feasible, it is also used as the sequential application of the strongest-post operator on a program path by the ValueAnalysisEdgeInterpolator. It is not necessary to transform program paths to constraints sequences, as the transfer relation can work on their edges directly.

The interface PrefixProvider, its implementation ValueAnalysisPrefix-Provider and the classPrefixSelector are used for the selection of an infeasible path prefix of the error path. Interpolation is then applied to this prefix only instead of the whole path, to have better control of the interpolants produced and the interpolation process itself. This concept was introduced in [BLW15] and will be used by us without modification. The algorithm for determining infeasible prefixes

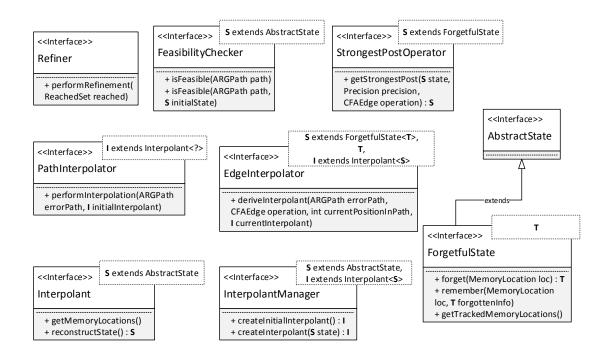


Figure 6.2: Interfaces used in refinement

also uses the strongest-post operator. In the implementation, ValueAnalysis-PrefixProvider uses the ValueAnalysisTransferRelation for this, as all others do. In addition, the infeasibility of a chosen prefix is checked again by using the ValueAnalysisFeasibilityChecker since it is possible to be feasible due to imprecision when structs or arrays occur.

6.1.2 Introduction of interfaces

Except for ValueAnalysisRefiner and ValueAnalysisPrefixProvider, none of these classes are accessed through an interface. So first, we created interfaces with type parameters that represent all components required for refinement in the domain of abstract variable assignments, based on the existing implementation of the value analysis refinement. These interfaces are displayed in Figure 6.2 next to the Refiner interface. FeasibilityChecker, PathInterpolator, Edge—Interpolator, Interpolant are based on their counterpart in value analysis refinement. The interface StrongestPostOperator provides the functionality of the strongest-post operator in the refinement algorithms. Its previous counterpart in value analysis refinement is the ValueAnalysisTransferRelation that was

used without an interface. InterpolantManager is an interface providing a previously not needed functionality: To be able to use an interface for interpolants, this interface is introduced to assume creating them at one point only, through injecting an interpolant manager in other classes of refinement. ForgetfulState is the interface for states used and created by the StrongestPostOperator and used by the EdgeInterpolator. It provides means for checking whether an element of the state is needed during interpolation and readding it, if it is.

Its type parameter T describes the type forgotten information is stored in. The method ForgetfulState.forget (MemoryLocation) returns the forgotten information as this type and the type is used to remember the forgotten information, if necessary. Another type parameter we use is S, which represents the ForgetfulState implementation used. Interpolant and Feasibility-Checker don't need the additional functionality this interface provides, so we just use its super-type AbstractState for these. The third and last type parameter, I, describes the concrete Interpolant implementation used. The use of a type parameter describing a concrete implementation instead of just using Interpolant<S> everywhere allows implementing classes to use methods specific to certain implementations.

6.1.3 Creation of generic refinement classes based on refactoring of value analysis refinement

After introducing above interfaces, we create new generic refinement classes implementing these interfaces for the domain of abstract variable assignments by using and refactoring most of the code of the existing value analysis refinement. The resulting structure can be seen in the UML diagram of Figure 6.3. All aggregation relationships represent dependency injection through the constructor of the classes. All parts of the refinement procedure are easily interchangeable. GenericRefiner is an abstract class. By implementing the method refineUsingInterpolants (ARGReachedSet, InterpolationTree) that is expected to use an interpolation tree to update the precision and reset the reached and waitlist sets represented by the type ARGReachedSet, subtypes can represent a complete refinement procedure. It is possible to reuse all of the shown classes. One only has to implement an Interpolant, a ForgetfulState, an InterpolantManager managing this interpolant type and supporting the state type, and a StrongestPostOperator using the state type. These types then have to be

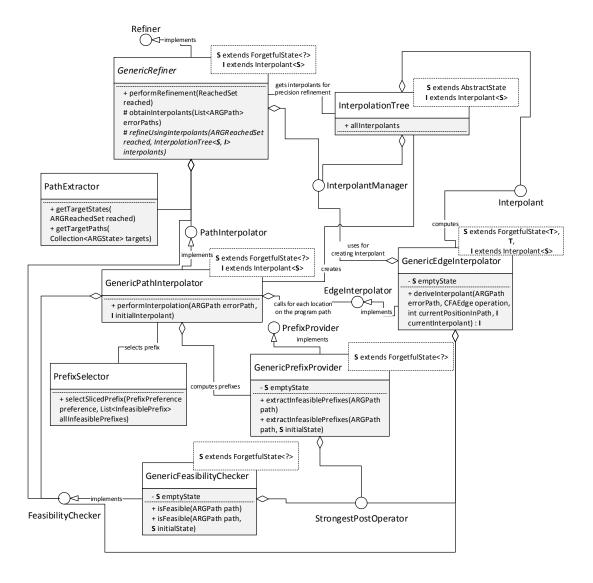


Figure 6.3: Structure of generic refinement procedure for abstract variable assignments

injected either through the constructor of the Generic* classes, or by choosing the correct type parameter.

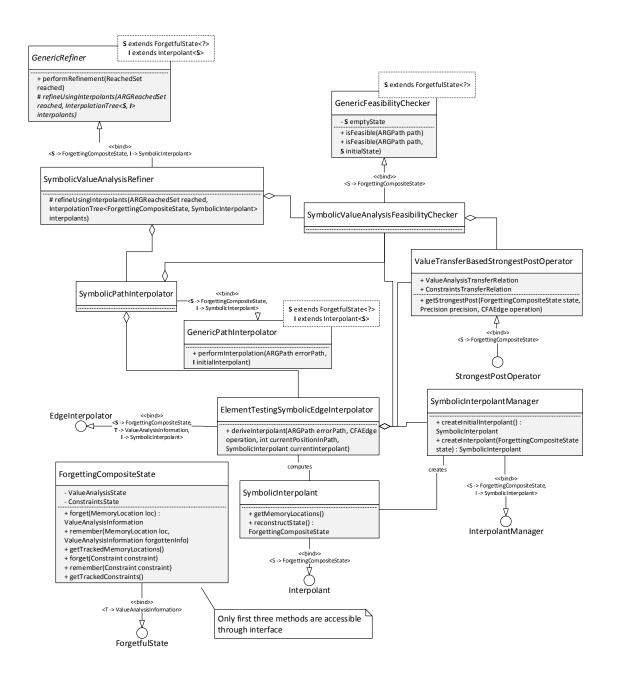


Figure 6.4: Structure of refinement procedure for symbolic execution

6.2 Refinement of Symbolic Value Analysis + ConstraintsCPA

Refinement of the symbolic value analysis CPA and constraints CPA is strongly based on these generic implementations. Besides Interpolant, ForgetfulState, InterpolantManager and StrongestPostOperator, we only create an own

implementation of EdgeInterpolator. For all other components, we inherit the behaviour of the generic implementations. Figure 6.4 shows the structure of this refinement.

SymbolicInterpolant implements Interpolant. It stores information about abstract variable assignments and constraints, so it can be used for interpolating over both these types. ForgettingCompositeState implements ForgetfulState. It is the composition of ValueAnalysisState and ConstraintsState and provides methods for forgetting/remembering both their elements separately. This is necessary for interpolation, described below. SymbolicInterpolantManager is an InterpolantManager able to create SymbolicInterpolants. Value—TransferBasedStrongestPostOperator is the implementation of the composite strongest-post operator using the value analysis transfer relation and constraints transfer relation, described in Section 4.3.1.

SymbolicEdgeInterpolator implements EdgeInterpolator. We can't use the functionality of GenericEdgeInterpolator since, depending on the configuration, we have to interpolate testing both constraints and/or variable assignments for their necessity. The generic edge interpolator only tests program variables (MemoryLocations), though.

6.2.1 Optimization: Perform basic Value Analysis refinement first

The strongest-post operator SP^S of symbolic execution refinement performs a SAT check at every assume operation, just like the symbolic execution CPA's transfer relation. To minimize these expensive computations, we refine using the semantics of value analysis's strongest-post operator SP only, if possible. To do this, we can't just use value analysis refinement because of the wrong interpolant type of Γ and the different precision type. If value analysis refinement was to update the precision by taking the current precisions of the locations whose states were removed from the reached set after successful interpolation and combining them with the newly computed precision, it would only consider the precision of the value analysis CPA and discard the existing precision of the constraints CPA. So instead, we built a new refinement procedure with a strongest-post operator representing the semantics of SP (see Section 4.3.1), generic refinement classes for interpolation and feasibility check using this strongest-post operator and ForgettingCompositeState, as

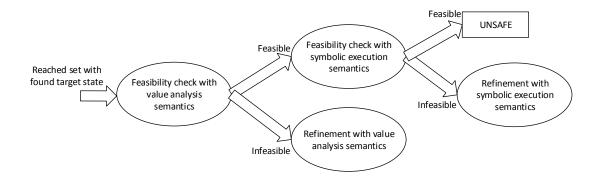


Figure 6.5: Symbolic execution refinement procedure. Before using constraints, try to prove infeasibility with value analysis semantics only

well as SymbolicValueAnalysisRefiner, which considers the precisions of both value analysis CPA and constraints CPA.

When refining, we first call this procedure. If it is able to prove the error path as infeasible, we use its refined precision. If it can't, we use our refinement procedure for symbolic execution to get a new precision. This way we only use SAT checks in refinement and only increase the precision of the expensive constraints CPA if this is really necessary for computing an error path as infeasible.

It is possible to choose the precision type to use for the constraints CPA with configuration property cpa.constraints.refinement.precisionType. Possible values are CONSTRAINTS and LOCATION for the corresponding types.

6.2.2 Extract precision from predicate refinement

For using the refinement procedure extracting a precision from the predicate precision created by predicate CPA's refinement, predicate CPA's refinement is just executed and all program variables are extracted from the predicates of the resulting precision for each location. These program variables are then used for the precision of the value analysis CPA, while the locations are used as precision for the constraints CPA, since it is not possible to derive an original assume statement from the predicates created by predicate CPA's refinement. Since the predicate CPAis more powerful than the symbolic execution CPA, it is possible that a refined precision is returned that is not sufficient for the symbolic execution CPA to prove the infeasibility of the error path. Because of this it is not always possible to use this alternative.

7 Evaluation

7.1 Evaluation setup

We performed each run of our benchmarks on a dedicated, unloaded server with a Intel Xeon E7-4870 with 2.40 GHz and 10 real and 20 virtual CPU cores, using the Linux operating system Ubuntu 14.04 for the x86_64 architecture. The resource limits for each run were 15.00 GB (13.97 GiB) of memory (RAM), a Java heap limit of 10.49 GB (10000 MiB = 9.767 GiB), a maximum use of two CPU cores, a CPU time limit of 900 seconds (which equals 15 minutes), after which CPACHECKER is supposed to shut down, and a hard time limit after which the run is killed of 1200 seconds (which equals 20 minutes). We chose a relatively big difference between the time limit in CPACHECKER and the hard time limit to give the analysis enough time to shut down in case of a long taking SAT check during analysis. As SAT checks are delegated to an SMT solver, CPACHECKER is unable to shut down during a check.

We took a subset of the benchmark repository¹ of the SV-COMP 2015 verification tasks for our benchmarks. A detailed explanation of all verification tasks present there can be found at [Bey15a]. We used:

- 1. BitVectors. Requires treatment of bit-operations, which allows us to check the need for and performance of a bitvector-based theory for SMT solving.
- 2. Floats. Requires handling of floats, which allows us to check the need for and performance of a float-based theory for SMT solving. An alternative is to just use rationals as approximations, whose use increases performance of SMT solving in comparison to floats.
- 3. ControlFlowInteger, ECA, Loops, ProductLines and Simple. All five of these sets are designed for testing the control flow and integer variable handling of analyses. Path explosion should occur here a lot.

¹ https://svn.sosy-lab.org/software/sv-benchmarks/tags/svcomp15

- 4. DeviceDrivers. This set of tasks consists of problems that require analysis of pointer aliases and function pointers. Since the value analysis CPA can't handle pointers, we expect the symbolic execution CPA to perform poorly, also. This set uses a simple memory model and the 64-bit architecture, the only task category we use that does so.
- 5. HeapManipulation. This set of tasks also consists of problems that require analysis of pointer aliases and function pointers, as well as data structures on the heap. In contrast to the set DeviceDrivers, this set uses a precise memory model and a 32-bit architecture.
- 6. Sequentialized. Different tasks derived from SystemC programs. SystemC provides means to simulate concurrent processes. Such programs were transformed to pure C programs so they can be analyzed by CPACHECKER.

A simple memory model denotes that variables can only be modified using direct assignments or by using a pointer which was obtained by using & on the corresponding variable. A precise memory model denotes that all memory cells can be written to, even by dereferencing uninitialized pointers.[?,?,?,?,?] We told CPACHECKER whether to assume a 32-bit or 64-bit architecture with the command-line parameters -32 (actually the default) and -64.

The external method ___VERIFIER_nondet_X() is used to introduce non-deterministic values of type X in a program. Although CPACHECKER is able to handle recursive function calls using block-abstraction memoization (BAM) [BF15], we do not use this feature, but skip such calls to keep our analysis focused on the performance of the symbolic execution CPA and its comparisons. To skip recursive function calls, we use the command-line parameter <code>-skipRecursion</code>.

All verification tasks are batch executed using a benchmark script that performs all runs with above specifications and which returns for each run one of the following results:

- TRUE, if the program is safe, i.e. the specification holds
- FALSE, if the program is potentially unsafe, i.e. the analysis found a specification violation and can't prove that the specification holds due to this
- UNKNOWN, if the result is unknown, due to an error, a crash, or exhausted resources (e.g. time or memory)

For each such result, the script assigns a number of points depending on the received and the expected result, and presents the sum of these points as a general indicator for the performance of the used program/analyses. The points assigned are:

Points	Result
0	UNKNOWN
+1	FALSE, correct
-6	FALSE, incorrect (false alarm)
+2	TRUE, correct
-12	TRUE, incorrect (unsound analysis)

This point scale rewards correct results, but punishes wrong ones stronger, especially unsoundness, the worst property a verifier can have. The script rewards correctly found bugs with less points then proving that a specification holds, since the latter is more complicated. In addition, the script does not check whether the error found by the analysis is an actual specification violation or just a lucky coincidence due to a too high level of abstraction, when a correct FALSE is returned. That means that simple analyses like the value analysis CPA, which don't track much information, can get points for finding a potential bug that not really is one in case another real bug exists. Keeping these points in mind, the resulting score for an analysis can give a fair general overview of its performance.

7.2 Evaluation on symbolic value analysis without CEGAR

- 7.2.1 Different merge operators
- 7.2.2 Different less-or-equal operators
- 7.2.3 SAT checks at different locations
- 7.3 Evaluation of symbolic value analysis with CEGAR
- 7.3.1 Refine only value analysis
- 7.3.2 Refine first value analysis, then constraints
- 7.3.3 Refine first constraints, then value analysis
- 7.4 Changed impact of above mentioned options when using CEGAR
- 7.5 Comparison of Symbolic Value Analysis with CEGAR and without CEGAR
- 7.6 Problems CEGAR can't solve/newly creates
- 7.6.1 Many variables needed for proof can decrease performance
- 7.6.2 Problem of long/endless loops and path explosion because of these can't be solved
- 7.7 Comparison with basic Value Analysis and Predicate Analysis

8 Future work

9 Conclusion

Bibliography

- [AGT08] Saswat Anand, Patrice Godefroid, and Nikolai Tillmann. *Demand-Driven Compositional Symbolic Execution*. Proceedings of the 14th International Conference, TACAS, pages 367–381, 2008.
- [Bey13] Dirk Beyer. Second Competition on Software Verification (Summary of SV-COMP 2013). Proc. TACAS, volume 7795 of LNCS, pages 594–609. Springer, 2013.
- [Bey14] Dirk Beyer. *Status Report on Software Verification (Competition Summary SV-COMP 2014)*. Proc. TACAS, volume 8413 of *LNCS*, pages 373–388. Springer, 2014.
- [Bey15a] Dirk Beyer. Competition on Software Verification (SV-COMP) 2015: Benchmark Verification Tasks, 2015. Online available at http://sv-comp.sosy-lab.org/2015/benchmarks.php.
- [Bey15b] Dirk Beyer. Software Verification and Verifiable Witnesses (Report on SV-COMP 2015). Proc. TACAS, LNCS. Springer, 2015. (To appear).
- [BF15] Dirk Beyer and Karlheinz Friedberger. *BAM Interprocedural: Domain-Independent Verification of Recursive Programs using Block-Abstraction Memoization*. Proc. FMAC, 2015. To be released.
- [BGS] Dirk Beyer, Sumit Gulwani, and David A Schmidt. *Combining Model Checking and Data-Flow Analysis*. volume pages 1–50.
- [BHT07] Dirk Beyer, Thomas a. Henzinger, and Grégory Théoduloz. *Configurable Software Verification: Concretizing the Convergence of Model Checking and Program Analysis*. Computer Aided Verification, volume 4590, pages 504–518. 2007.

- [BK11] Dirk Beyer and M. Erkan Keremoglu. *CPAchecker: A tool for configurable software verification*. Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics), volume 6806 LNCS, pages 184–190. 2011.
- [BL13] Dirk Beyer and Stefan Löwe. *Explicit-state software model checking based on CEGAR and interpolation*. Proceedings of the 16th International Conference on Fundamental Approaches to Software Engineering (FASE), pages 146–162, 2013.
- [BLW15] Dirk Beyer, Stefan Löwe, and Philipp Wendler. Sliced Path Prefixes. 2015.
- [BPR01] Tom Ball, Andreas Podelski, and Sriram K Rajamani. *Boolean and Cartesian abstractions for model checking C programs*. Proceedings of the 7th International Conference, TACAS, pages 268–283, 2001.
- [BW12] Dirk Beyer and Philipp Wendler. *Algorithms for Software Model Checking: Predicate Abstraction vs. IMPACT*. Proceedings of the 12th International Conference on Formal Methods in Computer-Aided Design (FMCAD), pages 106–113, 2012.
- [CGJ⁺03] Edmund Clarke, Orna Grumberg, Somesh Jha, Yuan Lu, and Helmut Veith. *Counterexample-guided abstraction refinement for symbolic model checking*. Journal of the ACM, volume 50, number 5, pages 752–794. 2003.
- [Lem15] Thomas Lemberger. *Symbolic Execution in CPAchecker*. Technical report, Chair of Software Systems, University of Passau, Passau, 2015.
- [MSB11] Glenford J. Myers, Corey Sandler, and Tom Badgett. *The Art of Software Testing*. John Wiley & Sons, 3rd edition, 2011.

Eidesstattliche Erklärung

Hiermit versichere ich, dass ich diese Bachelorarbeit selbstständig und ohne Benutzung anderer als der angegebenen Quellen und Hilfsmittel angefertigt habe und alle Ausführungen, die wörtlich oder sinngemäß übernommen wurden, als solche gekennzeichnet sind, sowie dass ich die Bachelorarbeit in gleicher oder ähnlicher Form noch keiner anderen Prüfungsbehörde vorgelegt habe.

Passau, den 3. Juli 2015	
Thomas Lemberger	