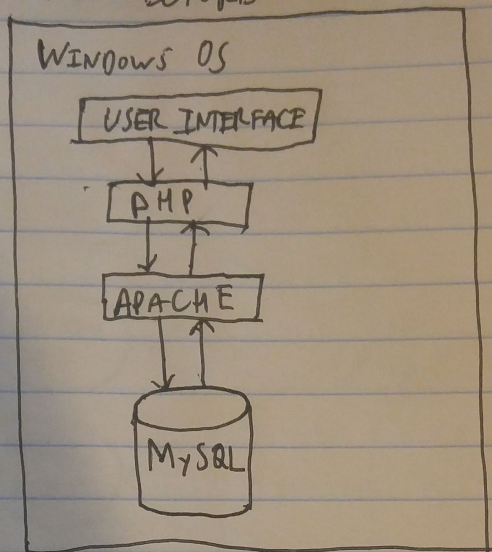


SYSTEM ARCHITECTURE DIAGRAM



DATA FLOW DIAGRAM

