

– Rev2.0

The 10Kohm pull-up resistor of CLK_REQ_N is required to 3.3V Suspend/Core/etc. power well, depending on the power well of PCH's input PCIECLKRQx# buffer. See Platform Design Guide for more details.

- * Place TVS devices close to the LAN connector.
- * Do not pair MDI0 and MDI1 on the same TVS device (avoid LAN POE connecting issue). Other pairing combination is ok.

