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X550-AT2 REFERENCE DESIGN

DUAL 10G/1000/100 BASE-T

PCIE GEN3 X4

INTEL CONFIDENTIAL

NETWORK DIVISION
2111 N.E. 25th AVENUE
HILLSBORO, OR 97124

TITLE	X550-AT2 REFERENCE DESIGN
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SIZE
B

CODE

DOCUMENT NUMBER

333901-001

REV
1.9

DATE	10/05/15
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SHEET
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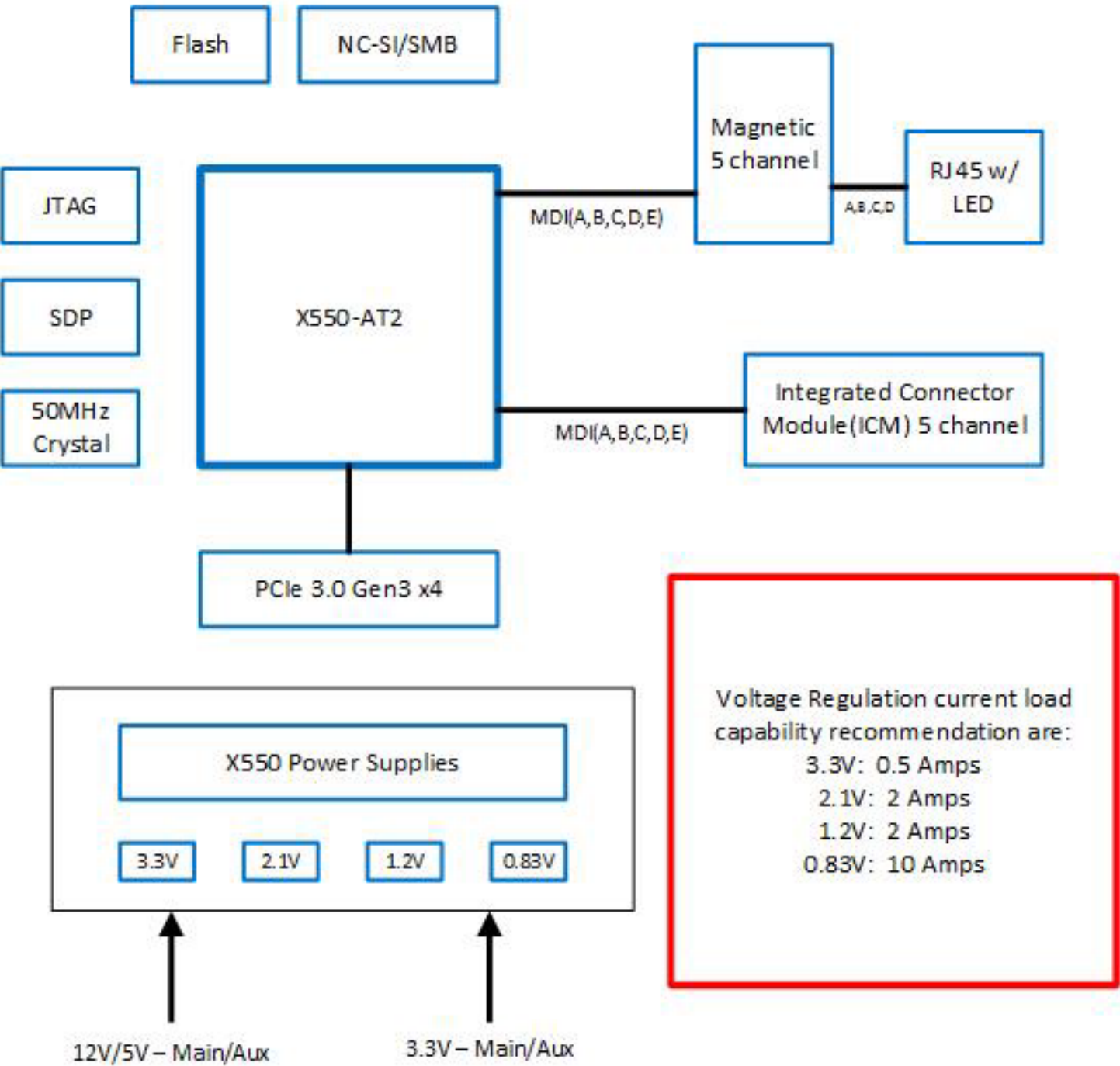
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NETWORK DIVISION 2111 N.E. 25th AVENUE HILLSBORO, OR 97124		TITLE X550-AT2 REFERENCE DESIGN				SIZE B	CODE	DOCUMENT NUMBER 333901-001				REV 1.9	DATE 10/05/15		SHEET 2	
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BLOCK DIAGRAM

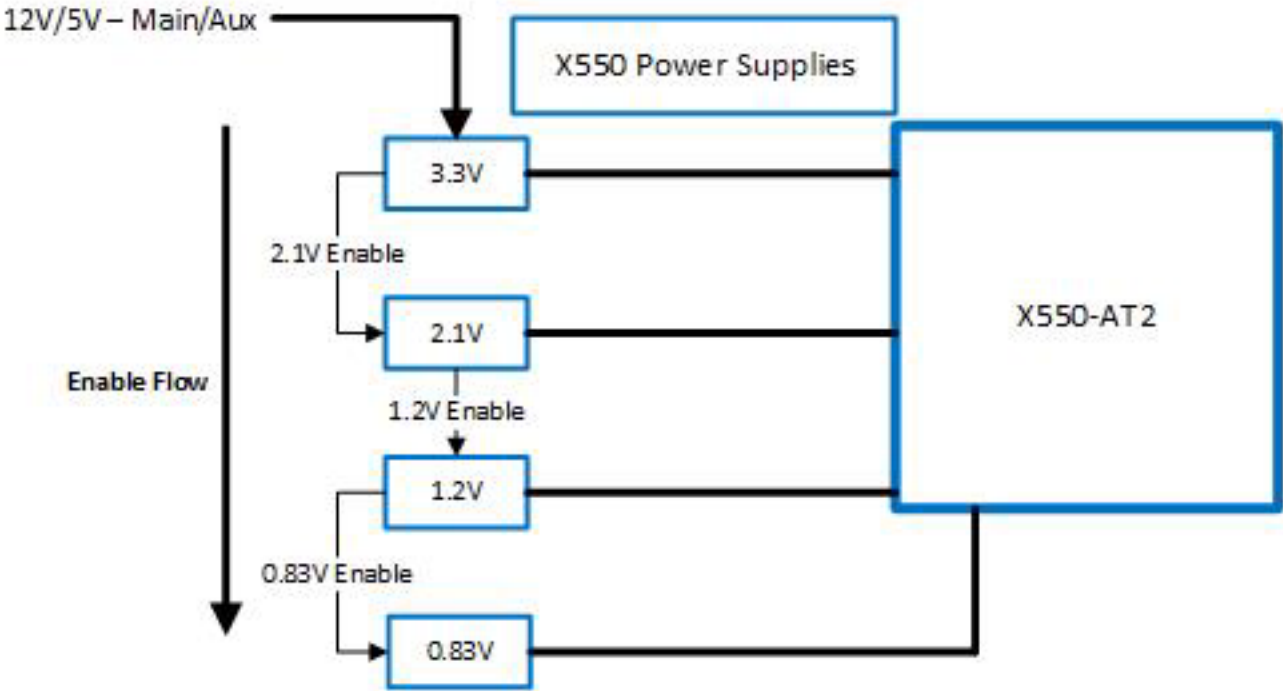
X550-AT2 Block Diagram



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POWER SUPPLY BLOCK DIAGRAM

X550-AT2 Supply Diagram



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NETWORK DIVISION
2111 N.E. 25th AVENUE
HILLSBORO, OR 97124

TITLE X550-AT2 REFERENCE DESIGN

SIZE
B

CODE

DOCUMENT NUMBER

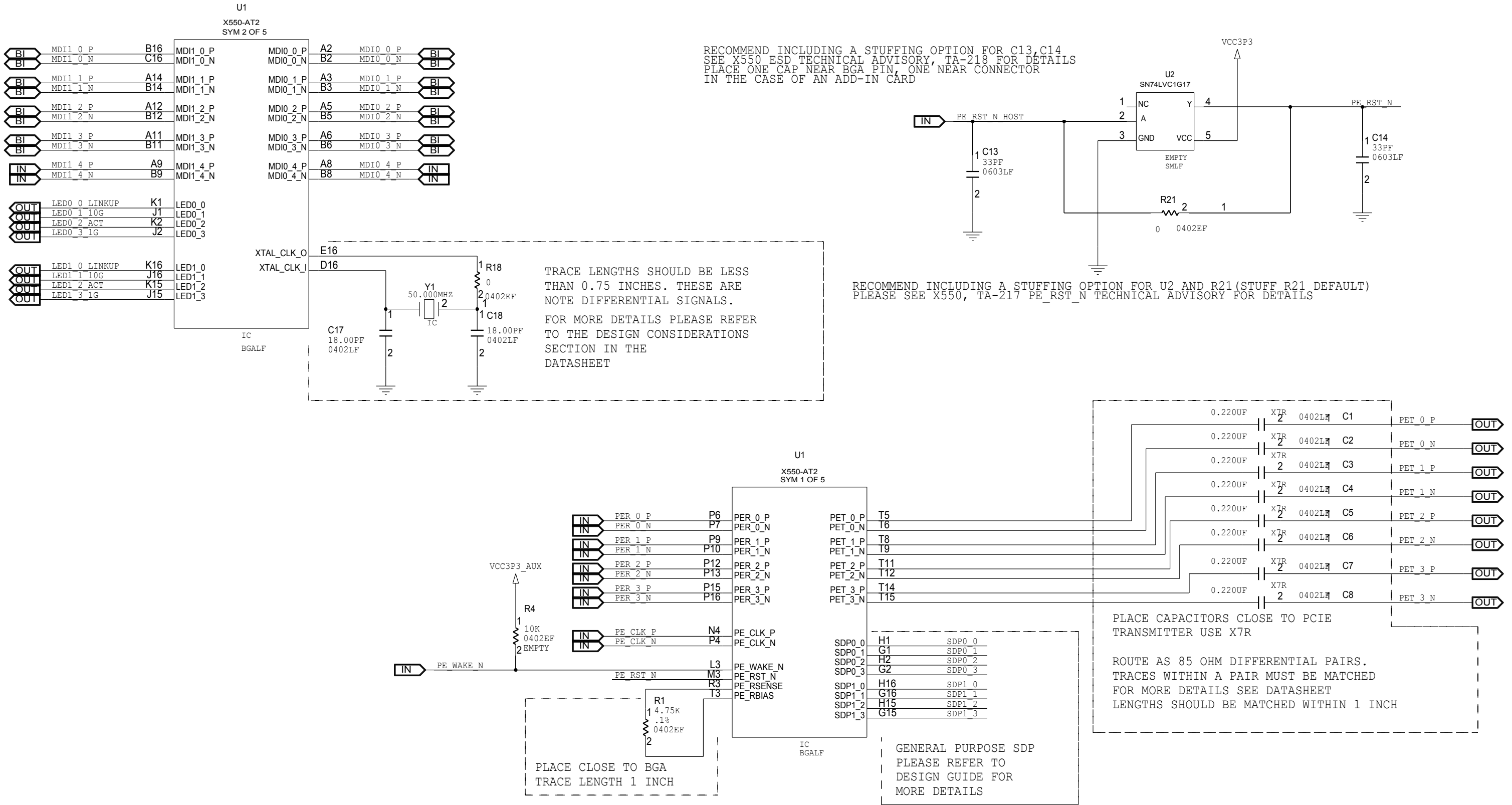
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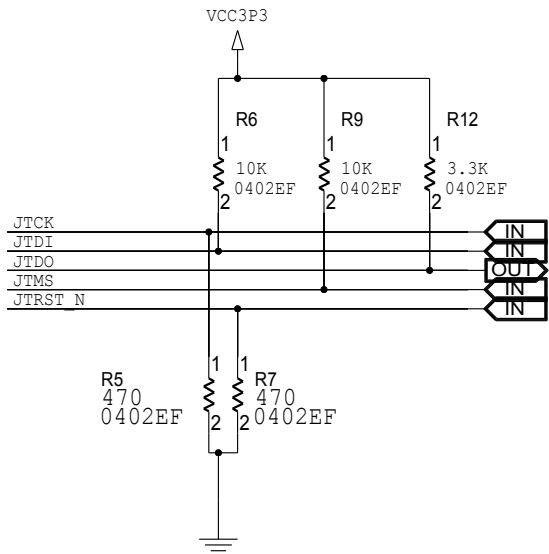
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HOST AND NETWORK INTERFACE

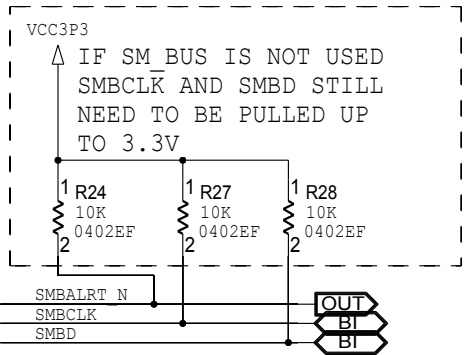


AUXILIARY CIRCUITRY
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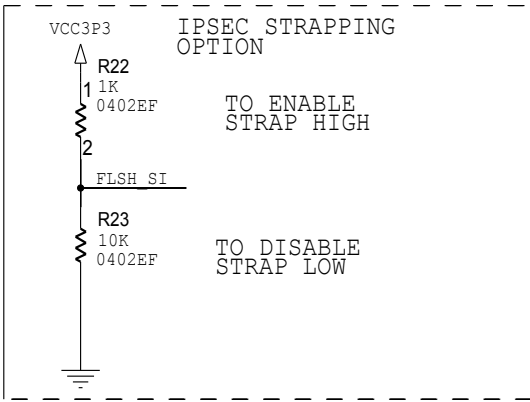
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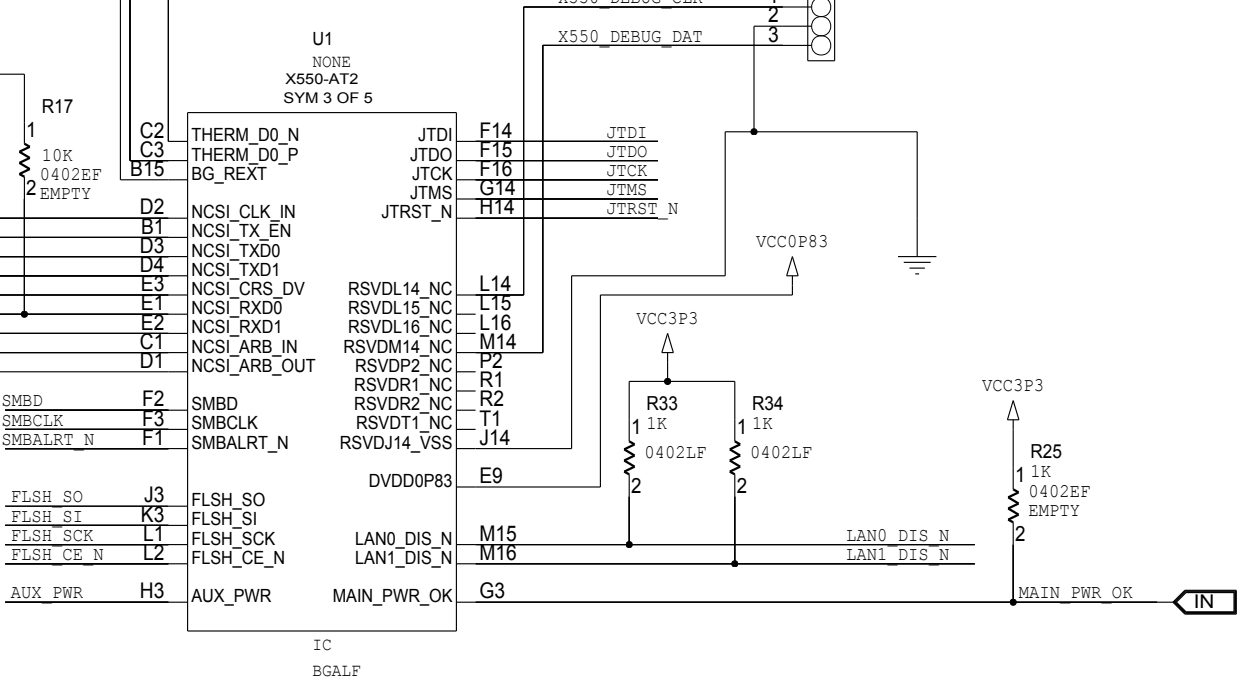
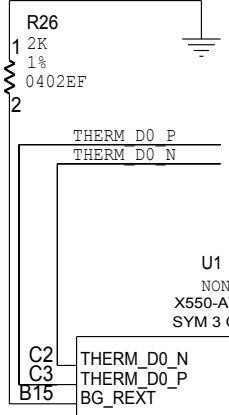
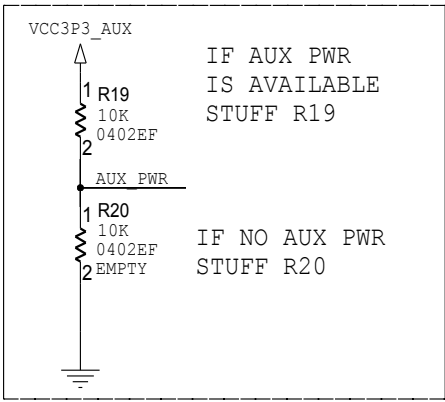
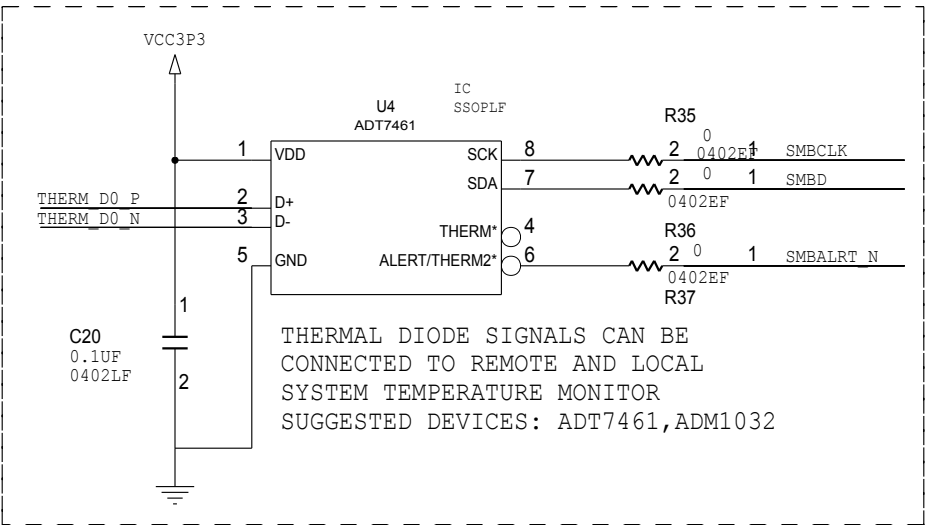
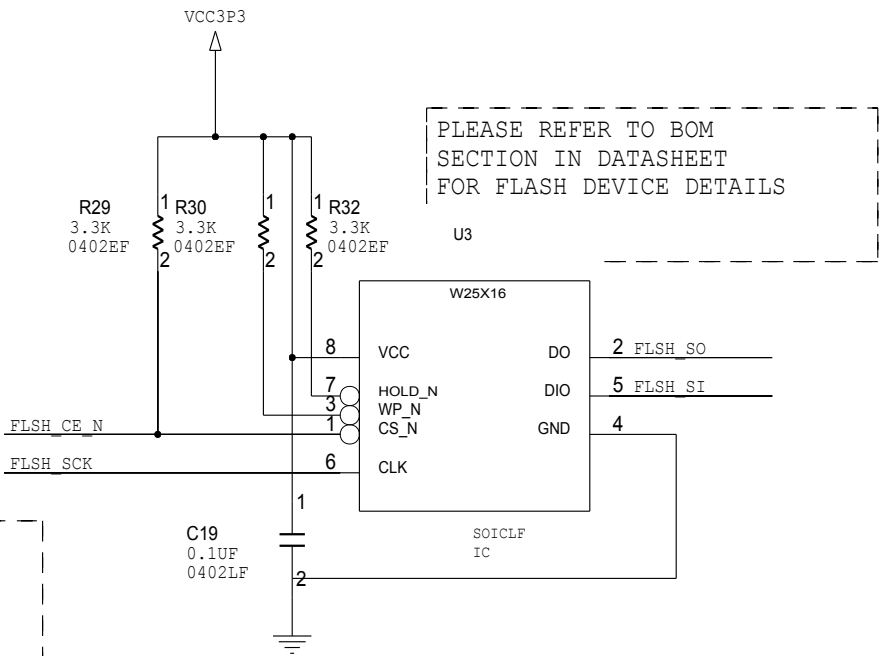
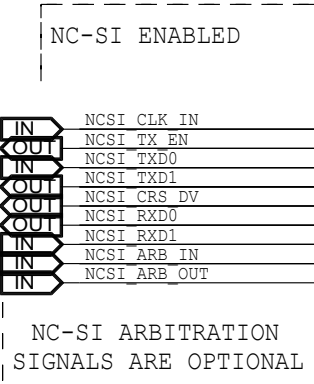
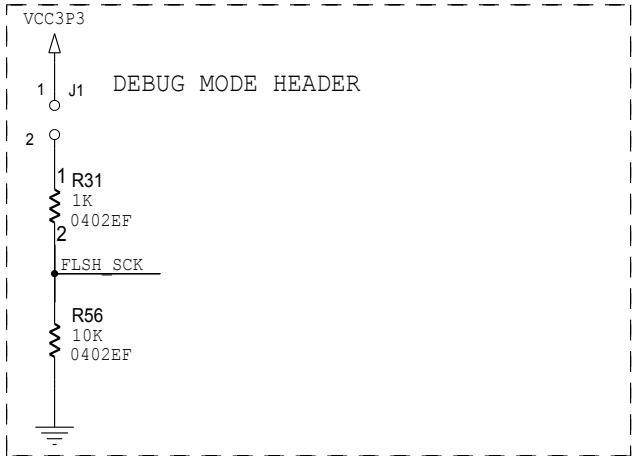
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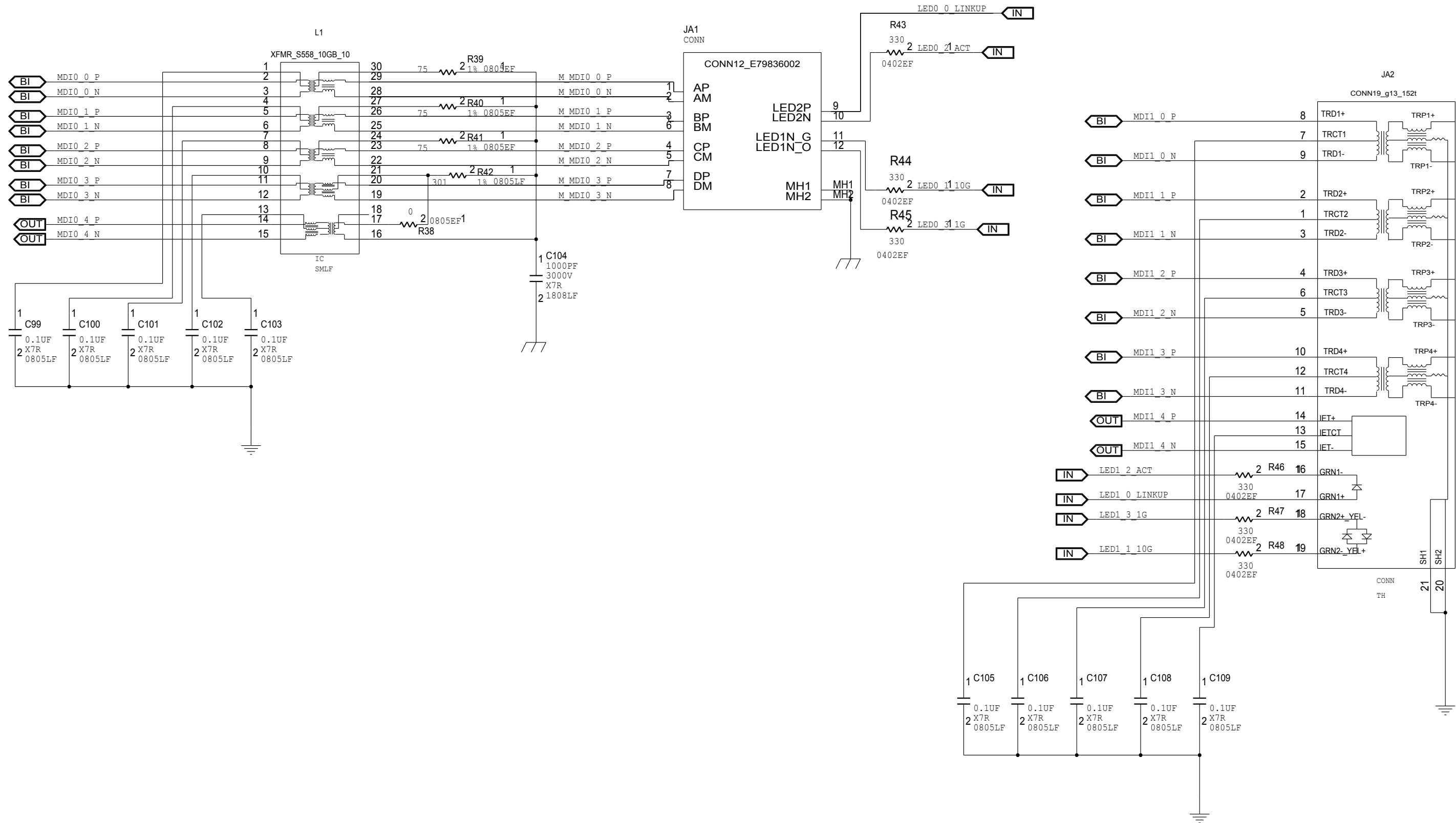
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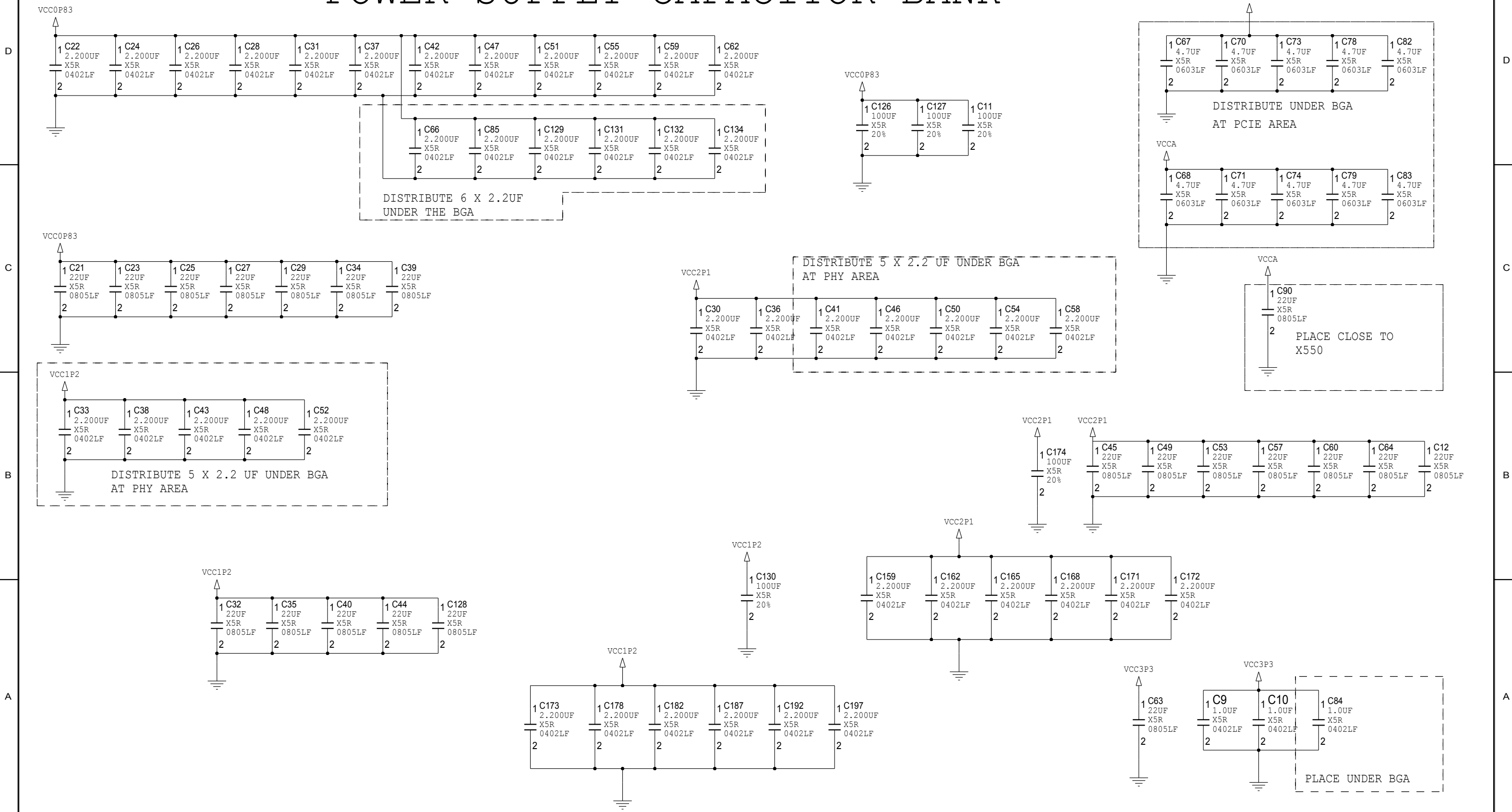
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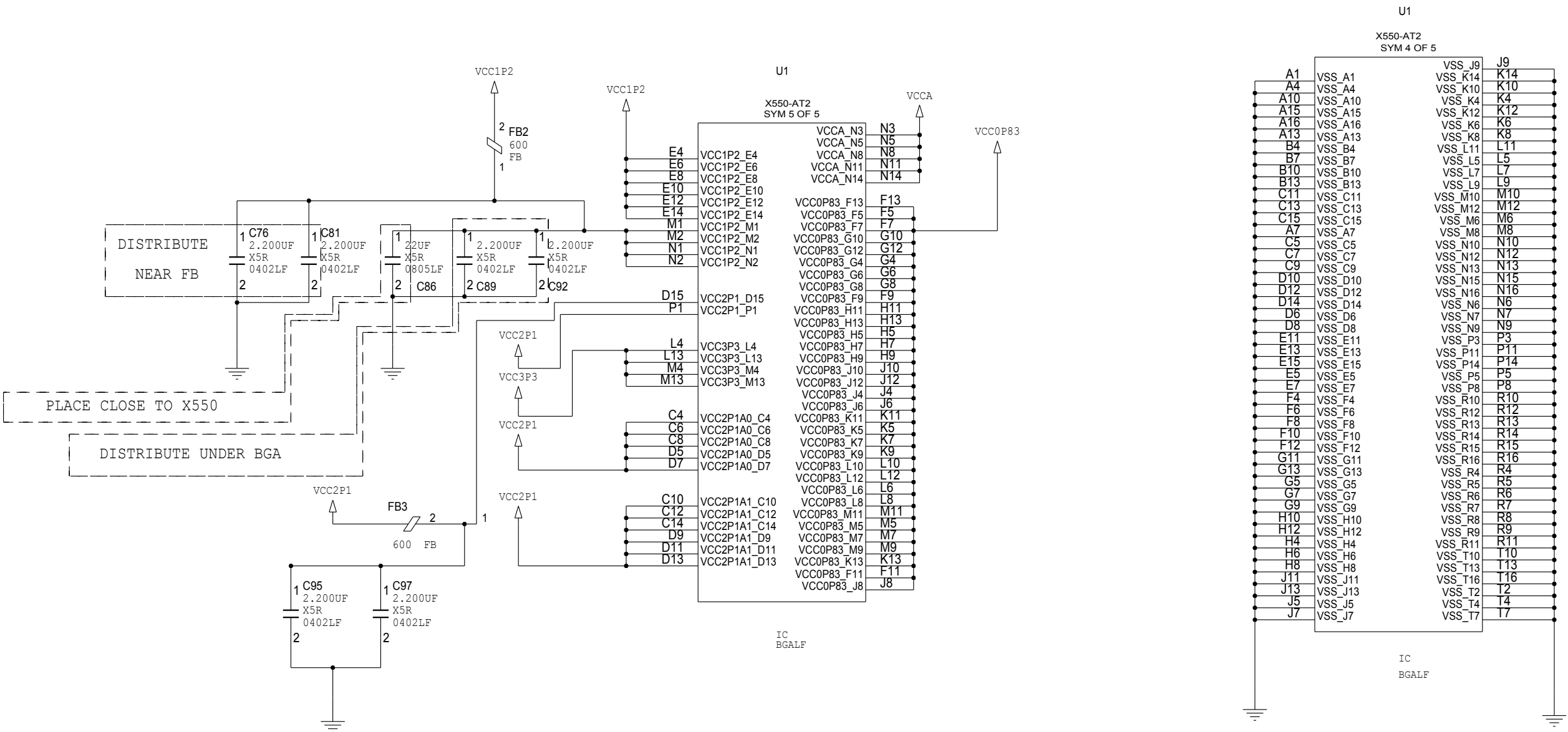
MDI INTERFACE



POWER SUPPLY CAPACITOR BANK

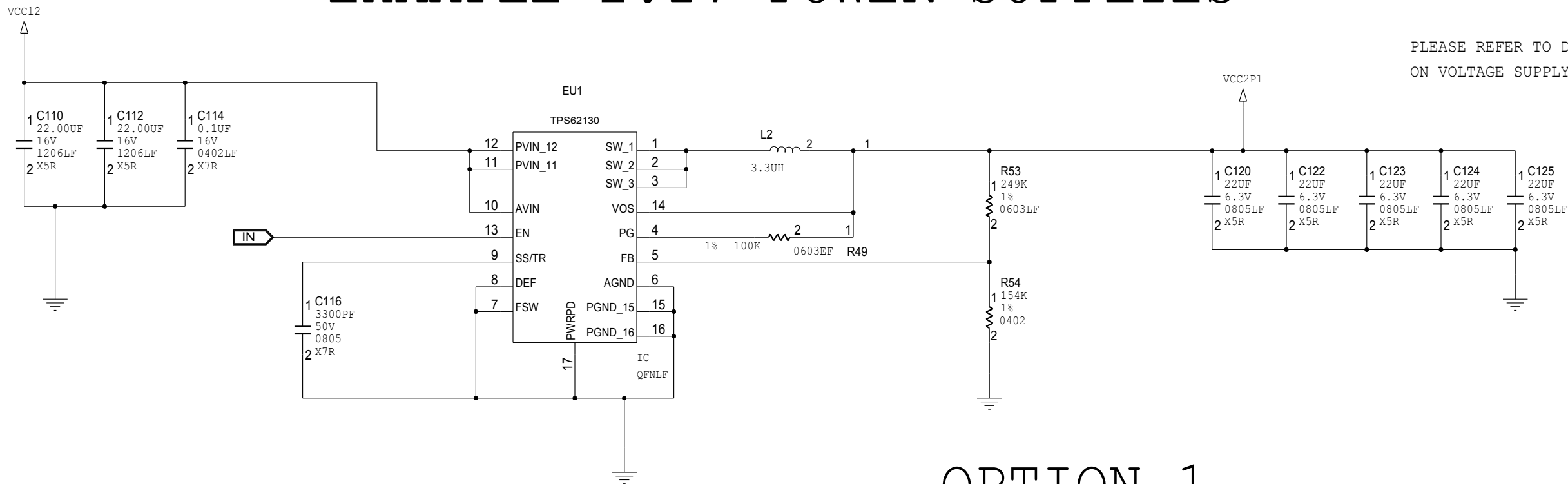


POWER AND GROUND CONNECTIONS



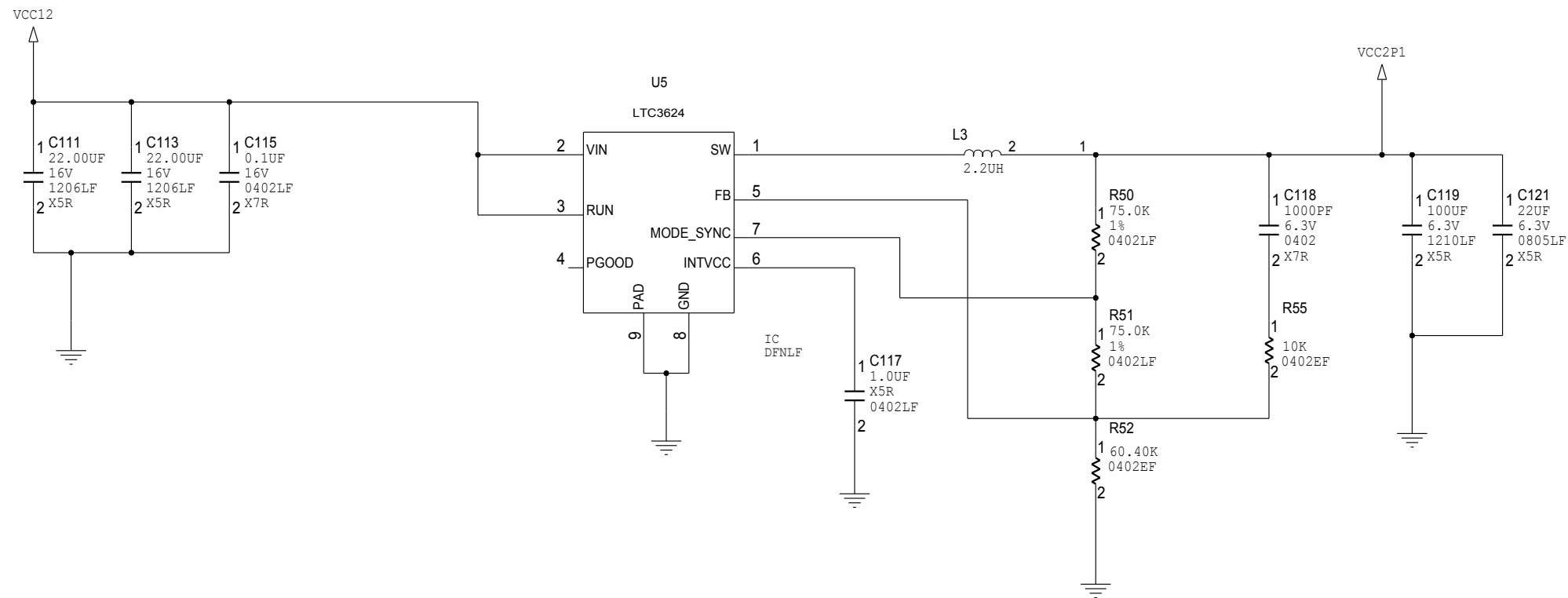
EXAMPLE 2.1V POWER SUPPLIES

PLEASE REFER TO DATASHEET FOR DETAILS
ON VOLTAGE SUPPLY SPECIFICATIONS



OPTION 1

PLEASE REFER TO DATASHEET FOR DETAILS
ON VOLTAGE SUPPLY SPECIFICATIONS



OPTION 2

8		7		6		5		4		3		2		1	
REVISION HISTORY															
0.5.....INITIAL RELEASE															
0.51.....REMOVED PE WAKE BUFFER															
1.00.....ADDED BLOCK DIAGRAM AND POWER SUPPLY DIAGRAM, CHANGED R25, R33, R34 TO 1K, ADDED STUFFING OPTIONS FOR DEBUG MODE ENABLE, IPSEC FEATURE ENABLE															
1.9.....ADDED ESD PE_RST_N FILTER STUFFING RECOMMENDATION, ADDED PE_RST_N SCHMITT TRIGGER OPTION ADDED DEBUG HEADER STUFFING OPTION															