

## Trabalho Final ESD

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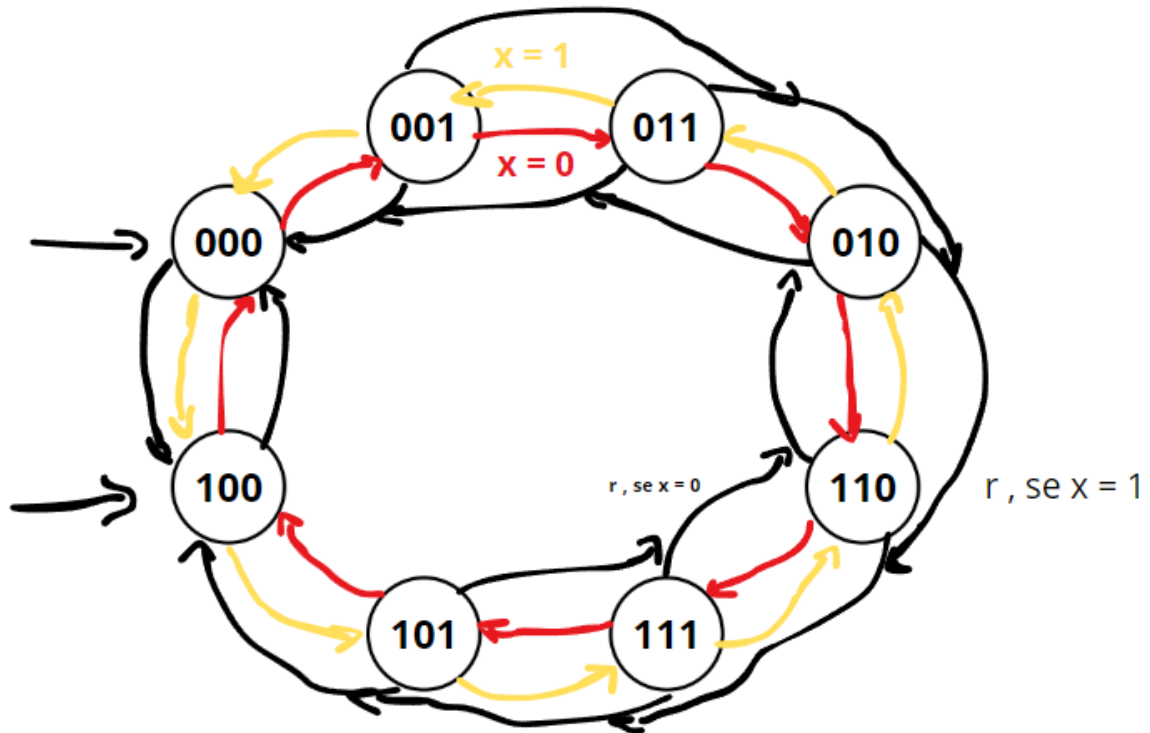
Turma: 104B

Contador Gray 3 bits Crescente/Decrescente com Reinício síncrono

Tabela de transição e entrada dos FF 's.

X	R	G2	G1	G0	B2	B1	B0
0	0	0	0	0	0	0	1
0	0	0	0	1	0	1	1
0	0	0	1	1	0	1	0
0	0	0	1	0	1	1	0
0	0	1	1	0	1	1	1
0	0	1	1	1	1	0	1
0	0	1	0	1	1	0	0
0	0	1	0	0	0	0	0
1	0	1	0	0	1	0	0
1	0	1	0	1	1	1	1
1	0	1	1	1	1	1	0
1	0	1	1	0	0	1	0
1	0	0	1	0	0	1	1
1	0	0	1	1	0	0	1
1	0	0	0	1	0	0	0
1	0	0	0	0	1	0	1
0	1	0	0	0	0	0	0
0	1	0	0	1	0	0	0
0	1	0	1	1	0	0	0
0	1	0	1	0	0	0	0
0	1	1	1	0	0	0	0
0	1	1	1	1	0	0	0
0	1	1	0	1	0	0	0
0	1	1	0	0	0	0	0
1	1	1	0	0	1	0	0
1	1	1	0	1	1	0	0
1	1	1	1	1	1	0	0
1	1	1	1	0	1	0	0
1	1	0	1	0	1	0	0
1	1	0	1	1	1	0	0
1	1	0	0	1	1	0	0
1	1	0	0	0	1	0	0

## Diagrama de Estados



## Funções Lógicas

$$g2 \\ ((A'B'C'D)' \cdot (A'B'CD)' \cdot (AB'C'D)' \cdot (AB'CD'))'$$

$$g1 \\ ((A'B'C'E)' \cdot (B'DE)' \cdot (AB'CE))'$$

$$g0 \\ ((AB)' \cdot (B'CE)' \cdot (AD'E)' \cdot (A'B'DE'))'$$