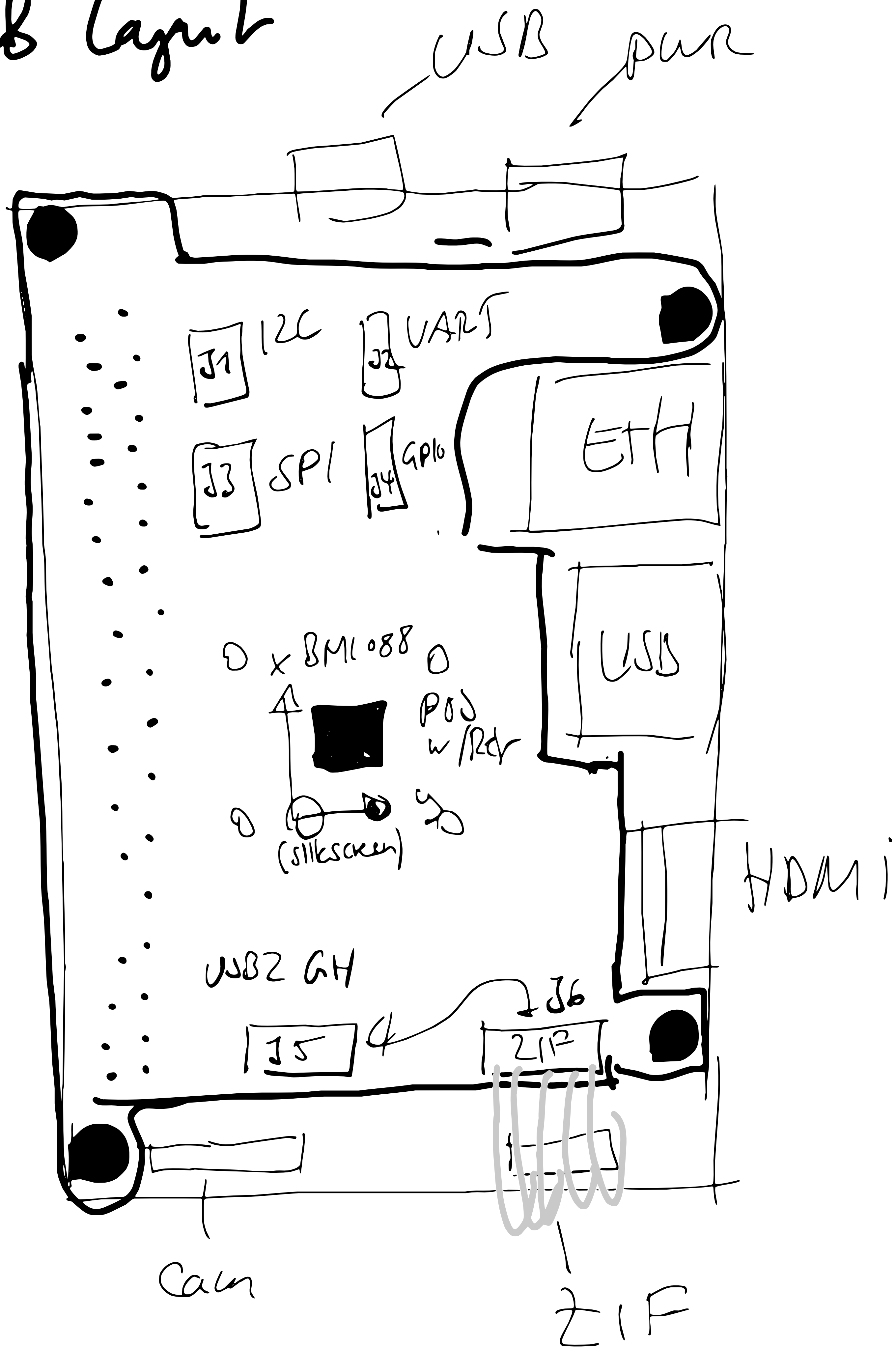


PCB Layout



J1: 3.3V
5V
I2C1-SDA
I2C1-SCL
GND

J2: 3.3V
5V
UART1-Tx
UART1-Rx
UART1-RTS
UART1-CTS
GND

J3: 3.3V
5V
SPI1-MOSI
SPI1-MISO
SPI1-SCK
SPI1-CS0
SPI1-CS1
GND

J4: 3.3V
5V
GPIO17
GPIO13
GPIO7
GND

J5: GND
USB2-DN
USB2-DP

J6:
Analog 2P
Carrier.
Mirrored?

BMI:

VDD ——— 3.3V

VDDIO ——— 3.3V

SDO1 ——— SPIO_MISO
SDO2 ———

CSB1 ——— SPIO_CS0

CSB2 ——— SPIO_CS1

SCL ——— SPIO_SCK

SDI ——— SPIO_MOSI

NC ——— GND

INT1 ——— GPIO1

INT2 ———

INT3 ———

INT4 ——— GPIO12

Questions / Comments:

- Silkscreens
- Battery holder for 3.3 bat?