Transient Execution Emulator

Meltdown and Spectre Behind the Scenes

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Structure

- Topic
- Background
- Our task
- · Our approach
- Backend
- · Demo
- Conclusion

Topic

- Meltdown and Spectre mostly patched
- · Difficult to experiment with
- $\boldsymbol{\cdot}$ Goal: Vulnerable CPU Emulator that runs on many systems

Background

- Frontend:
 - · Fetches/Decodes instructions, maintains queue
 - Branch prediction
- · Execution Engine:
 - · Multiple sets of execution units
- · Memory Subsystem:
 - Handles memory operations
 - · Maintains L1 cache

Out-of-order execution

- Independent instruction streams
- · Reservation stations
- · Common Data Bus

Speculative execution

Meltdown

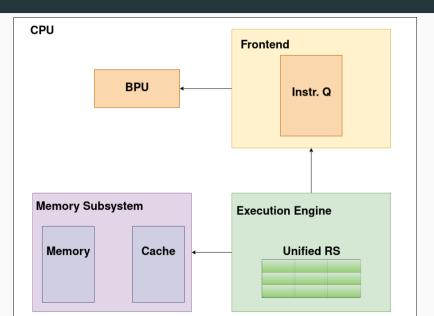
Spectre

Mitigations

Our task

Our approach

Our version



References

- Abbildung auf Folie 10 modifiziert von Abbildung 3.1 in:
 - Gruss, Daniel: "Transient-Execution Attacks", 2020, URL: https://gruss.cc/files/habil.pdf (besucht am 15.01.2021)