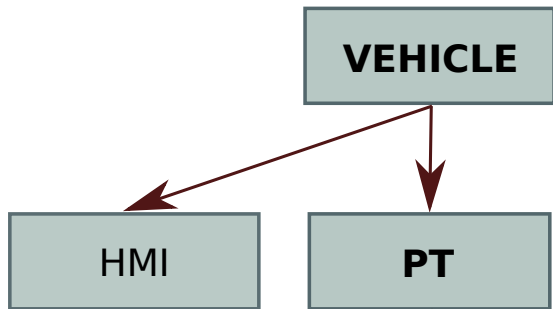
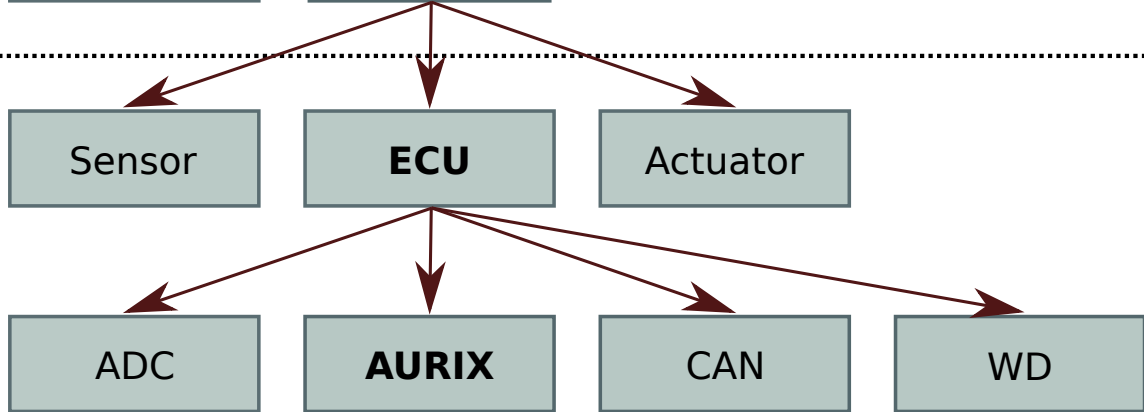


System Integration Level



Device Integration Level



Chip Integration Level

