

Andrew

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Research on USB Communication and Protocols

Diagramming of functions, including RTL diagram and state machine flowcharts.
Planning the breakdown and blocking of implementation.

Coded NZRI
Encoder /
Decoder

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Encoder /
Decoder

Coded RX/TX
Finite State
Machines

Coded Shift/Hold
registers

Coded USB
Transceiver Top
Layer

Debugged USB
Transceiver top
layer with coded
test bench

Debugged RX/TX
state machines
with coded test
benches

Debugged bit
modules with
coded test
benches

Simulating and Debugging Receiver and Transmitter

Presentation and Paper Creation