2023 Digital IC Design Homework 5

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| **Simulation Result** | | | | |
| Functional simulation | | Completed | Gate-level simulation | Completed |
|  | | |  | |
| **Evaluation Results** | | | | |
| test1.png | | 28.32 | test2.png | 26.82 |
| test3.png | | 32.09 | test4.png | 24.37 |
| test5.png | | 26.01 | test6.png | 26.54 |
| **Description of your design** | | | | |
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*Scoring = average PSNR of the six test images*

**\* PSNR of all interpolation results should meet at least the baseline.**