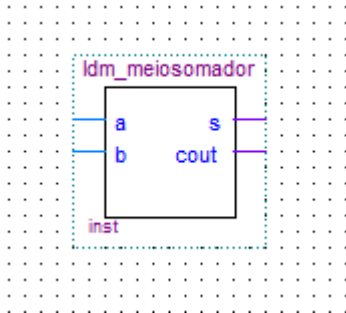


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

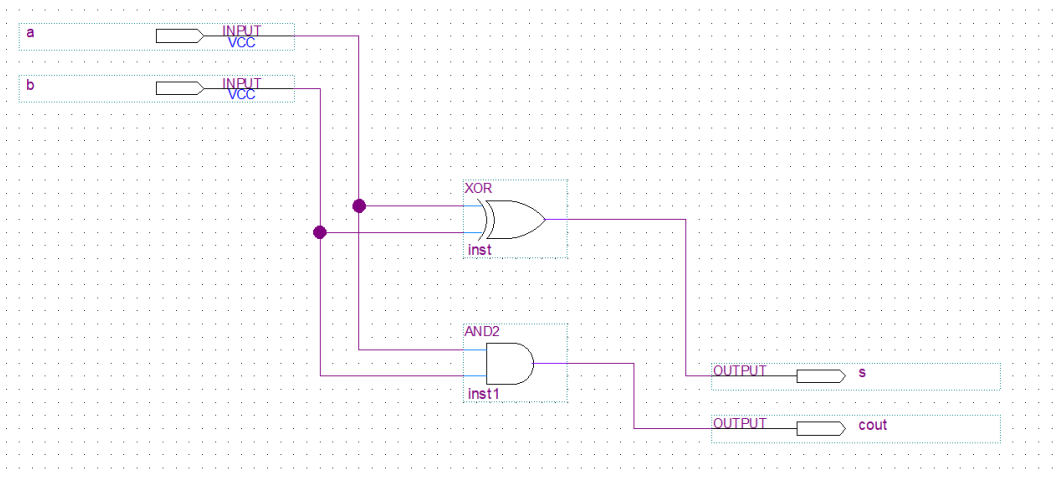
Nome do projeto: Idm_meiosomador

Descrição: Circuito do Meio Somador.

Símbolo:

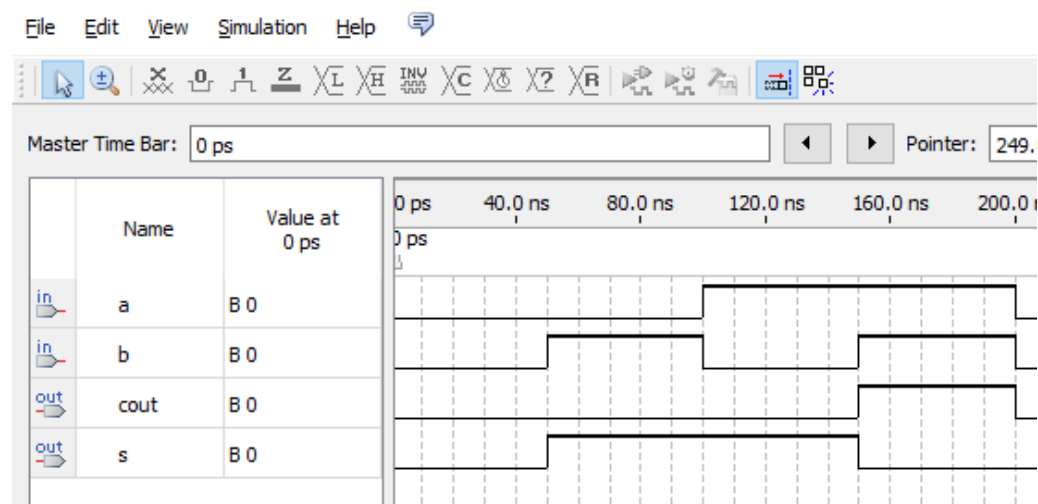


Circuito:



Simulação:

Simulation Waveform Editor - C:/QUARTUS_CD/Idm_meiosomador - Idm_meiosomador - [simulation]

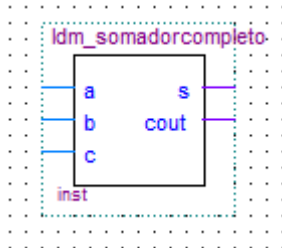


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

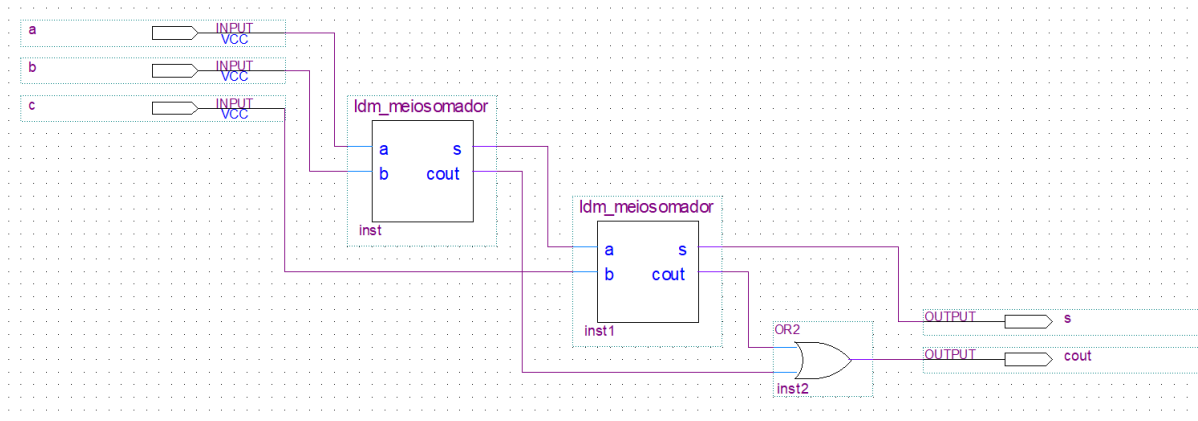
Nome do projeto: Idm_somadorcompleto

Descrição: Circuito do Somador Completo.

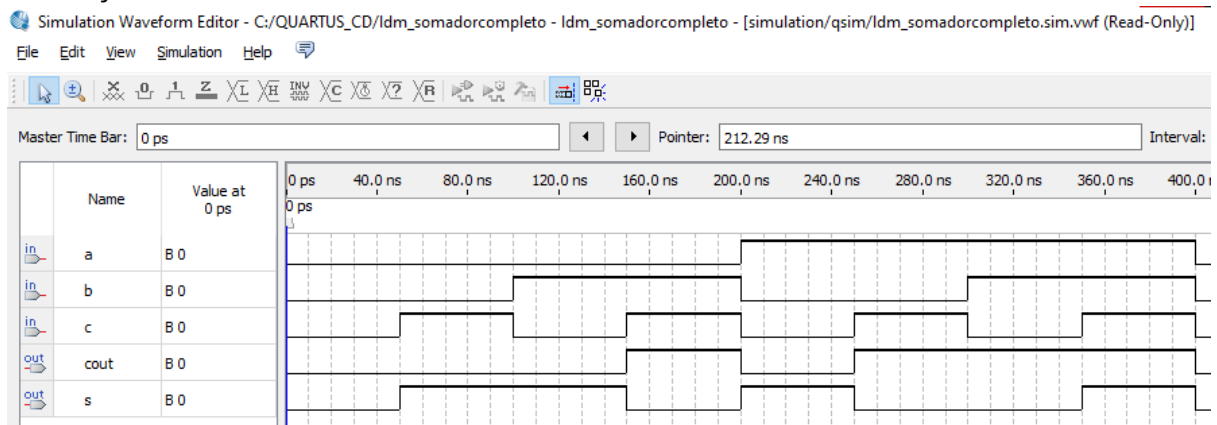
Símbolo:



Circuito:



Simulação:

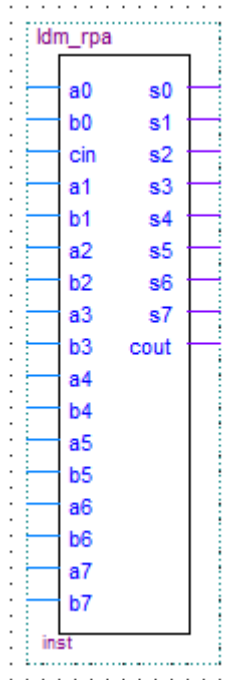


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

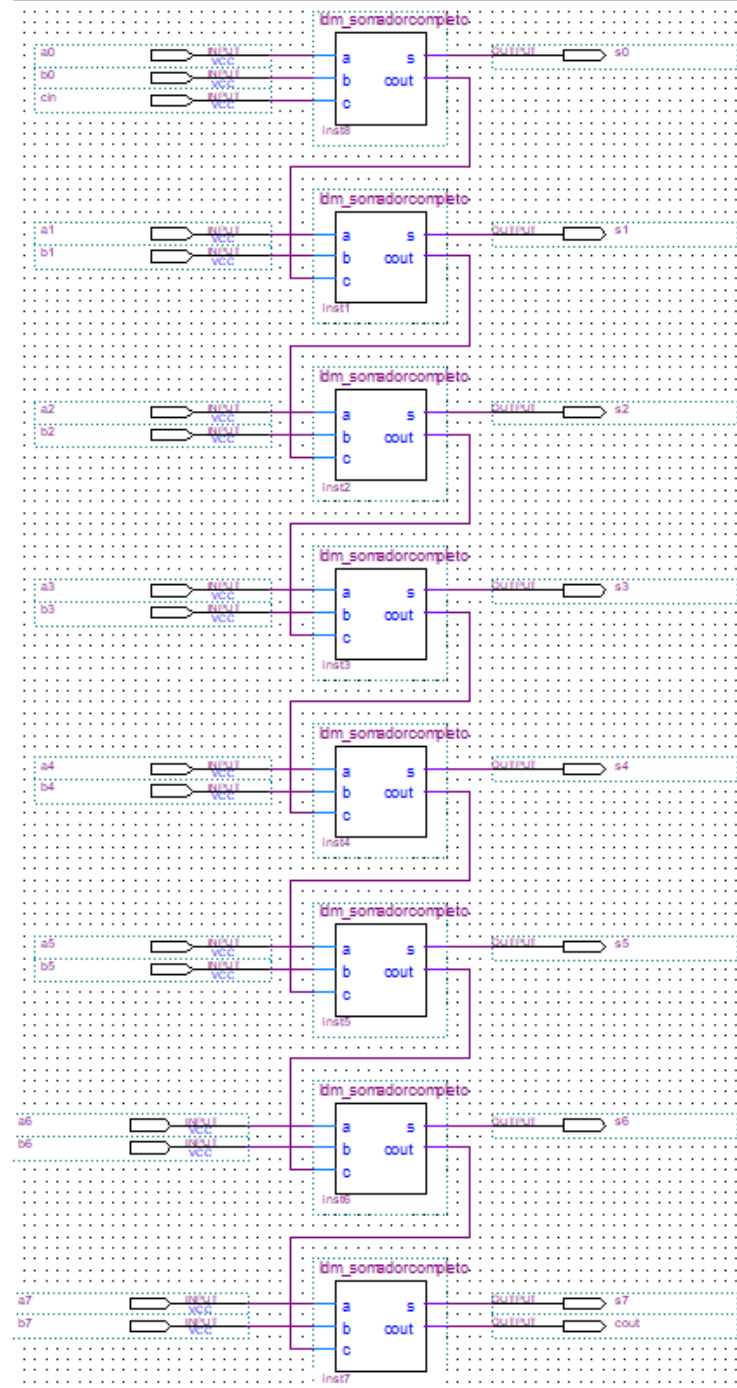
Nome do projeto: Idm_rca

Descrição: Circuito do somador e subtrator Ripple Carry Adder.

Símbolo:



Circuito:

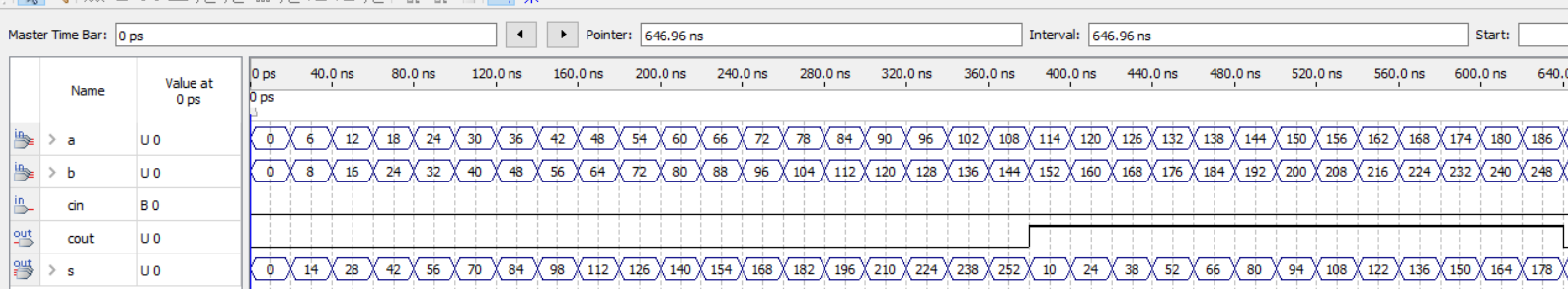


Simulação:

Simulation Waveform Editor - C:/QUARTUS_CD/Idm_rpa - ldm_rpa - [simulation/qsim/ldm_rpa.sim.vwf (Read-Only)]

File Edit View Simulation Help

Master Time Bar: 0 ps Pointer: 646.96 ns Interval: 646.96 ns Start:

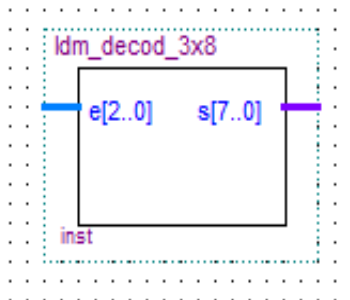


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

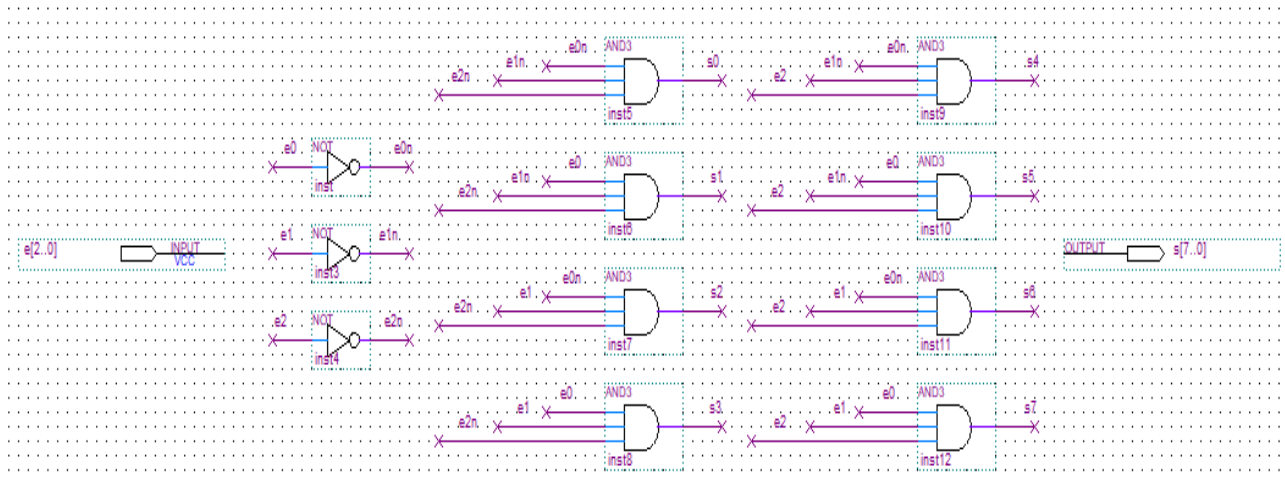
Nome do projeto: Idm_decod_3x8

Descrição: Circuito do Decodificador 3x8

Símbolo:

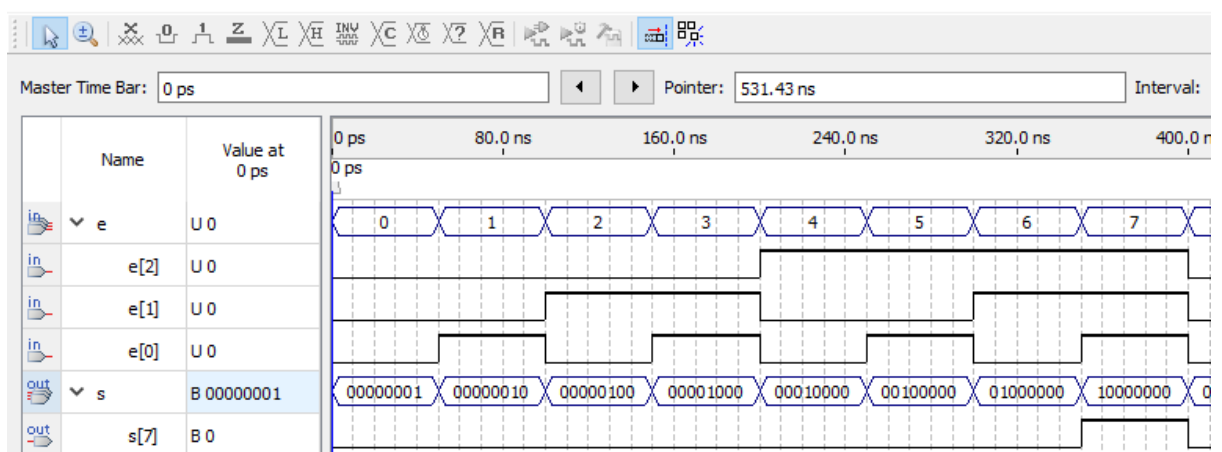


Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/aluno/CD-NEANDER-main/QUARTUS_CD/Idm_decod_3x8 - Idm_decod_3x8 - [simulation/qsim/Idm_c

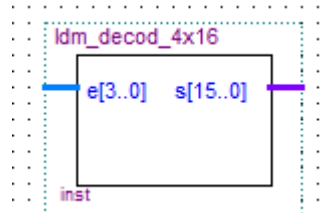


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

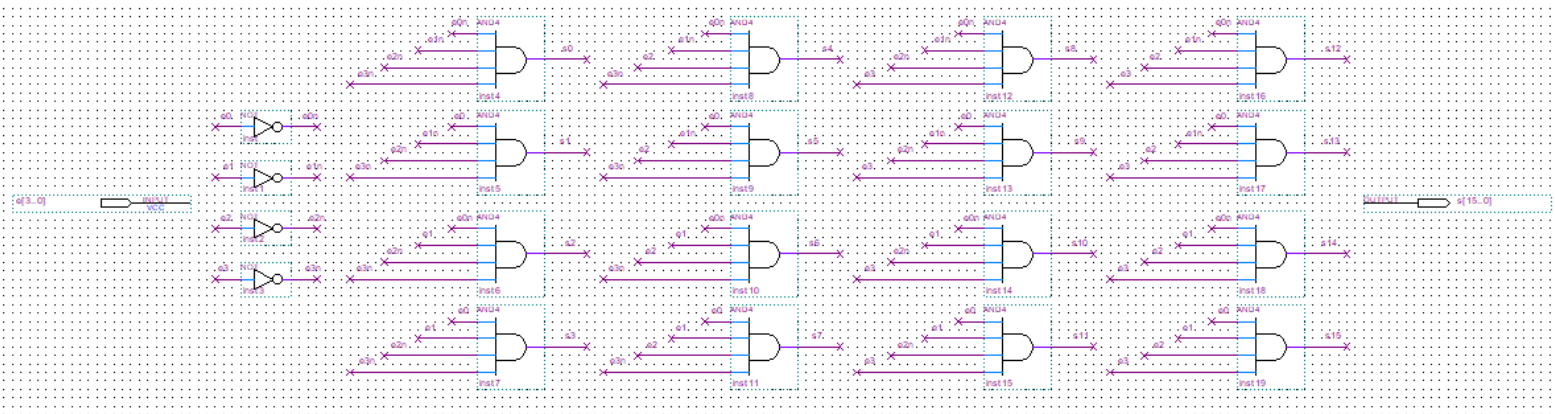
Nome do projeto: Idm_decod_4x16

Descrição: Circuito do Decodificador 4x16

Símbolo:



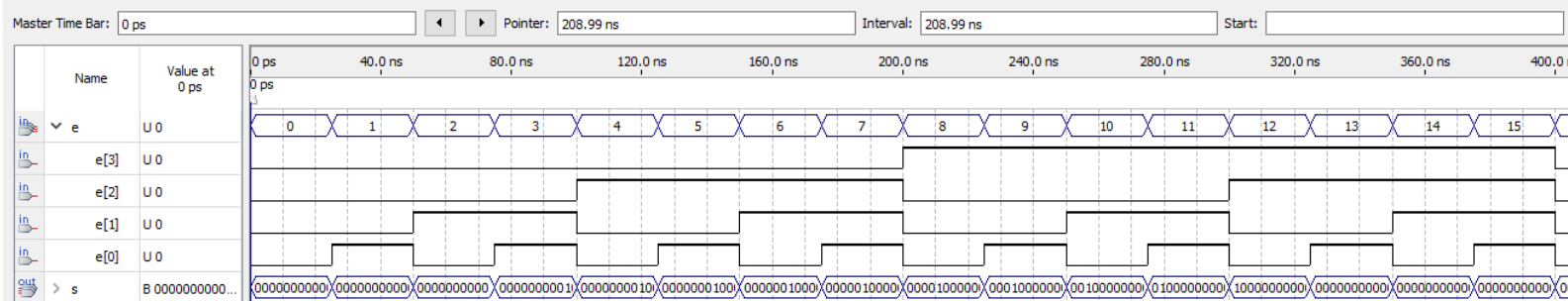
Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/aluno/CD-NEANDER-main/QUARTUS_CD/Idm_decod_4x16 - Idm_decod_4x16 - [simulation/qsim/Idm_decod_4x16.sim.vwf (Read-Only)]

File Edit View Simulation Help

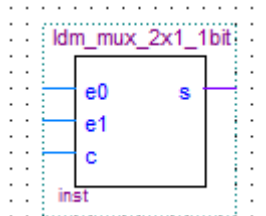


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

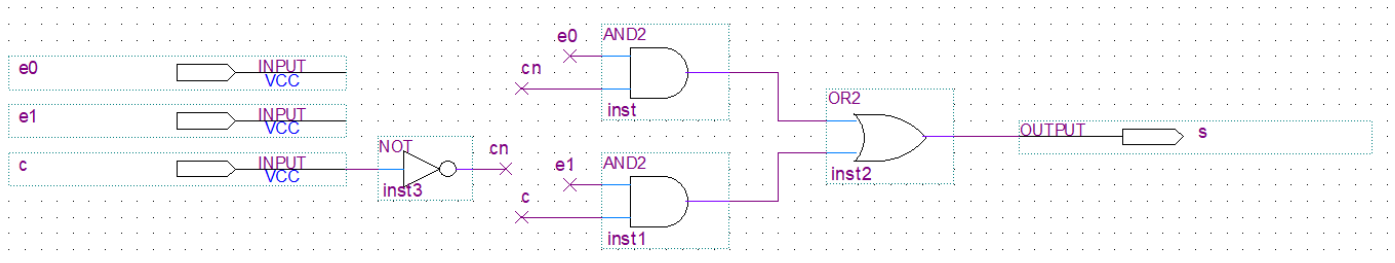
Nome do projeto: ldm_mux_2x1_1bit

Descrição: Circuito do Multiplexador 2x1 de 1bit.

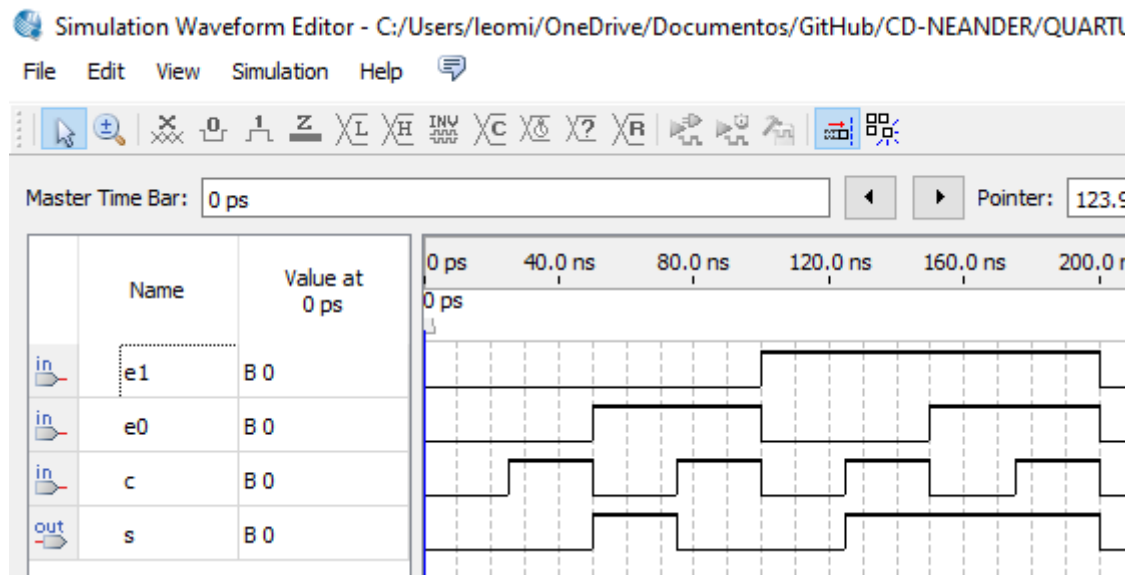
Símbolo:



Circuito:



Simulação:

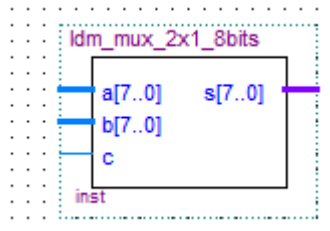


Aluno: Leonardo Dasso Migotto
Cartão: 00325098

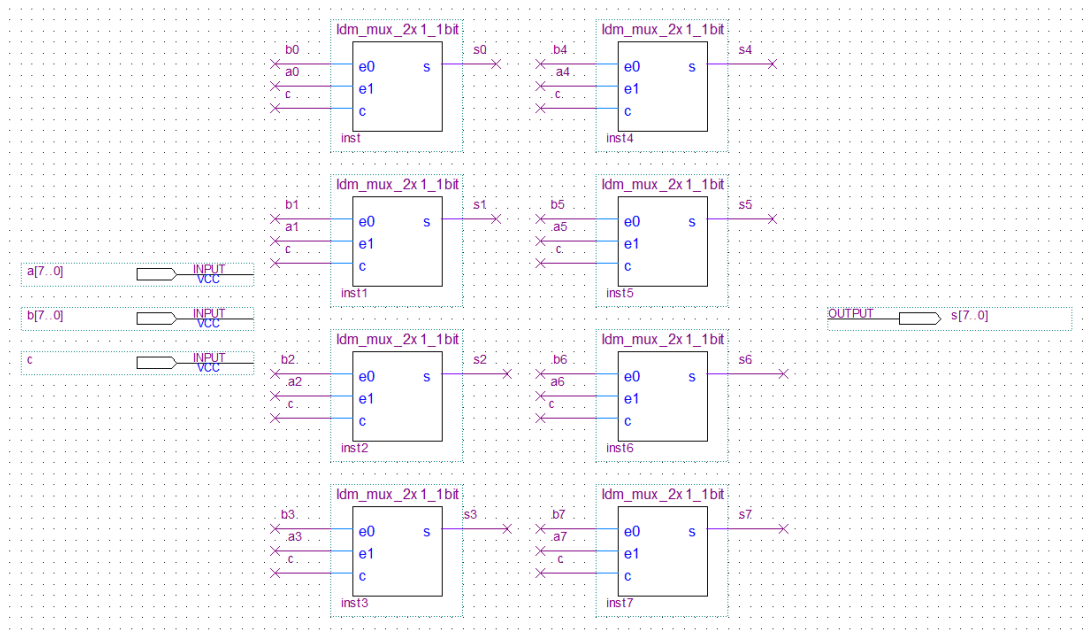
Nome do projeto: ldm_mux_2x1_8bits

Descrição: Circuito do Multiplexador 2x1 para 8bits.

Símbolo:



Circuito:



Simulação:

