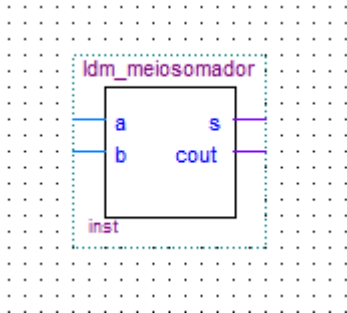


Aluno: Leonardo Dasso Migotto ([tudo pode ser encontrado neste repositório do Github](#))  
Cartão: 00325098

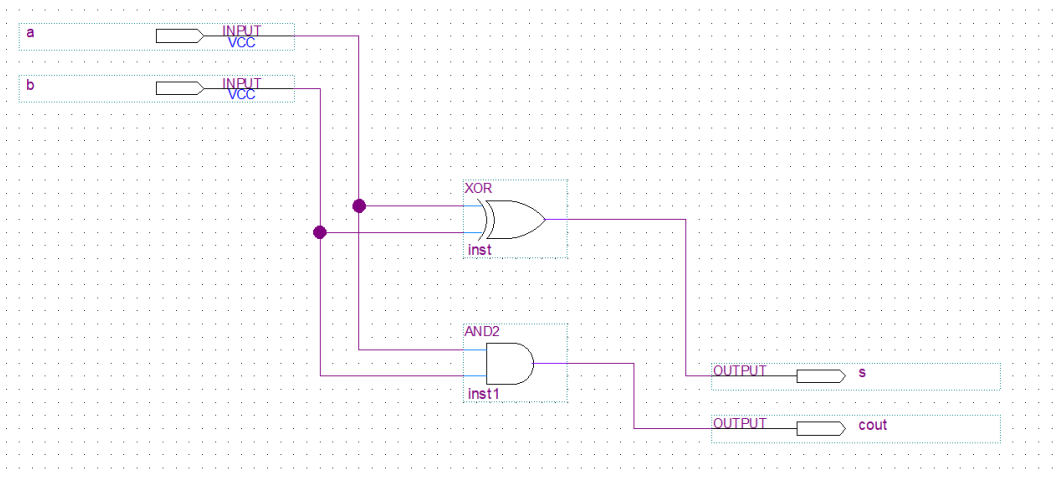
Nome do projeto: Idm\_meiosomador

Descrição: Circuito do Meio Somador.

Símbolo:

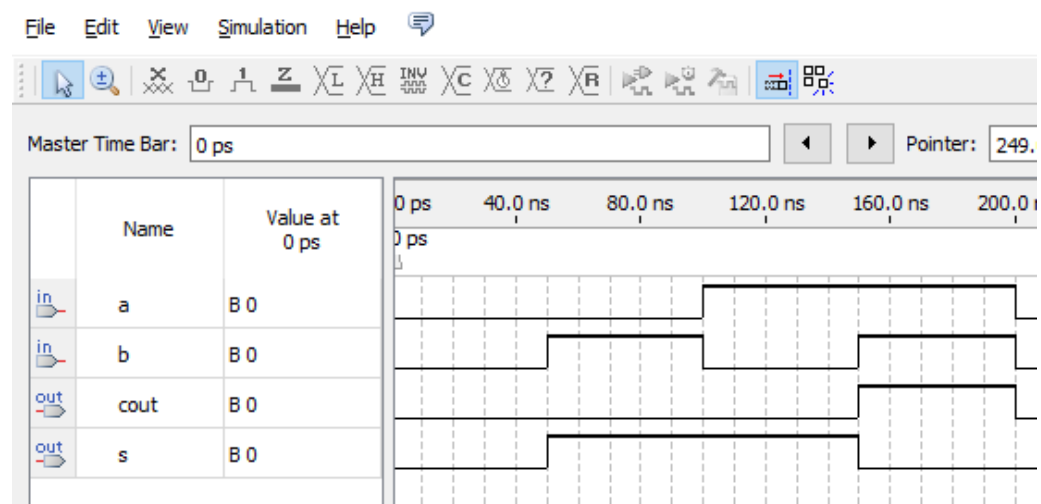


Circuito:



Simulação:

Simulation Waveform Editor - C:/QUARTUS\_CD/Idm\_meiosomador - Idm\_meiosomador - [simulation]

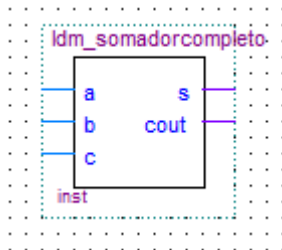


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

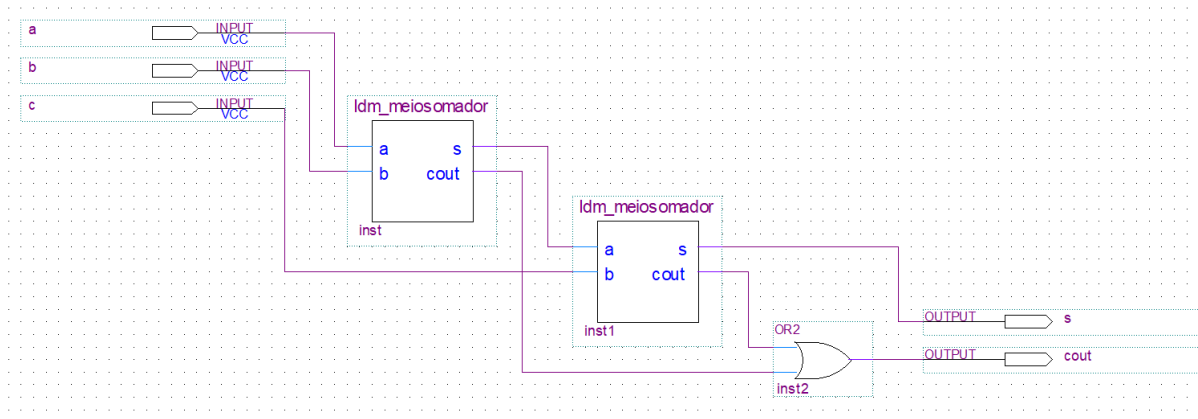
Nome do projeto: Idm\_somadorcompleto

Descrição: Circuito do Somador Completo.

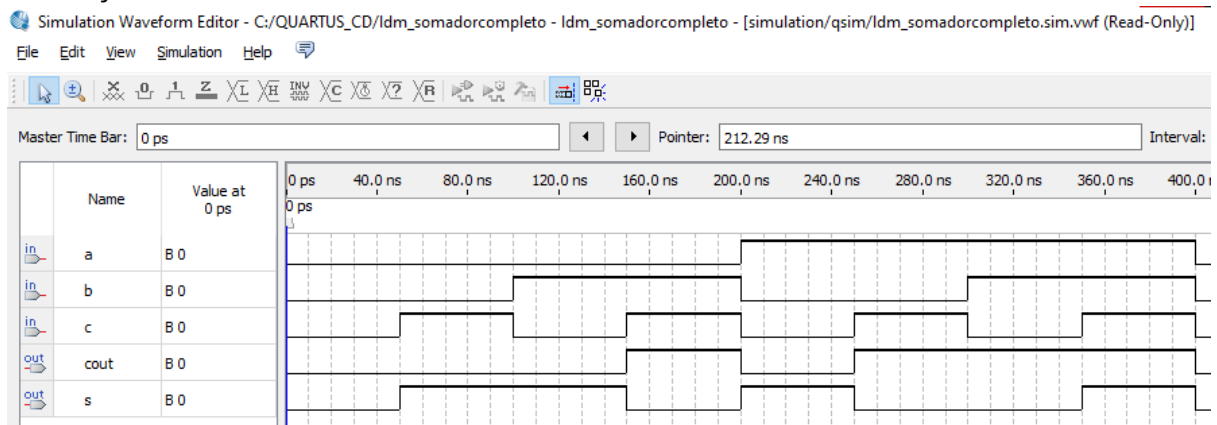
Símbolo:



Circuito:



Simulação:

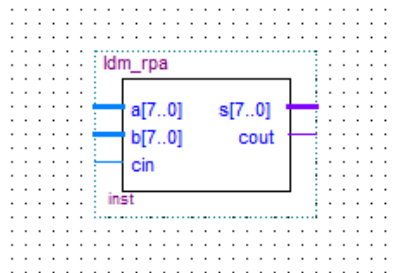


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

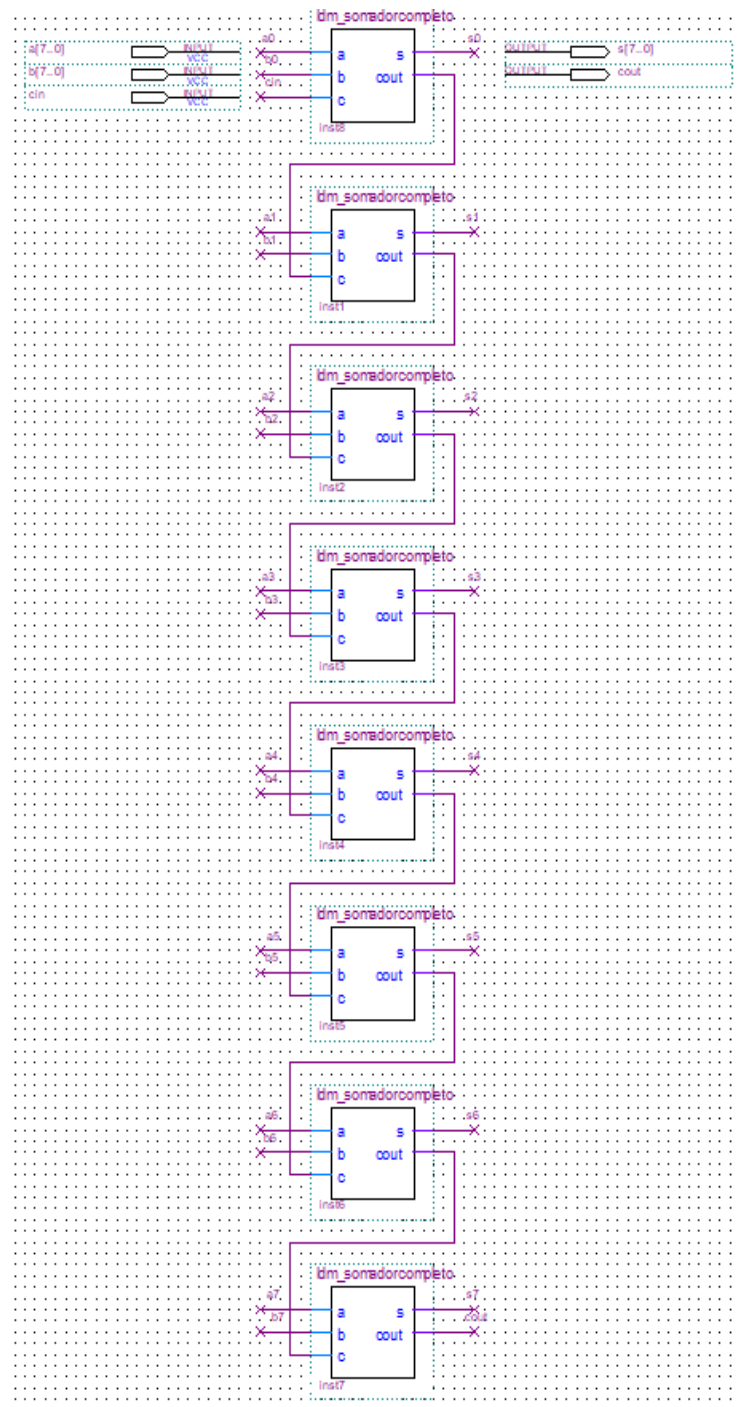
Nome do projeto: Idm\_rca

Descrição: Circuito do somador e subtrator Ripple Carry Adder.

Símbolo:



Circuito:

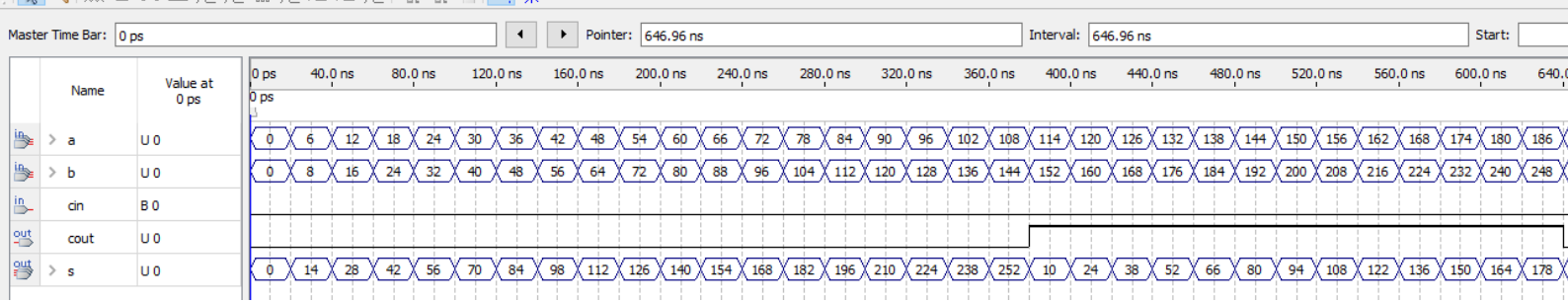


Simulação:

Simulation Waveform Editor - C:/QUARTUS\_CD/Idm\_rpa - Idm\_rpa - [simulation/qsim/Idm\_rpa.sim.vwf (Read-Only)]

File Edit View Simulation Help

Master Time Bar: 0 ps Pointer: 646.96 ns Interval: 646.96 ns Start:

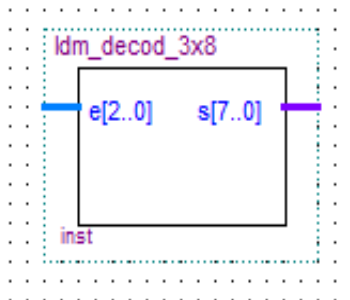


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

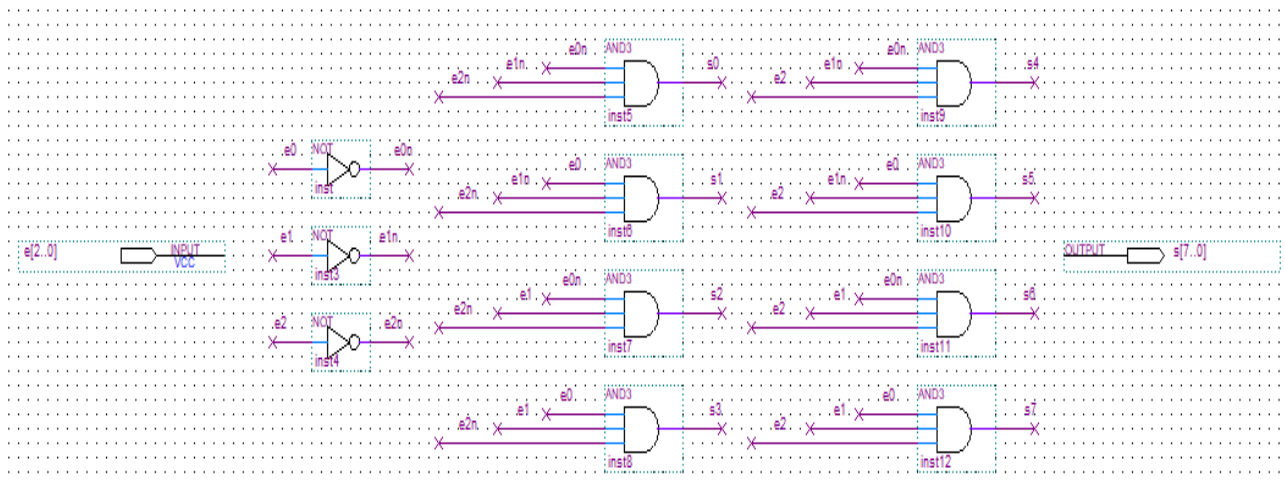
Nome do projeto: Idm\_decod\_3x8

Descrição: Circuito do Decodificador 3x8

Símbolo:



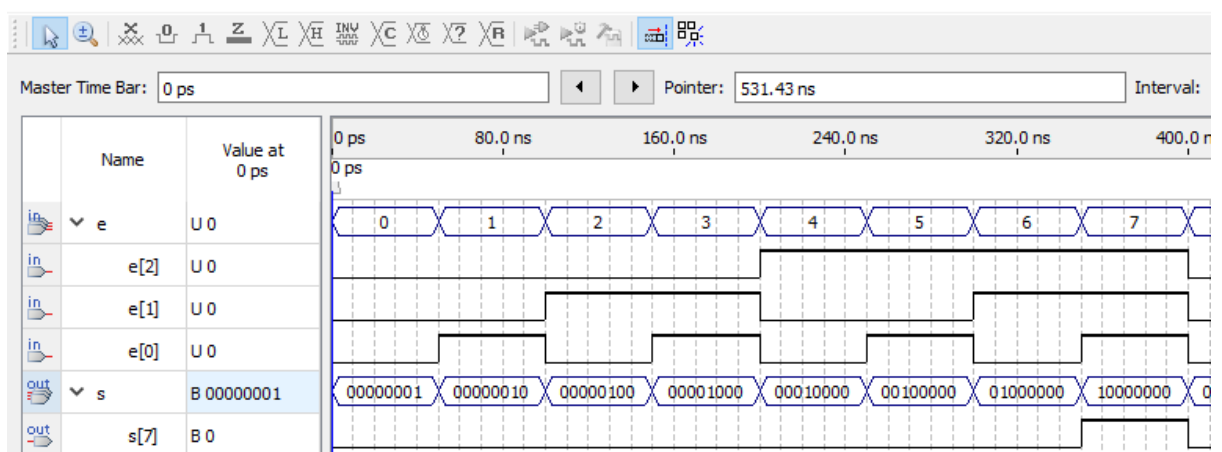
Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/aluno/CD-NEANDER-main/QUARTUS\_CD/Idm\_decod\_3x8 - Idm\_decod\_3x8 - [simulation/qsim/Idm\_c

File Edit View Simulation Help

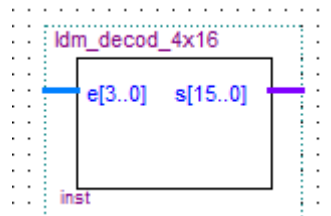


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

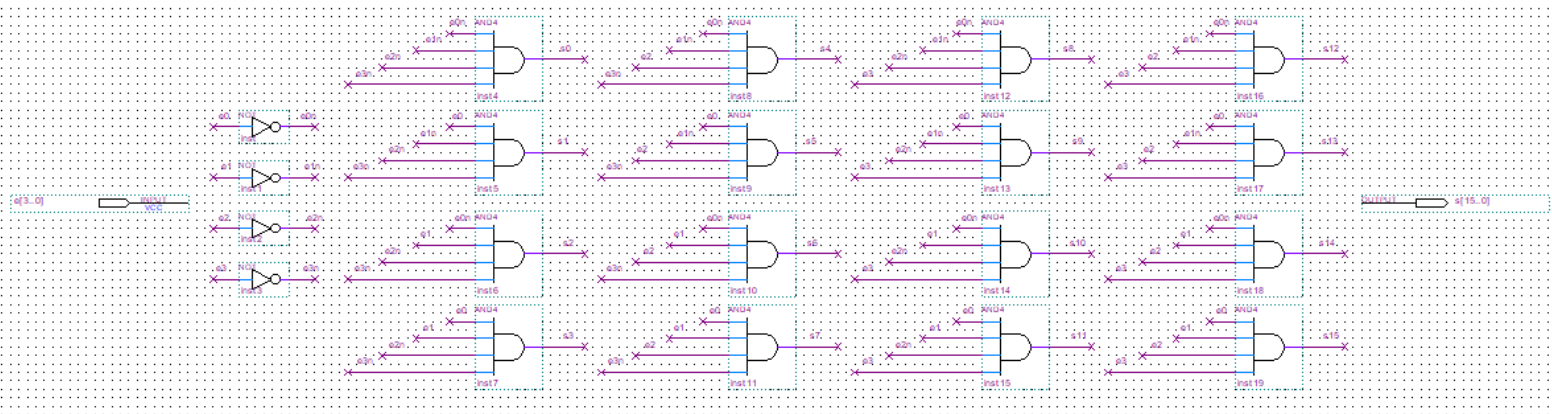
Nome do projeto: Idm\_decod\_4x16

Descrição: Circuito do Decodificador 4x16

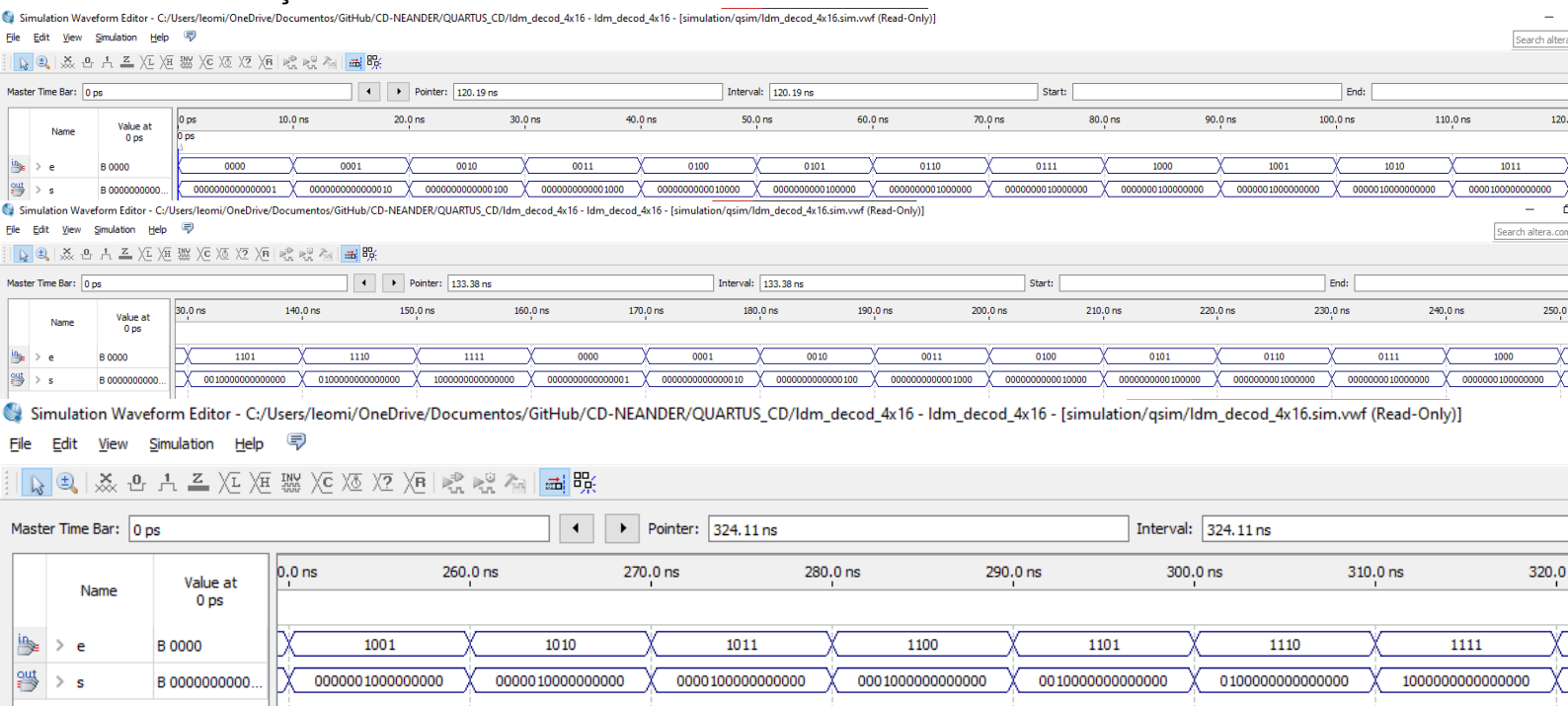
Símbolo:



Circuito:



Simulação:

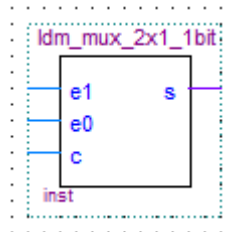


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

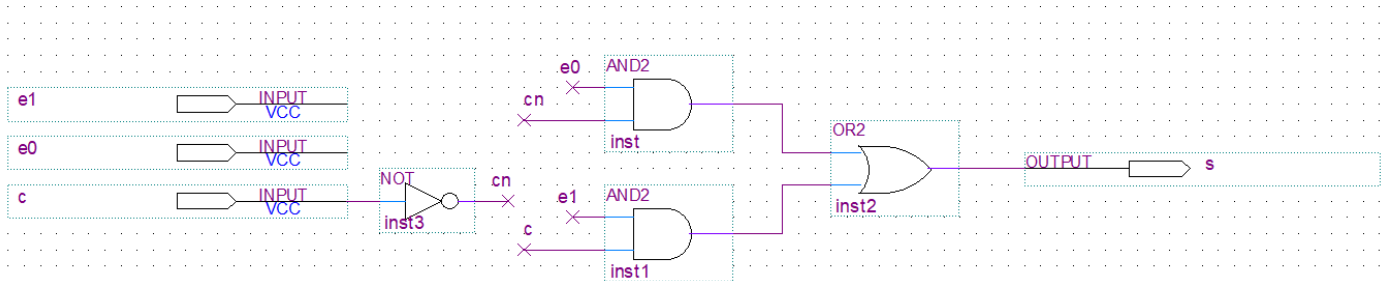
Nome do projeto: Idm\_mux\_2x1\_1bit

Descrição: Circuito do Multiplexador 2x1 de 1bit

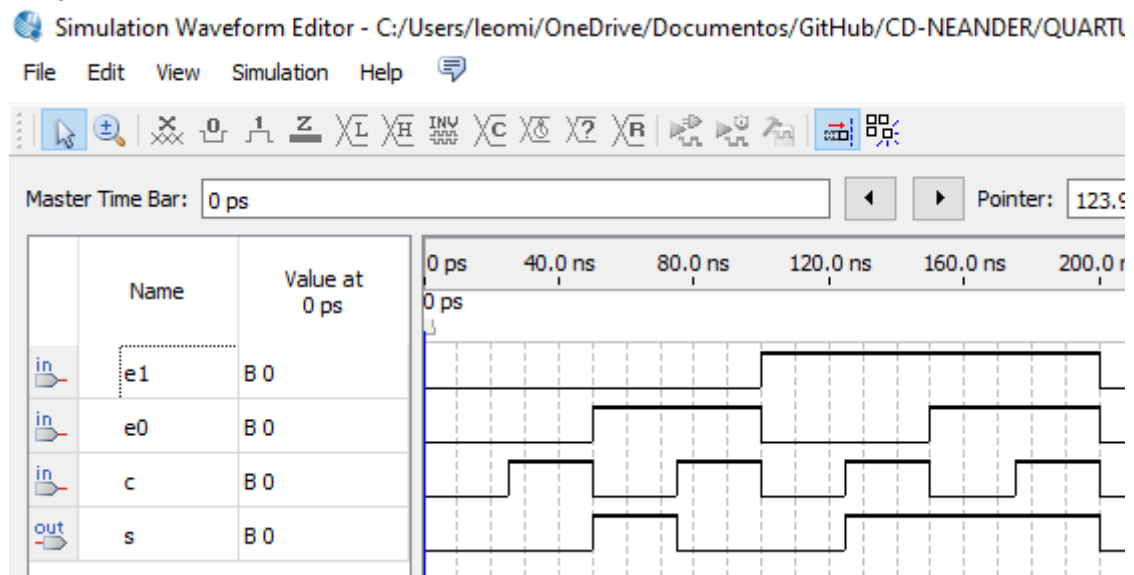
Símbolo:



Circuito:



Simulação:

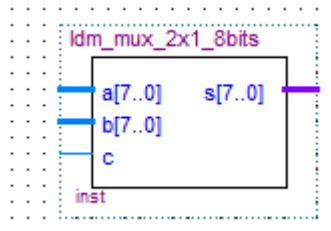


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

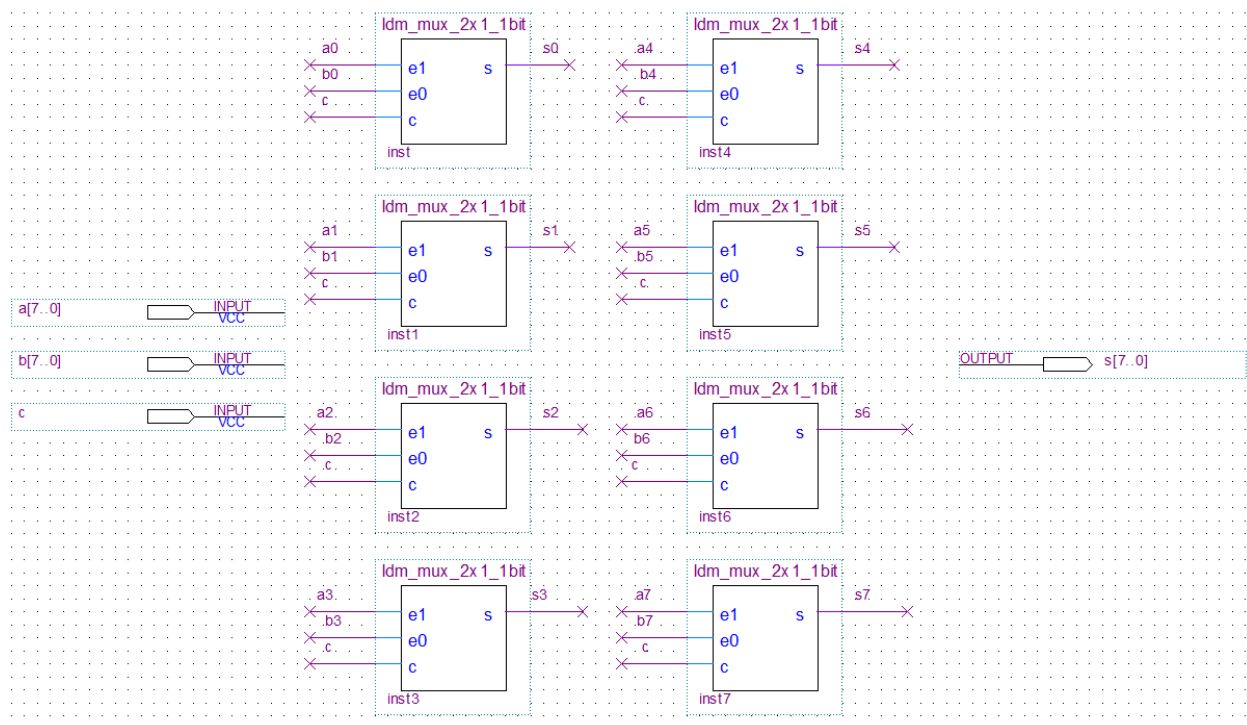
Nome do projeto: ldm\_mux\_2x1\_8bits

Descrição: Circuito do Multiplexador 2x1 de 8bits

Símbolo:



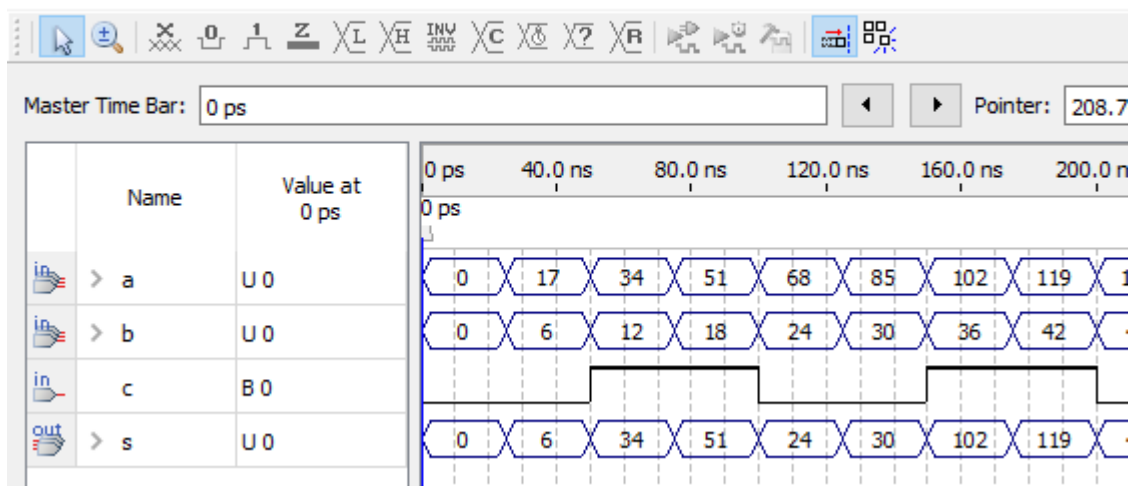
Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/leomi/OneDrive/Documentos/GitHub/CD-NEANDER/QUARTU

File Edit View Simulation Help

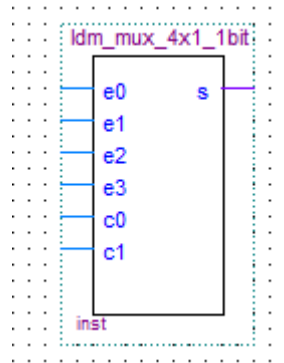


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

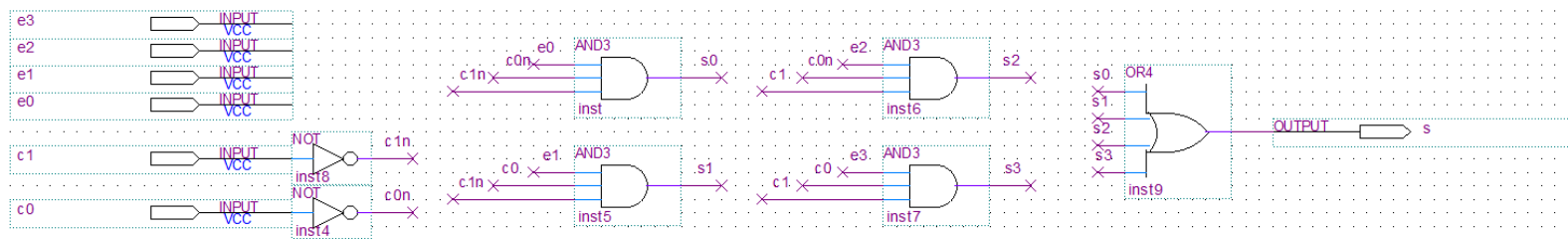
Nome do projeto: Idm\_mux\_4x1\_1bit

Descrição: Circuito do Multiplexador 4x1 de 1bit

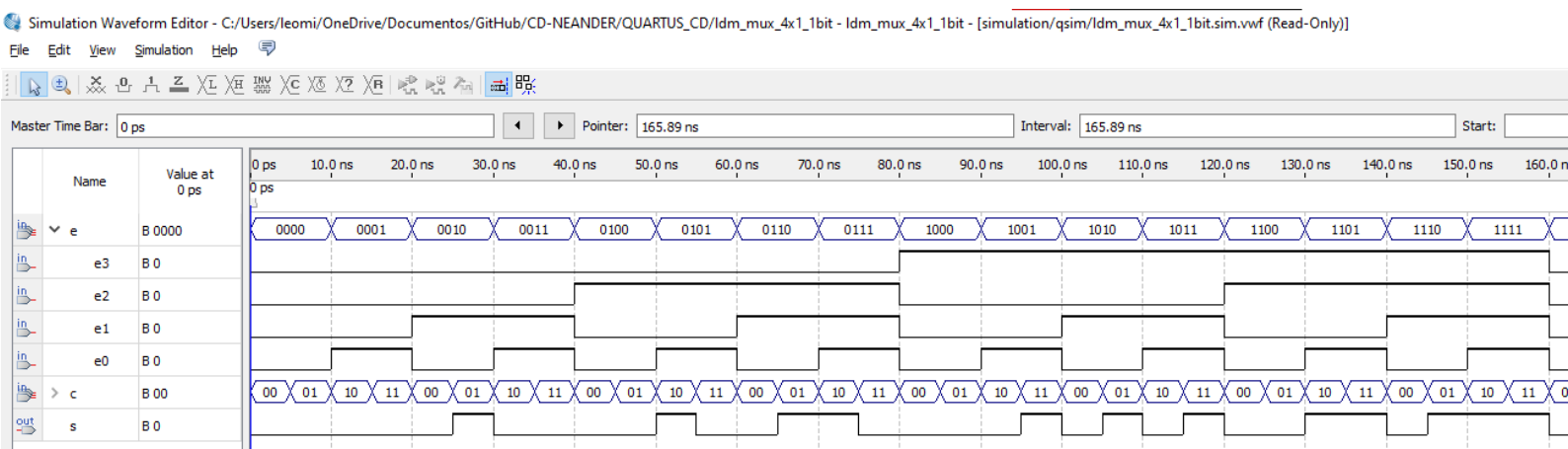
Símbolo:



Circuito:



Simulação:



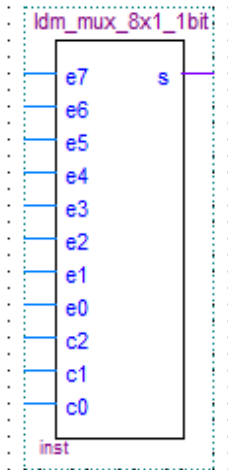


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

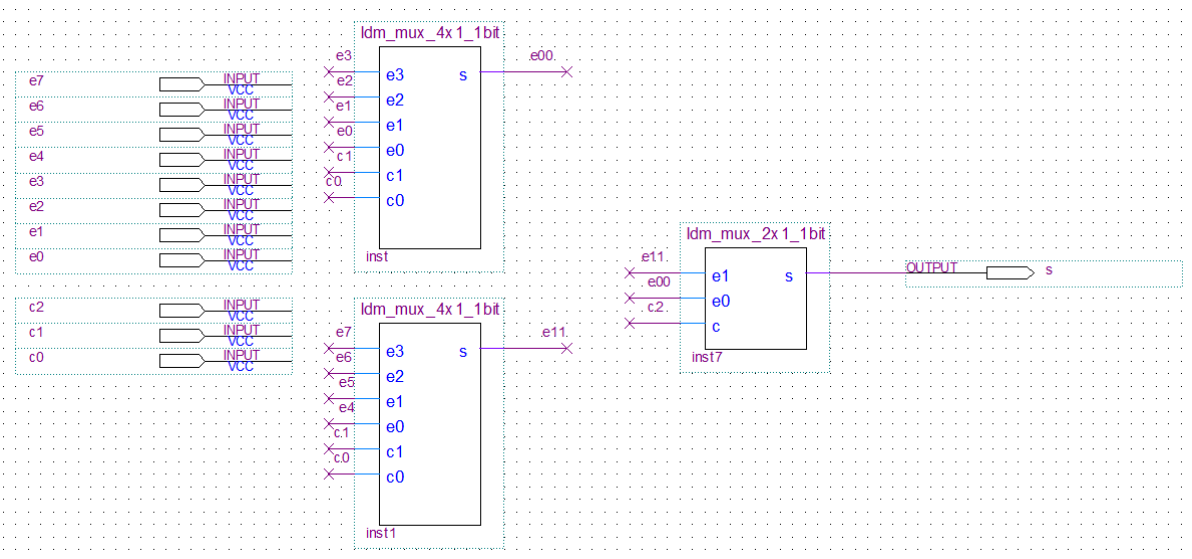
Nome do projeto: Idm\_mux\_8x1\_1bit

Descrição: Circuito do multiplexador 8x1 de 1 bit

Símbolo:

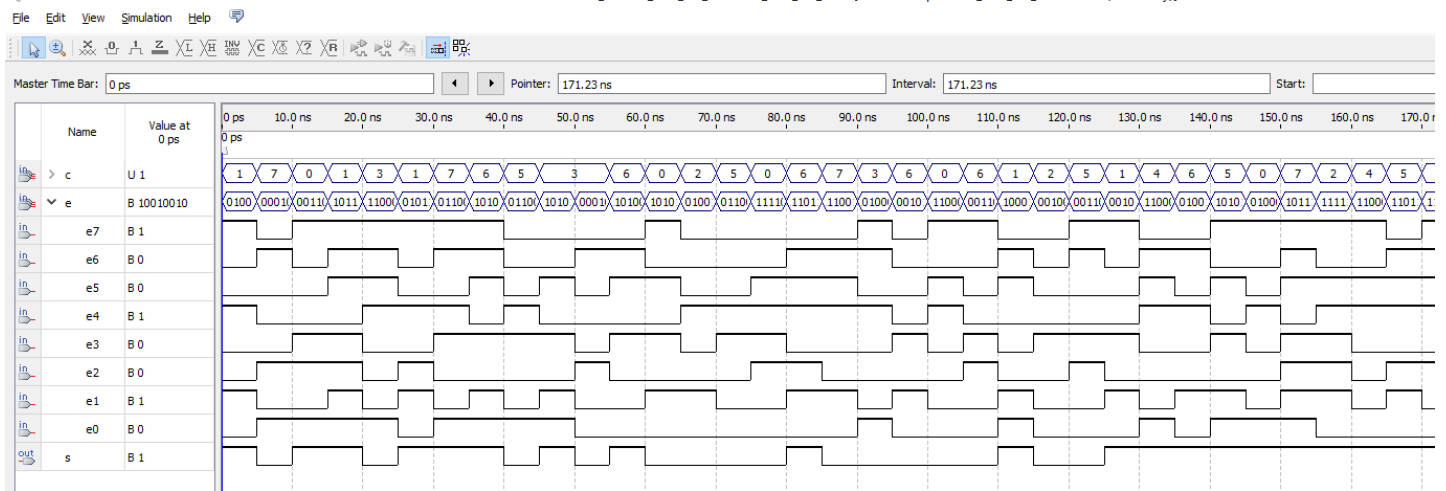


Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/leoni/OneDrive/Documents/GitHub/CD-NEANDER/QUARTUS\_CD/ldm\_mux\_8x1\_1bit - ldm\_mux\_8x1\_1bit - [simulation/qsim/ldm\_mux\_8x1\_1bit.sim.vwf (Read-Only)]

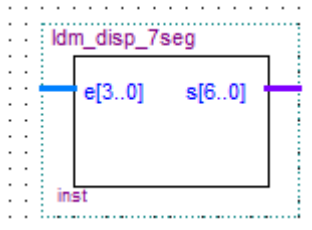


Aluno: Leonardo Dasso Migotto  
Cartão: 00325098

Nome do projeto: Idm\_disp\_7seg

Descrição: Circuito do display de 7 segmentos

Símbolo:

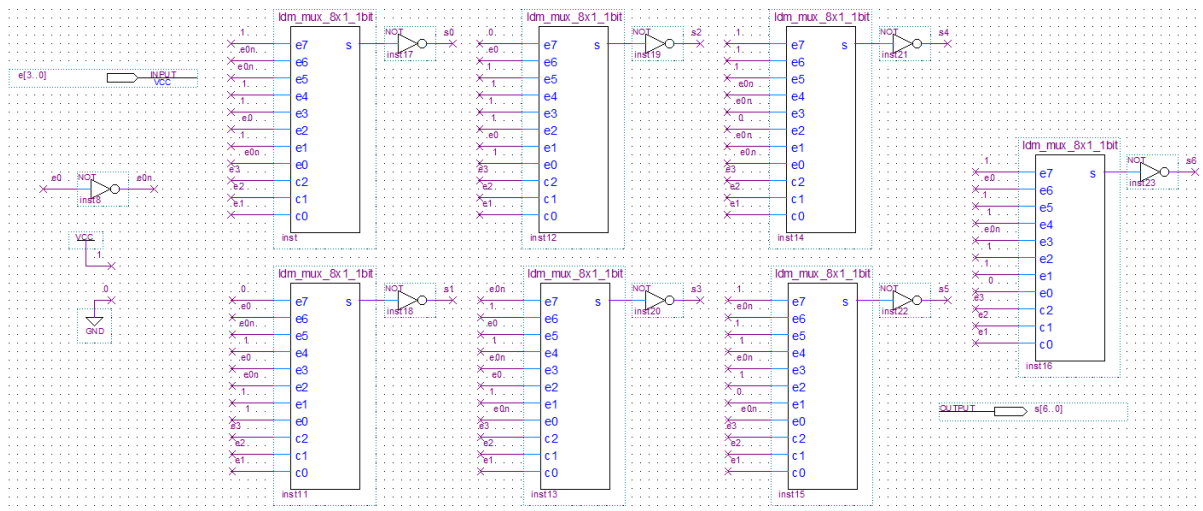


Display 7 seg

Inverter saída 0  
pra placa

	e <sub>3</sub>	e <sub>2</sub>	e <sub>1</sub>	e <sub>0</sub>	s <sub>0</sub>	s <sub>1</sub>	s <sub>2</sub>	s <sub>3</sub>	s <sub>4</sub>	s <sub>5</sub>	s <sub>6</sub>	
0	0	0	0	0	0	1	1	0	0	0	0	0
1	0	0	1	0	0	1	0	1	0	0	0	1
2	0	0	1	1	0	1	1	0	0	1	0	2
3	0	1	0	0	0	1	0	0	1	1	0	3
4	0	1	0	0	0	1	1	0	1	1	0	4
5	0	1	1	0	0	1	1	0	1	1	1	5
6	0	1	1	0	0	1	1	1	1	1	1	6
7	1	0	0	0	1	1	1	0	1	1	1	7
8	1	0	0	1	1	1	1	0	1	1	1	8
9	1	0	1	0	0	1	1	0	1	1	1	9
A	1	0	1	1	0	0	1	1	1	1	0	A
B	1	1	0	0	0	0	1	1	1	0	1	B
C	1	1	0	0	0	0	1	1	1	1	1	C
D	1	1	1	0	0	0	0	1	1	1	1	D
E	1	1	1	0	1	0	0	1	1	1	1	E
F	1	1	1	1	0	0	0	1	1	1	1	F

Circuito:



Simulação:

Simulation Waveform Editor - C:/Users/leomi/OneDrive/Documents/GitHub/CD-NEANDER/QUARTUS\_CD/Idm\_disp\_7seg - Idm\_disp\_7seg - [simulation/qsim/Idm\_disp\_7seg.sim.vwf (Read-Only)]

File Edit View Simulation Help

