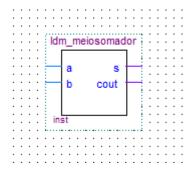
Aluno: Leonardo Dasso Migotto (tudo pode ser encontrado neste repositório do Github)

Cartão: 00325098

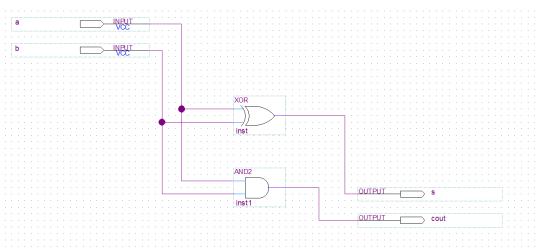
Nome do projeto: Idm\_meiosomador

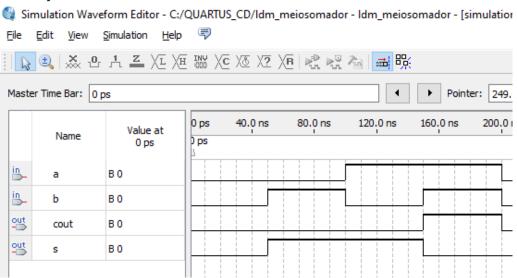
Descrição: Circuito do Meio Somador.

### Símbolo:



## Circuito:



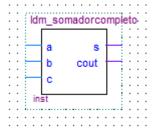


Cartão: 00325098

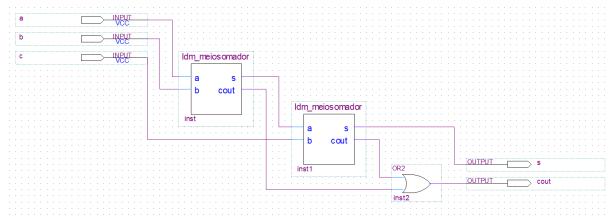
Nome do projeto: ldm\_somadorcompleto

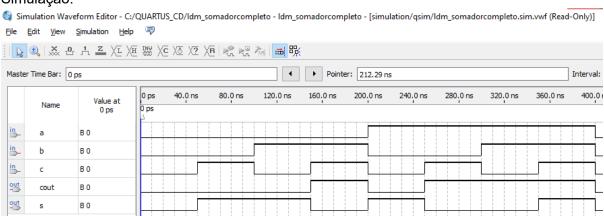
Descrição: Circuito do Somador Completo.

### Símbolo:



## Circuito:

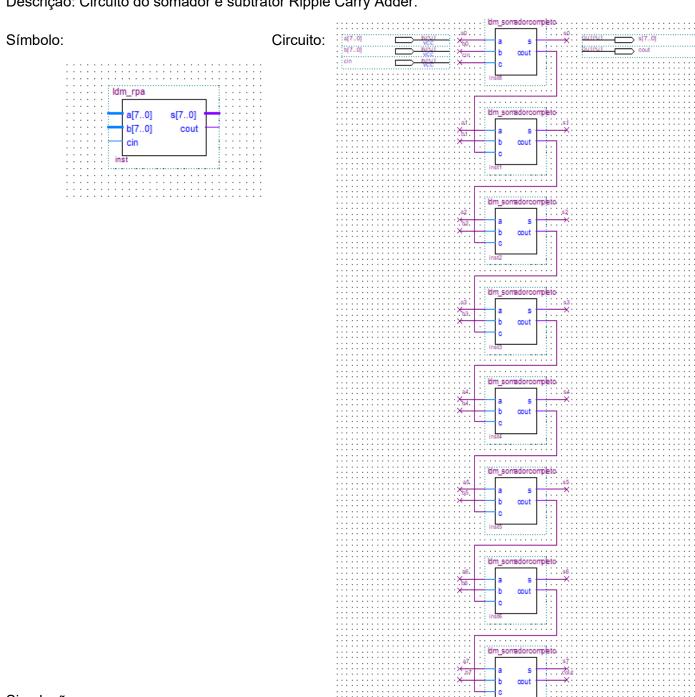




Cartão: 00325098

Nome do projeto: Idm\_rca

Descrição: Circuito do somador e subtrator Ripple Carry Adder.



# Simulação:

mulation Waveform Editor - C:/QUARTUS\_CD/ldm\_rpa - ldm\_rpa - [simulation/qsim/ldm\_rpa.sim.vwf (Read-Only)]

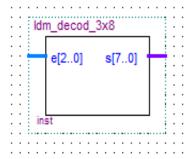
🔽 🔍 🗻 🕾 4. 🚣 🕮 🕮 🔀 🗷 🗷 🗷 🗷 🙈 🖼 🛼 240.0 ns Value at 0 ps 42 \ 48 \ 54 \ 60 \ 66 \ 72 \ 78 \ 84 \ 90 \ 96 \ 102 \ 108 \ 114 \ 120 \ 126 \ 132 \ 138 \ 144 \ 150 \ 156 \ 162 \ 168 \ 174 \ 180 \ 186  $40 \times 48 \times 56 \times 64 \times 72 \times 80 \times 88 \times 96 \times 104 \times 112 \times 120 \times 128 \times 136 \times 144 \times 152 \times 160 \times 168 \times 176 \times 184 \times 192 \times 200 \times 208 \times 216 \times 224 \times 232 \times 240 \times 248 \times 100 \times$ υo U O \* U O 52 | 66 | 80 | 94 | 108 | 122 | 136 | 150 | 164 | 178 |

Cartão: 00325098

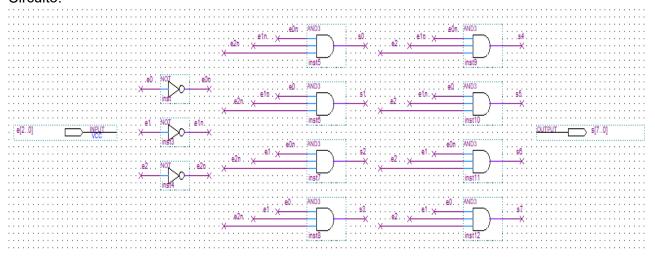
Nome do projeto: ldm\_decod\_3x8

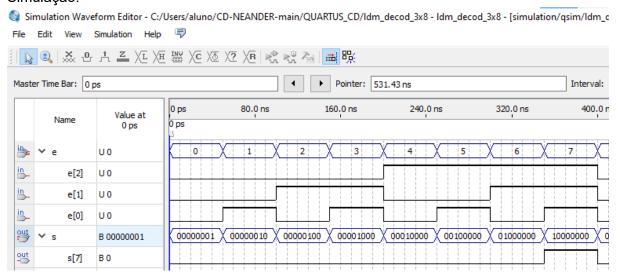
Descrição: Circuito do Decodificador 3x8

### Símbolo:



## Circuito:



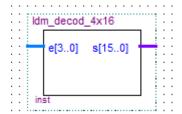


Cartão: 00325098

Nome do projeto: ldm\_decod\_4x16

Descrição: Circuito do Decodificador 4x16

### Símbolo:



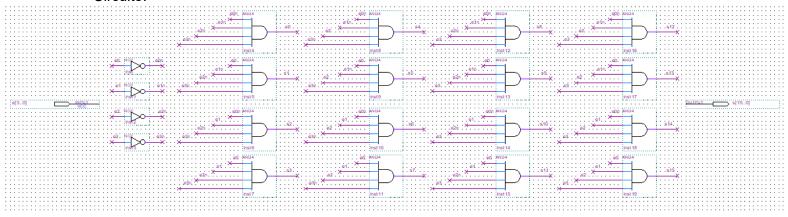
## Circuito:

Value at 0 ps

0000001000000000

0000010000000000

B 0000 B 0000000000





0000100000000000

0001000000000000

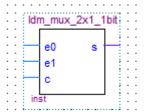
10000000000000000

Cartão: 00325098

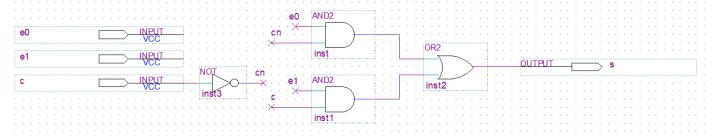
Nome do projeto: ldm\_mux\_2x1\_1bit

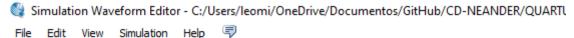
Descrição: Circuito do Multiplexador 2x1 de 1bit

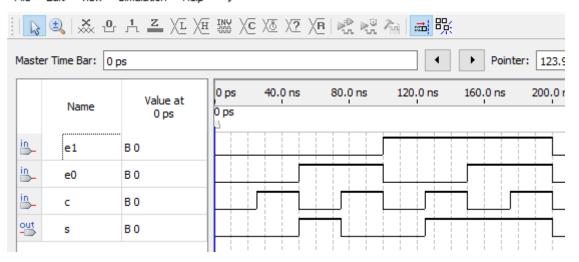
### Símbolo:



# Circuito:





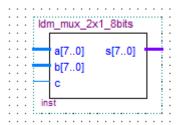


Cartão: 00325098

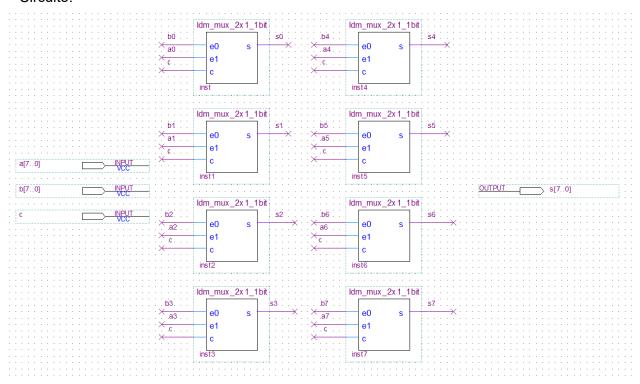
Nome do projeto: ldm\_mux\_2x1\_8bits

Descrição: Circuito do Multiplexador 2x1 de 8bits

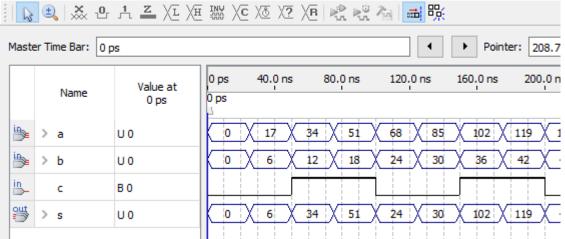
### Símbolo:



## Circuito:





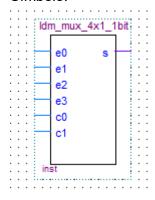


Cartão: 00325098

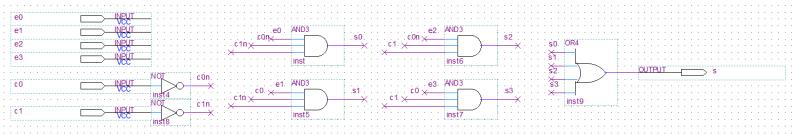
Nome do projeto: ldm\_mux\_4x1\_1bit

Descrição: Circuito do Multiplexador 4x1 de 1bit

### Símbolo:



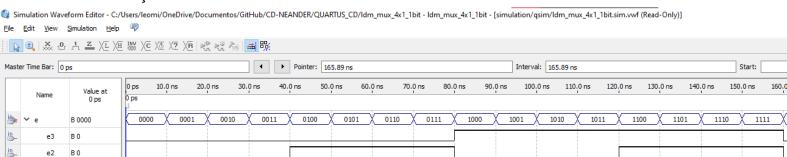
## Circuito:



## Simulação:

ВО

B 00

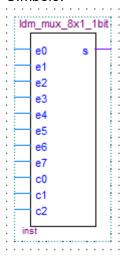


Cartão: 00325098

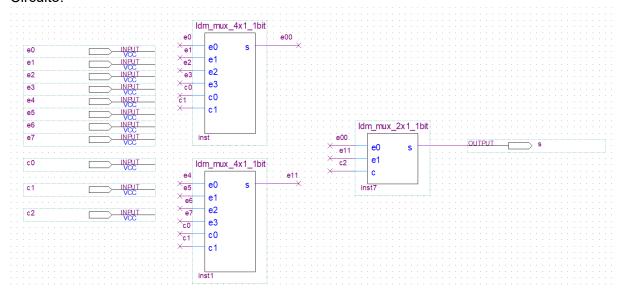
Nome do projeto: ldm\_mux\_8x1\_1bit

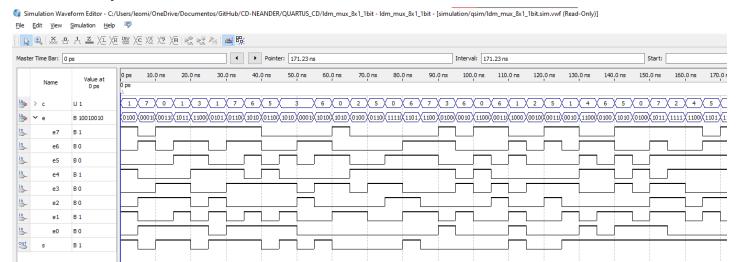
Descrição: Circuito do multiplexador 8x1 de 1 bit

### Símbolo:



### Circuito:



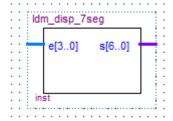


Cartão: 00325098

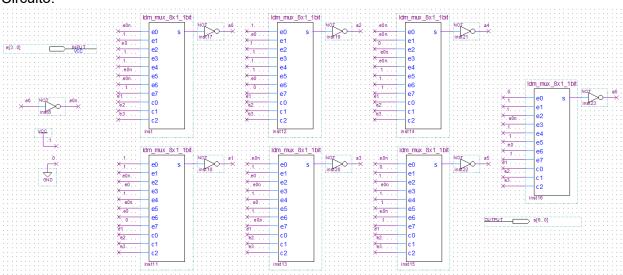
Nome do projeto: ldm\_disp\_7seg

Descrição: Circuito do display de 7 segmentos

### Símbolo:



## Circuito:



# Simulação:

out

out

out

out — s[3] B 0

s[2] B 0

s[0] B 0

s[1] B 0

