

25 MIPS, 64 kB Flash, 16-Bit ADC, 64-Pin Mixed-Signal MCU

Analog Peripherals

Two 16-Bit ADCs

- ±0.75 LSB INL; no missing codes
- Programmable throughput up to 1 Msps (each ADC)
- 1 external input each; programmable as two single-ended or one differential ADC
- DMA to XRAM or external memory interface
- Data-dependent windowed interrupt generator

Three Comparators

- 16 programmable hysteresis values
- Configurable to generate interrupts or reset

Internal Voltage Reference

Precision V_{DD} Monitor/Brown-out Detector

On-Chip JTAG Debug & Boundary Scan

- On-chip debug circuitry facilitates full speed, non-intrusive in-system debug (no emulator required)
- Provides breakpoints, single stepping, watchpoints, stack monitor
- Inspect/modify memory and registers
- Superior performance to emulation systems using ICE-chips, target pods, and sockets
- IEEE1149.1 compliant boundary scan

High-Speed 8051 µC Core

- Pipelined instruction architecture; executes 70% of instructions in 1 or 2 system clocks
- Up to 25 MIPS throughput with 25 MHz system clock
- Expanded interrupt handler

Memory

- 4352 bytes data RAM
- 64 kB Flash; in-system programmable in 1024-byte sectors (1024 bytes are reserved)

Digital Peripherals

- 24 port I/O; all are 5 V tolerant
- Hardware SMBus™ (I2C™ compatible), SPI™, and two UART serial ports available concurrently
- Programmable 16-bit counter/timer array with six capture/compare modules
- 5 general-purpose 16-bit counter/timers
- Dedicated watchdog timer; bidirectional reset
- Real-time clock mode using timers or PCA

Clock Sources

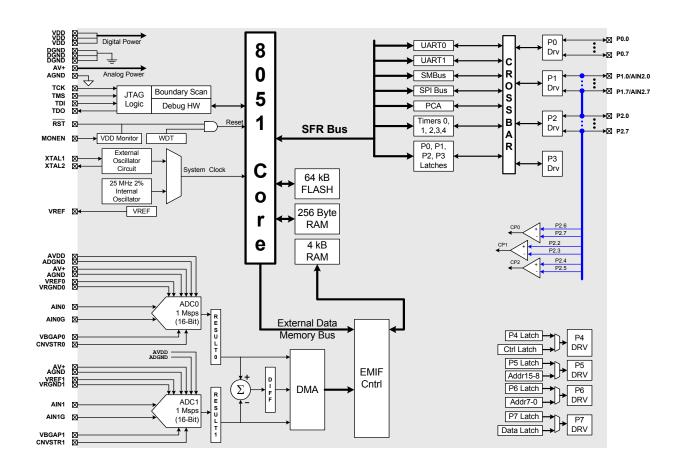
- Internal oscillator: 24.5 MHz, 2% accuracy supports UART operation
- External oscillator: Crystal, RC, C, or Clock
- Can switch between clock sources on-the-fly

Supply Voltage: 2.7 to 3.6 V

- Typical operating current: 18 mA at 25 MHz
- Multiple power saving sleep and shutdown modes

64-Pin TQFP

Temperature Range: -40 to +85 °C



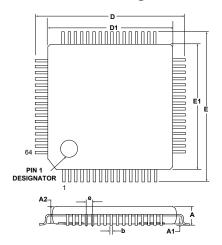
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Selected Electrical Specifications

 $(T_A = -40 \text{ to } +85 \text{ C}^\circ, V_{DD} = 2.7 \text{ V} \text{ unless otherwise specified})$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GLOBAL CHARACTERIS	TICS				
Supply Voltage		2.7		3.6	V
Supply Current	Clock = 25 MHz		18		mA
(CPU active)	Clock = 1 MHz		0.7		mA
	Clock = 32 kHz; V _{DD} Monitor Enabled		20		μA
Supply Current	Oscillator not running; V _{DD} Monitor		0.1		μA
(shutdown)	Disabled				
Clock Frequency Range		DC		25	MHz
16-BIT A/D CONVERTERS	8				
Resolution			16		bits
Integral Nonlinearity	Single-ended Mode		±0.75	±2	LSB
	Differential Mode		±0.50	±1	LSB
Differential Nonlinearity	Guaranteed Monotonic		±0.5	±1	LSB
Signal-to-Noise Plus	Fin = 10 kHz, Single-ended		86		dB
Distortion	Fin = 10 kHz, Differential		89		dB
Total Harmonic Distortion	Fin = 10 kHz, Single-ended		96		dB
	Fin = 10 kHz, Differential		103		dB
Spurious-Free Dynamic	Fin = 10 kHz, Single-ended		97		dB
Range	Fin = 10 kHz, Differential		104		dB
Throughput Rate				1	Msps
Input Voltage Range	Single-ended (AlNn–AlNnG)	0		V_{REF}	V
	Differential (AIN0–AIN1)	$-V_{REF}$		V_{REF}	V
Power Supply Current	Operating Mode, 1 Msps				
(each ADC)	(AVDD + AV+)		5.5		mA
,	Shutdown Mode		1		μA

Package Information



	MIN (mm)	NOM (mm)	MAX (mm)
A	-	ı	1.20
A 1	0.05	-	0.15
A2	0.95	-	1.05
b	0.17	0.22	0.27
D	-	12.00	-
D1	-	10.00	-
е	-	0.50	-
E	-	12.00	-
E1	-	10.00	-

C8051F060DK Development Kit

