



A ₁	555 timer	B ₁	counter (4520)	B ₁₅	DFF (4013)	C ₁	NOT (4069) D ₁ , 14-segment display
A ₂		B ₂	buffer (4050)	B ₁₆	E PROM	C ₂	SRI (4015)
A ₃	DFF (4013)	B ₃	counter (4520)	B ₁₇	E PROM	C ₃	SRII (4015)
A ₄	AND (4081)	B ₄	SRI (4015)	B ₁₈	JKFF (4027)	C ₄	SR III (4015)
A ₅	counter (4520)	B ₅	NOT (4069)	B ₁₉	OR (4071)	C ₅	SR IV (4015)
A ₆	JKFF (4027)	B ₆	555 timer	B ₂₀	MUX (4051)	C ₆	SR V (4015)
A ₇	counter (4520)	B ₇	ORI (4071)	B ₂₁	AND (4081)	C ₇	NOT (4069)
A ₈	AND (4081)	B ₈	DFF (4013)			C ₈	SR VI (4015)
A ₉	DFF (4013)	B ₉	MUX (74LS257)			C ₉	NOT (4069)
A ₁₀	XOR (4001)	B ₁₀	MUX (74LS257)			C ₁₀	SR VII (4015)
A ₁₁	DFF (4013)	B ₁₁	AND (4082)				
A ₁₂	NOT (4069)	B ₁₂	RAM I (2 ¹⁴)				
A ₁₃	buffer (4050)	B ₁₃	counter (4520)				
		B ₁₄	RAM II (2 ¹⁴)				