

Hardware

Presentation

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Introduction

HDWR - Target Learning Outcome

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- **Map** boolean circuits, time and memory concepts in a CPU by **explaining** links between them.
- **Explain** the specificities and technical constraints of a given CPU architecture by linking them to the syntax and semantics of an assembly language.
- **Produce** an assembly program involving the usage of a stack to call functions and save a context.

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- Week 36*
- Week 37*
- Week 38*

Moodle

All the ressources you need will be provided on the HDWR moodle page:

- Slides
- Lectures
- Tutorials
- Announcements and communications

Exams

Each week will begin by a 1h exam on Moodle to evaluate your learnings from the previous week.

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Final grade will be calculated as follows: Each of the 3 moodle exams and the final assembly teamwork are worth $1/4$ of the final grade.

Each week, you will be provided an **optional** exam on Moodle.

The content of theses exams will be similar to that of the mandatory ones.

Theses exams **will not** be part of the final grade and are only here to let you prepare for the mandatory tests.

Assembly teamwork

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During week 38, you will be asked to work in group of two on a simple assembly program.

This work will be graded during the last practical session.

You **must** register your group to the HDWR moodle page.

QUESTIONS ?

CPU Schematic

