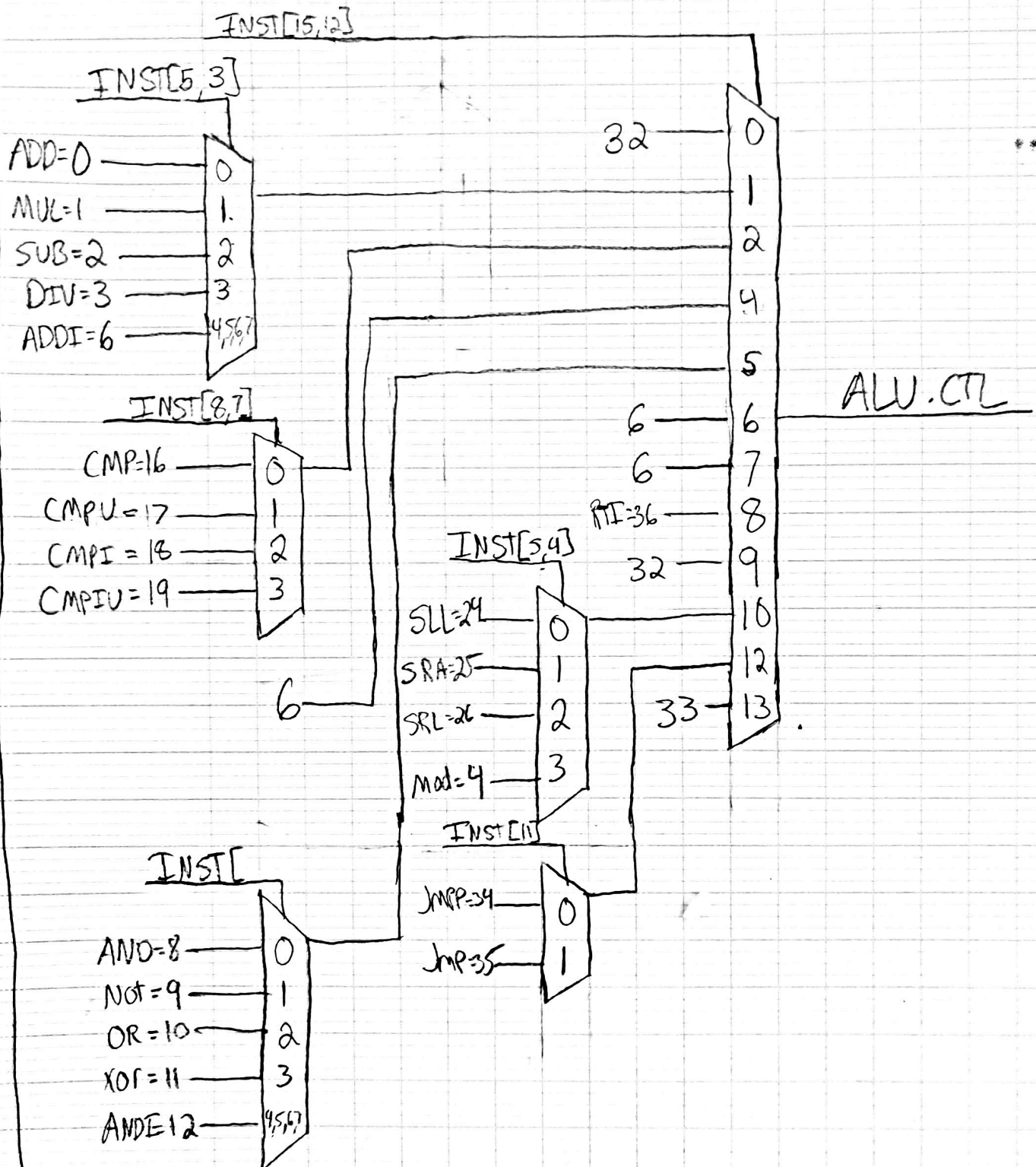
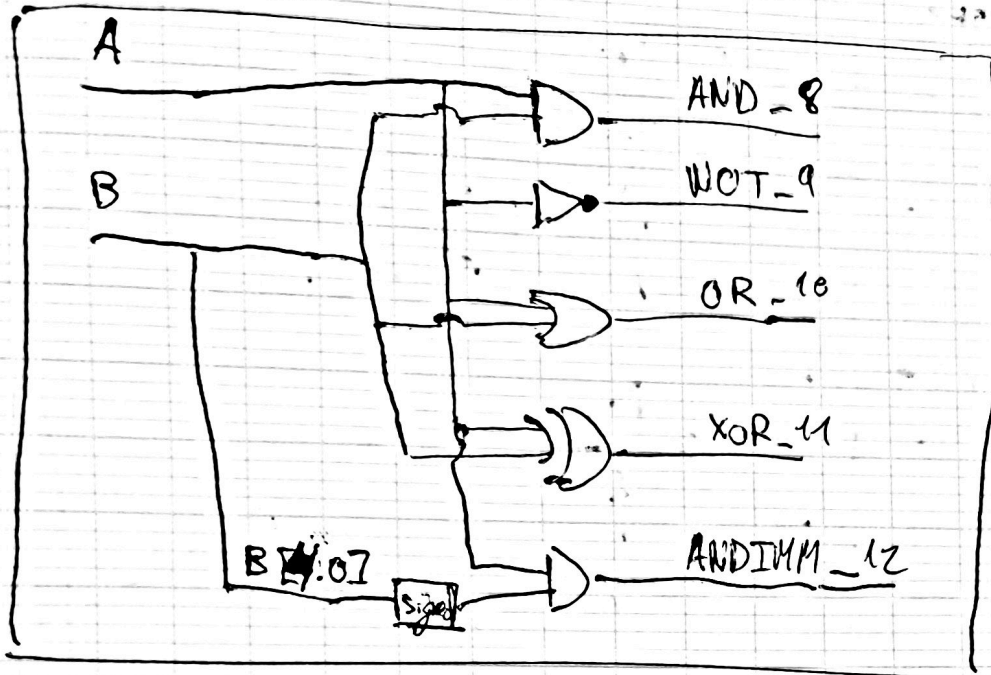


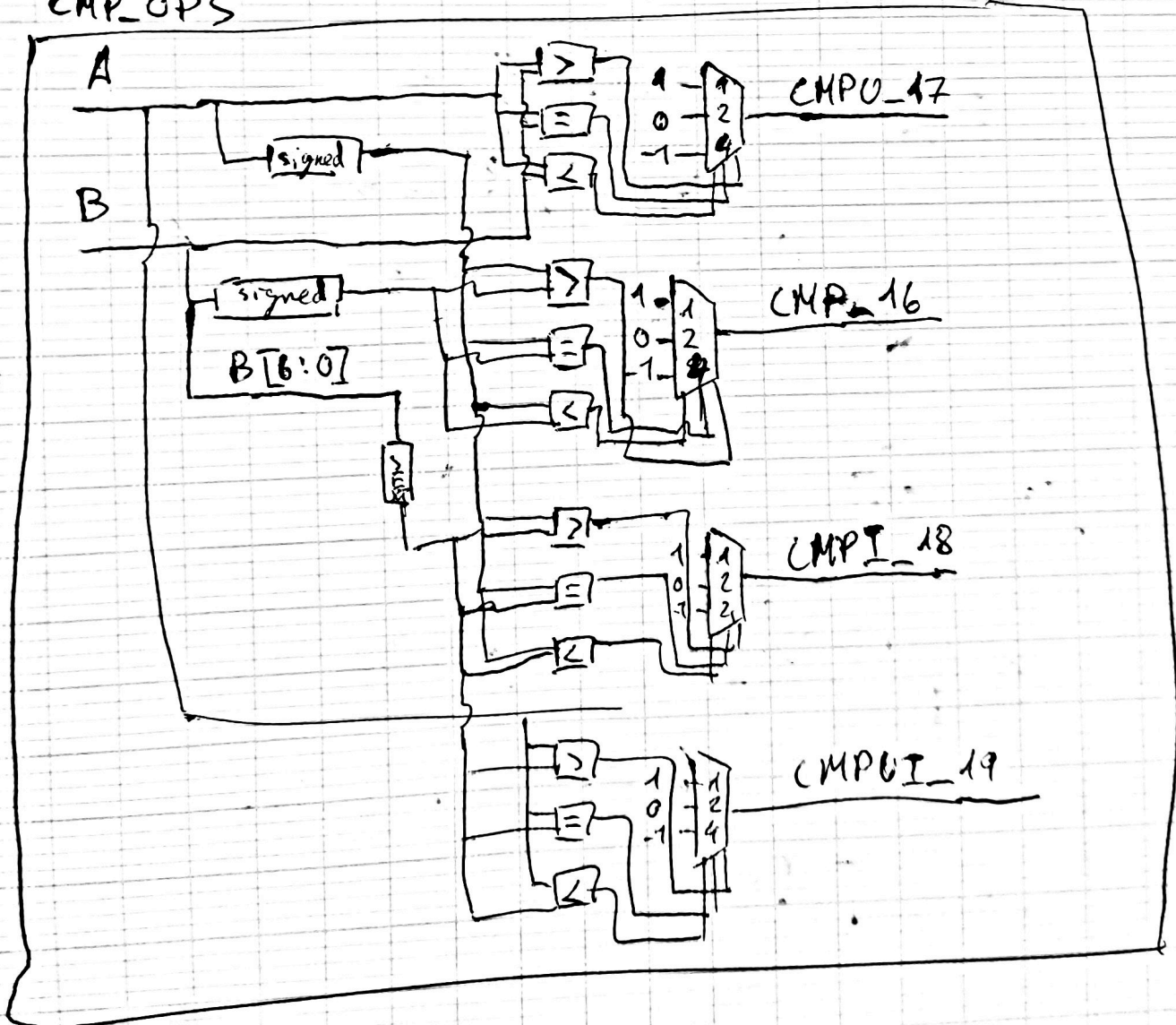
# ALU CTL



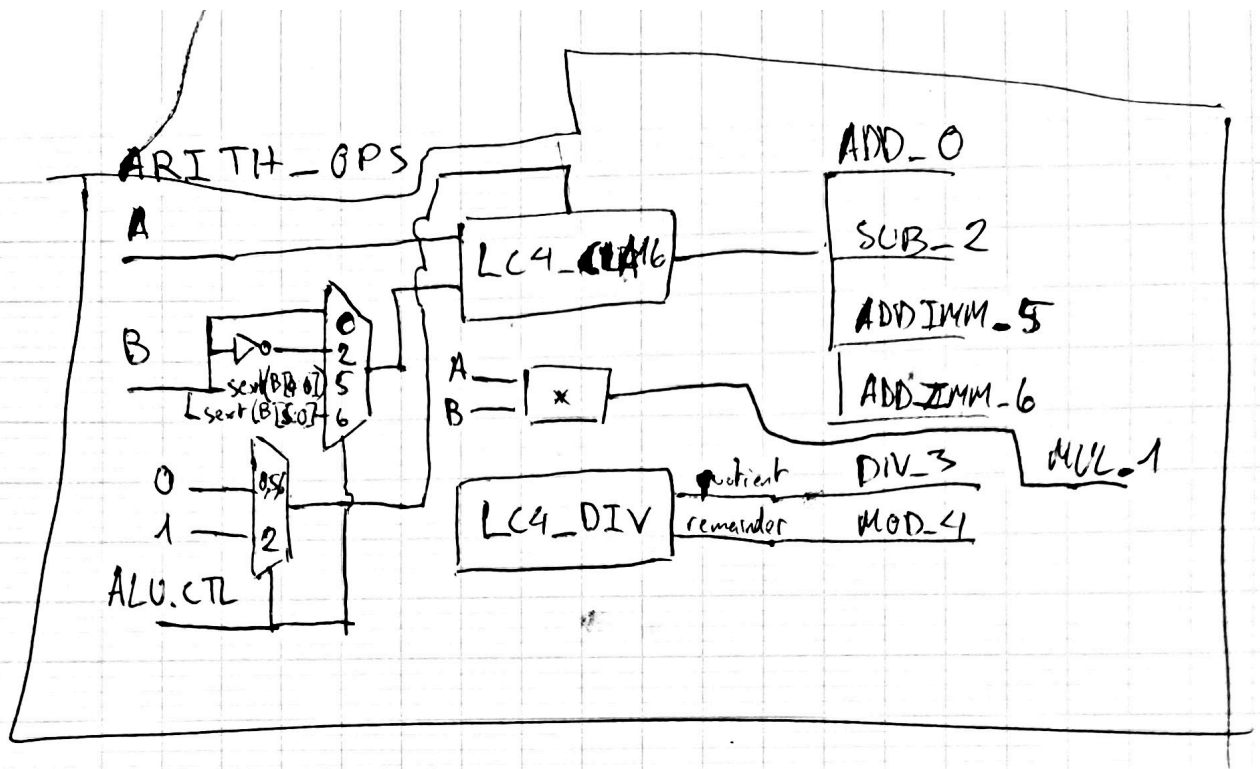
## LOG\_OPS



## CMP\_OPS





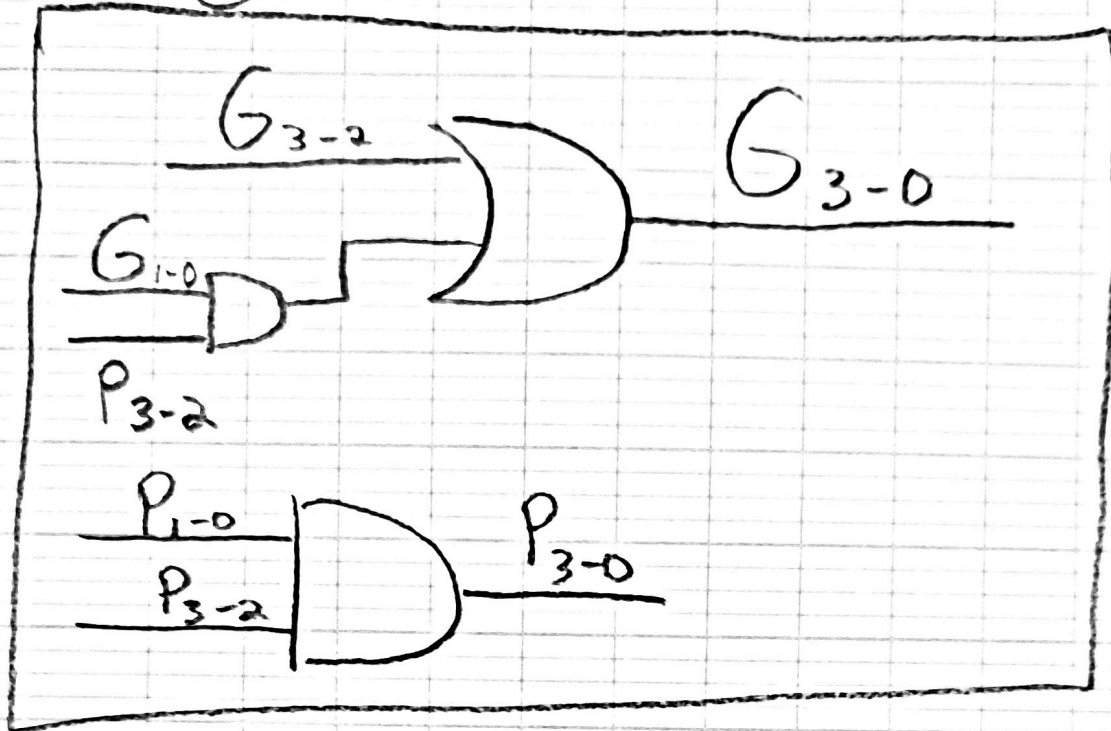


## ALU\_OUT

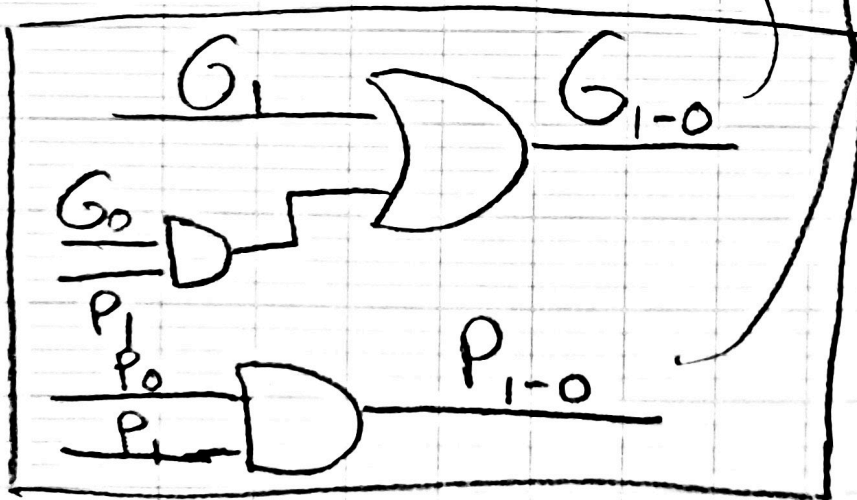
ADD_0	0
MUL_1	1
SUB_2	2
DIV_3	3
MOD_4	4
ADDIMM_5	5
ADDIMM_6	6
AND_8	8
WOT_9	9
OR_10	10
XOR_11	11
ANDIMM_12	12
CMP_16	16
CMPO_17	17
CMPI_18	18
CMPII_19	19
SLL_24	24
SRA_25	25
SRL_26	26
CONST_32	32
HICONST_33	33

ALU\_CTL

GP4



GP2



to create  
signals above,  
use 2 GP2