

Yin-Chiuan Chen

☎ (+886) 918-459-923 | ✉ leovincentses@gmail.com | 📷 leovincentses | 🌐 leochen17

Education

National Taiwan University (NTU)

Taipei, Taiwan

M.S. IN GRADUATE INSTITUTE OF NETWORKING AND MULTIMEDIA (GINM)

2019.9 - 2021.10

- Overall GPA: 4.21/4.30 (2nd/44)
- Thesis Topic: How to Achieve Performance and Fairness in Task Executions over a Hybrid DRAM-NVM System
- Related Courses: Advanced Operating Systems, Virtual Machines

National Chiao Tung University (NCTU)

Hsinchu, Taiwan

B.S. IN ELECTRICAL AND COMPUTER ENGINEERING (ECE)

2015.9 - 2019.6

- Overall GPA (157 credits): 4.17/4.30 (2nd/44)
- Related Courses: Operating Systems Design And Implementation (OSDI), Introduction to Operating Systems, Computer Organizations, Introduction to Algorithms, Data structures

Experience

Graduate Student Researcher

Taipei, Taiwan

EMBEDDED SYSTEM AND WIRELESS NETWORKING LAB, NATIONAL TAIWAN UNIVERSITY

2019.9 - 2021.10

- Advisor: Prof. Tei-Wei Kuo, Computer Science and Information Engineering, National Taiwan University
- Co-Advisor: Prof. Yuan-Hao Chang, Institute of Information Science, Academia Sinica
- Research topic: Explore on the challenges brought by the emerging NVM devices. The traditional design concept of the operating system is not suitable for the modern storage devices. Both the memory management and process scheduling should be aware of this trending.

Network Administrator

Taipei, Taiwan

EMBEDDED SYSTEM AND WIRELESS NETWORKING LAB, NATIONAL TAIWAN UNIVERSITY

2019.9 - 2020.8

- Network infrastructure maintenance
- Server maintenance and mailing list migration
- Deployed web pages with responsive web design.

Network Administrator

Hsinchu, Taiwan

HIGH-PRECISION ANALOG AND MIXED-SIGNAL CIRCUITS DESIGN LAB, NATIONAL CHIAO TUNG UNIVERSITY

2018.9 - 2019.9

- Network infrastructure maintenance
- Web pages and Electronic Design Automation (EDA) servers deployment

Honors & Awards

Scholarship from the Memorial Scholarship Foundation to Mr. Lin Hsiung Chen

Taipei, Taiwan

MEMORIAL SCHOLARSHIP FOUNDATION TO MR. LIN HSIUNG CHENG

2018.9

- Only fifty students are selected among all the applicants in Taiwan.
- Average score from 2017.9 to 2018.6 is 94.04/100.00.

Academic Achievement Award

Hsinchu, Taiwan

NATIONAL CHIAO TUNG UNIVERSITY (NCTU)

2016.2 - 2018.6

- Earned scholarships (top 5% among the class) for five contiguous semesters.

Freshmen Programming Contest

Hsinchu, Taiwan

NATIONAL CHIAO TUNG UNIVERSITY (NCTU)

2016.9

- Won third place among 30 teams in the intramural competition.

Calculus Contest Award

NATIONAL CHIAO TUNG UNIVERSITY (NCTU)

Hsinchu, Taiwan

2016.1

- Won first place among 800 students in the intramural competition.

Academic Projects

C- Compiler

INTRODUCTION TO COMPILER DESIGN

Hsinchu, Taiwan

2018.9 - 2019.1

- Implemented the C- compiler which understands the C- language and generates the corresponding JAVA Bytecode.
- Created the lexical analyzer and parser generator with the help of lex and yacc respectively.

IDE with multi-language supports

JAVA PROGRAMMING

Hsinchu, Taiwan

2017.2 - 2017.6

- Designed and implemented an Integrated Development Environment in java.
- Supported multiple programming languages such as C/C++, JAVA, and Python.

Predict the game results of NBA Using Deep Learning

ELECTRICAL ENGINEERING PROJECTS

Hsinchu, Taiwan

2017.2 - 2017.6

- Trained the deep neural network model and predicted the game results of NBA.
- Crawled the NBA game statistics from the ESPN websites and preserved the data in the SQL server.

Fantasy Circuit

ADVANCED OBJECT-ORIENTED PROGRAMMING

Hsinchu, Taiwan

2016.9 - 2017.1

- Designed and implemented the circuit analyzer which provides an Graphic User Interface (GUI) to draw the specific circuit schema.
- Analyzed the circuit schema by solving the node equations and plotted the result voltage waveform for any circuit node.

Skills

Operating System, Memory Management, Process Scheduling, Algorithm, Database, Network Programming

Programming Languages: C/C++, Python, Java, Verilog HDL, C#, Visual C++

Others: Linux, Assembly, Git, GNU Make, MySQL

Publications

Journal Papers

- **Yin-Chiuan Chen**, Chun-Feng Wu, Yuan-Hao Chang, and Tei-Wei Kuo, "Exploring Synchronous Page Fault Handling", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD). (Integrated with ACM/IEEE CODES+ISSS'22).

Conference Papers

- **Yin-Chiuan Chen**, Chun-Feng Wu, Yuan-Hao Chang, and Tei-Wei Kuo, "Exploring Synchronous Page Fault Handling", ACM/IEEE International Conference on Hardware/Software Codesign and System Synthesis (CODES+ISSS), Hybrid-Shanghai, Oct. 7-14, 2022. (Journal Track, Integrated with IEEE TCAD). **(Best Paper Nomination) (Top Conference)**