

Mem[(PC) + SEXT(IR[8:0])] < - SR

SR1MUX: IR[11:9] ALUK (pass through)

Gate.ALU LD.MDR

ADDR1MUX: PC

ADDR2MUX: SEXT(IR[8:0] MARMUX: Addr.Addr

Gate.MarMUX

LD.MAR Mem.en /w