

Homework 5

Homework instructions

1. **(3 points)** Study Examples 4.1, 4.2, and 4.5 in the text (*pages 103, 104 & 108 resp.*)
 . Now do exactly the same analysis (using the same reasoning and the same format) for the instruction BRnzp (the conditional branch instruction). You will need to look up the exact meaning and functioning of the NZP condition codes to do this.
2. **(2 points)** Example 4.3 (*p. 107*) of the text describes an instruction of the IA-32, the ISA of the Intel Pentium. Study this, and do q. 4.13
(Study it carefully, noting the "relative" or register-pointer addressing mode used for both the load & store steps).
3. **(2 points)** q. 4.7
4. **(2 points)** q. 4.8
5. **(1 point)** q. 4.16