AND   DR ← AND(SR1, SR2)
BR   PC ← PC + SEXT([IR8:0])
JMP   PC ← (BaseReg)
JSR   R7 ← PC
        PC ← PC + SEXT(IR[10:0])
JSRR R7 ← PC
        PC ← (BaseReg)
LD   DR ← Mem[PC + SEXT(IR[8:0])
LDI   DR ← Mem[Mem[PC + SEXT(IR[8:0])]
LDR   DR ← Mem[BaseReg + SEXT(IR[5:0])]
LEA   DR ← PC + SEXT(IR[8:0])
NOT   DR ← NOT(SR)
RET   PC ← R7
ST   Mem[(PC) + SEXT(IR[8:0])] ← SR
STI   Mem[Mem[PC + SEXT(IR[8:0])] ← SR
STR   Mem[(BaseReg) + SEXT(IR[5:0])] ← SR
TRAP R7 ← PC
        PC ← Mem[ZEXT(IR[7:0])]