

1010-DR-PCoffset9

 $DR \leftarrow Mem[Mem[PC+SEXT(IR[8:0])]]$

Addr2MUX: SEXT(IR[8:0])

MARMUX: Addr. Adder

Gate.MARMUX

LD.MAR

Mem.en /R wait for ready signal

LD.MDR Gate.MDR LD.MAR Mem.en /R

LD.MDR Gate.MDR

DRMUX: IR[11:9]

LD.Reg