

## Homework 8

### Homework instructions

1. **(3 points)** Given the data path of the LC-3 as on p.142, give a complete description of the JSRR instruction, as follows:
- a) Give the RT (Register Transfer) specification of the instruction.
- b) List, in the correct sequence, every control signal set by the FSM to implement this instruction. Remember that not every tri-state device may be actually depicted on the schematic – but you must still specify the control signal if it is involved (make up names for any control signals that are not shown on the schematic).
- For multi-bit control signals, you must specify the actual value where possible:  
e.g. if the control signal is a 2-bit MUX selector, selecting for the input that is third from the right, then you must specify the value 10

11	10	01	00

If several control signals act at the same time, indicate that fact; otherwise list them in sequence.

2. **(3 points)** Following the same instructions, give a complete description of the TRAP instruction
3. 9.10 (1 point)
4. 9.15 (1 point)
5. 9.19 (2 points)
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