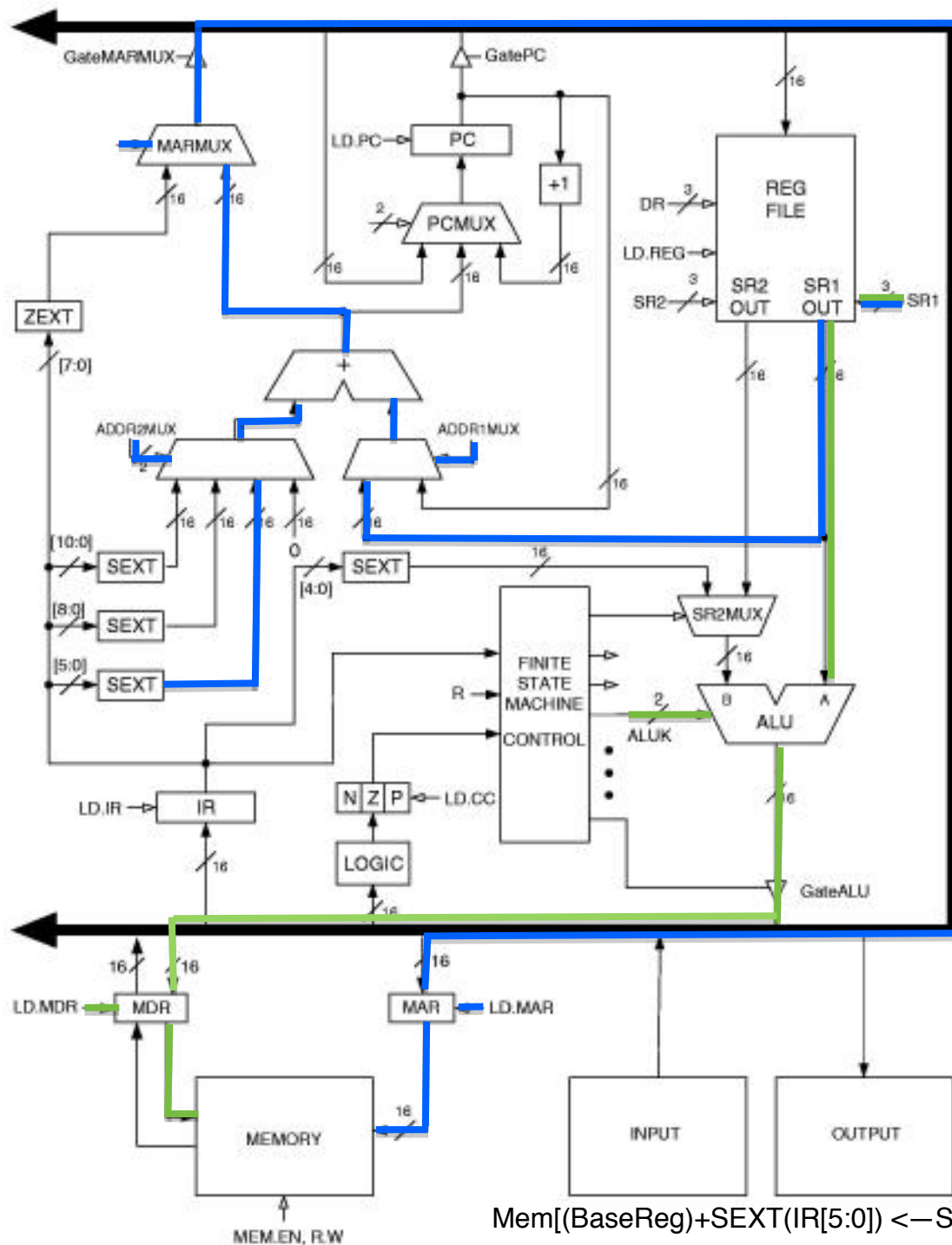


Last name: STR

First name: _____



0111-SR-BaseR-offset6

$\text{Mem}[(\text{BaseReg}) + \text{SEXT}(\text{IR}[5:0])] \leftarrow \text{SR}$

SR1MUX: IR[11:9]
 ALUK (pass through)
 Gate.ALU
 Gate.MDR
 LD.MDR
 SR1MUX: IR[8:6]
 Addr1MUX: SR1 BUS
 Addr2MUX: SEXT(IR[5:0])
 MARMUX: Addr. Adder
 Gate.MARMUX
 LD.MAR
 Mem.en /w