

that you can run zIIP processors in symmetric multithreading mode (SMT). When running in SMT mode, you can run two threads in parallel on a single zIIP. Check the settings of your processors with the operator command **D M=CPU**. You can see the output of this command in Example 2-1 on page 23. The core status tells you, that this LPAR is running in multithreading (MT) mode 1 for CPs (1 is the only option for CPs), and running in MT mode 2 for zIIPs.

*Example 2-1 Output of D M=CPU operator command*

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D M=CPU
IEE174I 09.38.33 DISPLAY M 231
CORE STATUS: HD=Y   MT=2   MT_MODE: CP=1   zIIP=2
ID   ST   ID RANGE   VP   ISCM   CPU   THREAD STATUS
0000  +   0000-0001   H    FC00  +N
0001  +   0002-0003   M    0000  +N
0002  +   0004-0005   LP   0000  +N
0003  +   0006-0007   LP   0000  +N
0004  +I   0008-0009   H    0200  ++
0005  +I   000A-000B   H    0200  ++
0006  +I   000C-000D   M    0200  ++
0007  +I   000E-000F   M    0200  ++
0008  -   0010-0011
0009  -   0012-0013
000A  -   0014-0015
000B  -   0016-0017
000C  -I   0018-0019
000D  -I   001A-001B
000E  -I   001C-001D
000F  -I   001E-001F

CPC ND = 008561.T01.IBM.02.00000002B7F8
CPC SI = 8561.716.IBM.02.00000000002B7F8
      Model: T01
CPC ID = 00
CPC NAME = ARIES
LP NAME = ARIES22      LP ID = 22
CSS ID = 2
MIF ID = 2

+ ONLINE      - OFFLINE      N NOT AVAILABLE      / MIXED STATE
W WLM-MANAGED

I          INTEGRATED INFORMATION PROCESSOR (zIIP)
CPC ND    CENTRAL PROCESSING COMPLEX NODE DESCRIPTOR
CPC SI    SYSTEM INFORMATION FROM STSI INSTRUCTION
CPC ID    CENTRAL PROCESSING COMPLEX IDENTIFIER
CPC NAME  CENTRAL PROCESSING COMPLEX NAME
LP NAME   LOGICAL PARTITION NAME
LP ID     LOGICAL PARTITION IDENTIFIER
CSS ID    CHANNEL SUBSYSTEM IDENTIFIER
MIF ID    MULTIPLE IMAGE FACILITY IMAGE IDENTIFIER

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For core mode, which is specified in the LOADxx member of SYSn.IPLPARM (see extract from LOADxx member in Example 2-2 on page 24), the **D M=CPU** operator command is internally changed to **D M=CORE**. If a system is running in core mode, Hiperdispatch is also required (HIPERDISPATCH=YES in IEAOPTxx parmlib member).