

- HCD prompts you to select to which partitions the CHPID should have access. Enter a forward slash '/' next to the partitions that you want (see Figure 14-25), and press Enter.

```

*----- Define Access List -----*
                                     Row 22 of 31
Command ==> _____ Scroll ==> CSR

Select one or more partitions for inclusion in the access list.

Channel subsystem ID : 2
Channel path ID . . : EE      Channel path type . : ICP
Operation mode . . . : SPAN   Number of CHPIDs . . : 1

/ CSS ID Partition Name      Number Usage Description
/ 1      MUSCA11             1      OS
_ 1      MUSCA12             2      OS
_ 1      MUSCA13             3      OS
_ 1      MUSCA14             4      OS
_ 1      MUSCA15             5      OS
_ 1      MUSCA16             6      OS
_ 1      MUSCA17             7      OS
_ 1      MUSCA18             8      OS
_ 1      MUSCA19             9      OS
/ 2      MUSCA2A             A      CF      MUSCA2A test CF partition
***** Bottom of data *****
*-----*

```

Figure 14-25 Processors: Define Access List, ICP

Because more partitions than the selected two are defined, the next panel Define Candidate List appears. For this example, we do not add any partitions in the access list. Therefore, press Enter.

HCD returns to the Channel Path List, which shows you the CHPID that was defined (see Figure 14-26).

```

                                     Channel Path List      Row 1 of 5 More:
Command ==> _____ Scroll ==> CSR

Select one or more channel paths, then press Enter. To add use F11.

Processor ID . . . . : MUSCA      Musca
Configuration mode . : LPAR
Channel Subsystem ID : 2

      CHID+      Dyn Entry +
/ CHPID AID/P Type+ Mode+ Sw+ Sw Port Con Mng Description
_ E0    12/1  CS5  SPAN  _ _ _ Y No FC 0172 ICA SR 2 Links
_ E4    16/1  CS5  SPAN  _ _ _ Y No FC 0172 ICA SR 2 Links
_ E8    124/1 CL5  SPAN  _ _ _ Y No FC 0433 Coupling Express LR
_ ED    144/2 CL5  SPAN  _ _ _ Y No FC 0433 Coupling Express LR
_ EE    _ _ _ ICP  SPAN  _ _ _ N No ICP Internal Coupling Peer

```

Figure 14-26 Processors: Channel Path List, ICP