

Table 2-4 lists all the hardware components and maximum capacities that are supported for the DS8910F model 993. When integrated into the ZR1 or LR1, the DS8910F uses the two upper Z iPDUs and shares the console and keyboard with system Z.

*Table 2-4 DS8910F model 993 components*

Features	DS8910F Model 993
Integration	ZR1, LR1, or Standard 19-inch that conforms to EIA 310D
Processor Complex (CEC)	2 - IBM Power Systems Power 9 MTM 9009-22A
I/O Bay Pairs Min / Max	1 / 1
Power 9 Cores per system Min / Max	16 / 16
System Memory Min / Max	192 GB / 512 GB
System NVS Memory Min / Max	8 GB / 32 GB
Host adapters Min / Max <sup>a</sup>	2 / 8
Host adapter ports Min / Max	8 / 32
zHyperLink adapters Min / Max	0 / 4
Flash drives Min / Max	16 / 96
HPFE Gen2 pairs Min / Max	1 / 2
Flash Raid adapter pairs Min / Max	1 / 2
iPDU	2 optional iPDUs with standard rack integration
Power	Single Phase or Three Phase
HMC	2 in Management Enclosure (ME)
Ethernet Switches	2 8-port switches in ME
Keyboard/Monitor	Optional 1 with standard rack integration

a. For more details see "I/O enclosure adapters" on page 46

### 2.2.3 DS8950F Agility Class configuration

The DS8950F Model 553x-996 is available with 1 x 10-core or 2 x 10-core processor / CPC, with a maximum of 16 Fibre Channel/Fibre Connection (FICON) host adapters (HAs) in the base frame and a maximum of 16 Fibre Channel/Fibre Connection (FICON) host adapters (HAs) in the expansion frame.

This model is available with a maximum of 6 zHyperLink adapters for a system with 10-core processor complexes and a maximum of 12 zHyperLink adapters for a system with 2 x 10-core processor / CPC and with an E96 expansion frame.

Figure 2-4 shows the maximum configuration of a DS8950F 996+E96 system.