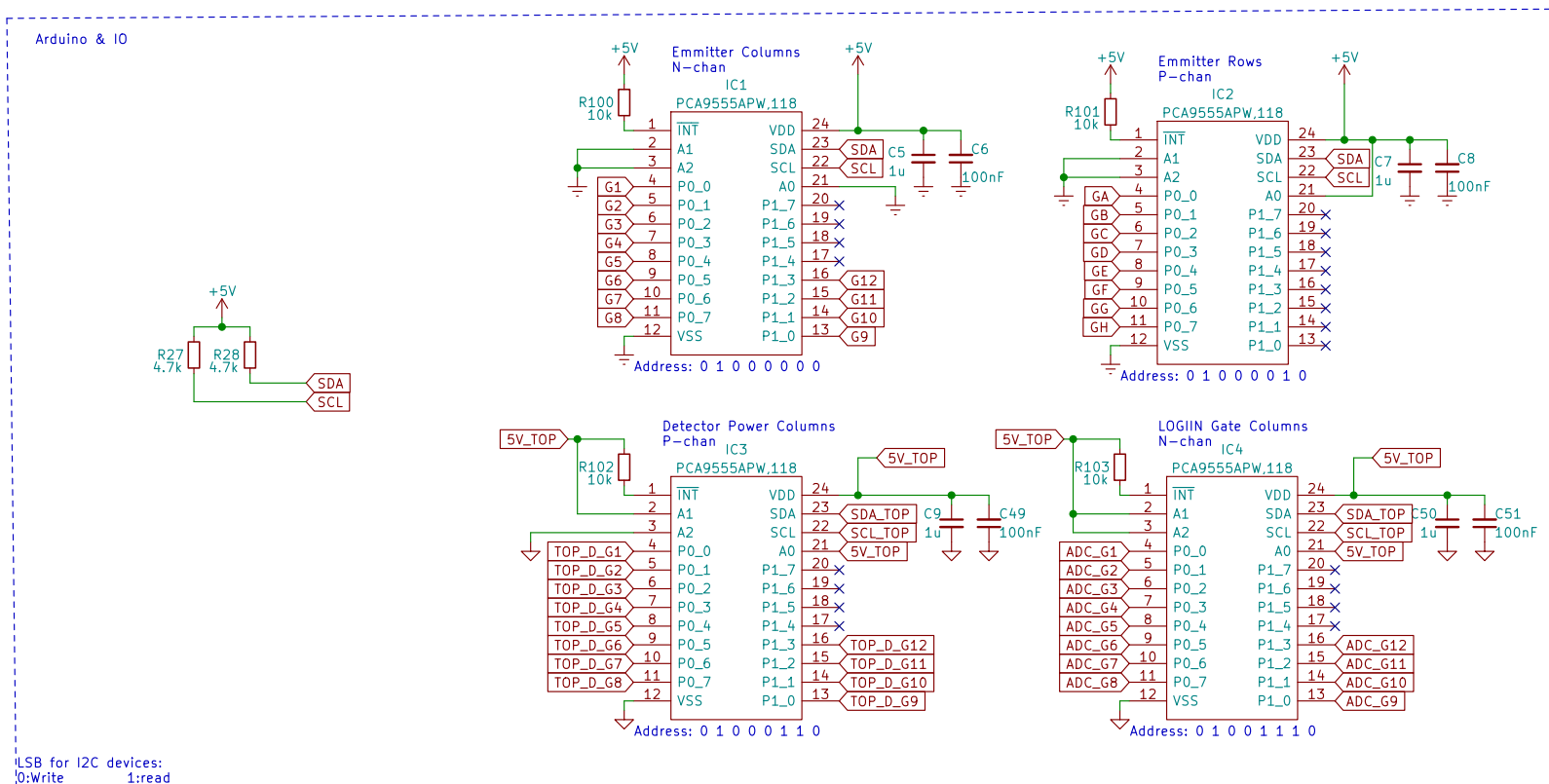
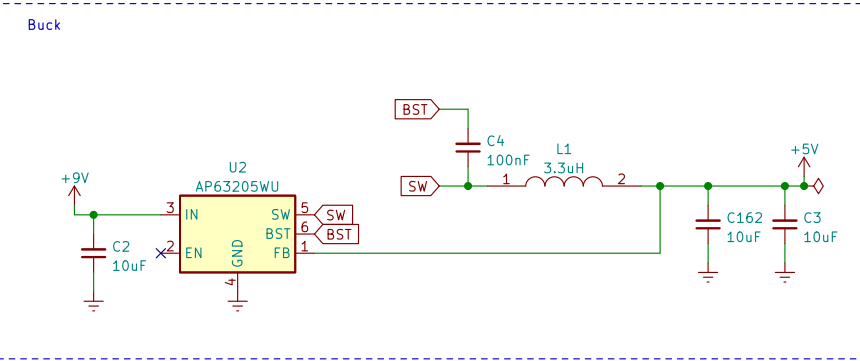
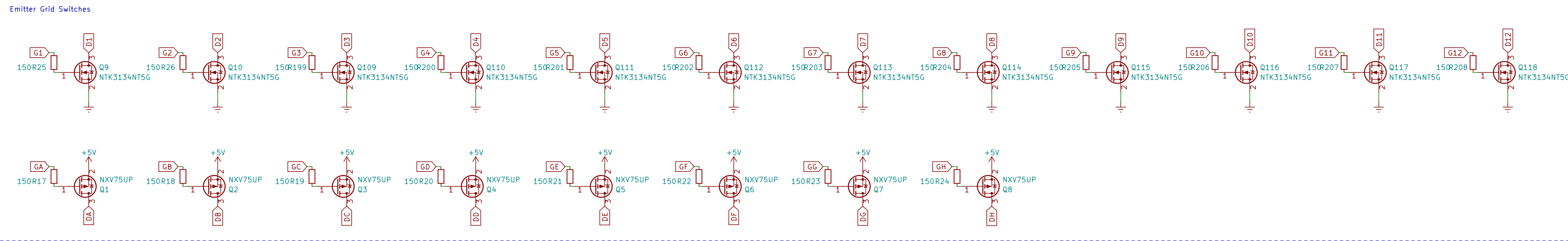
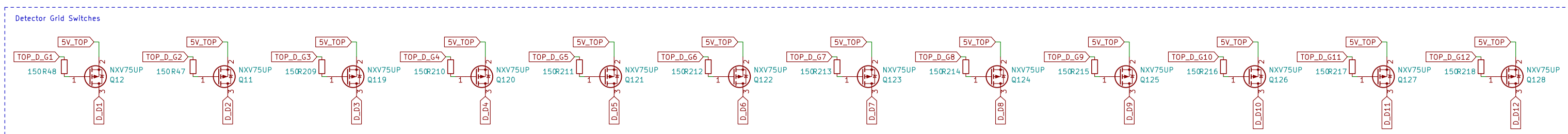
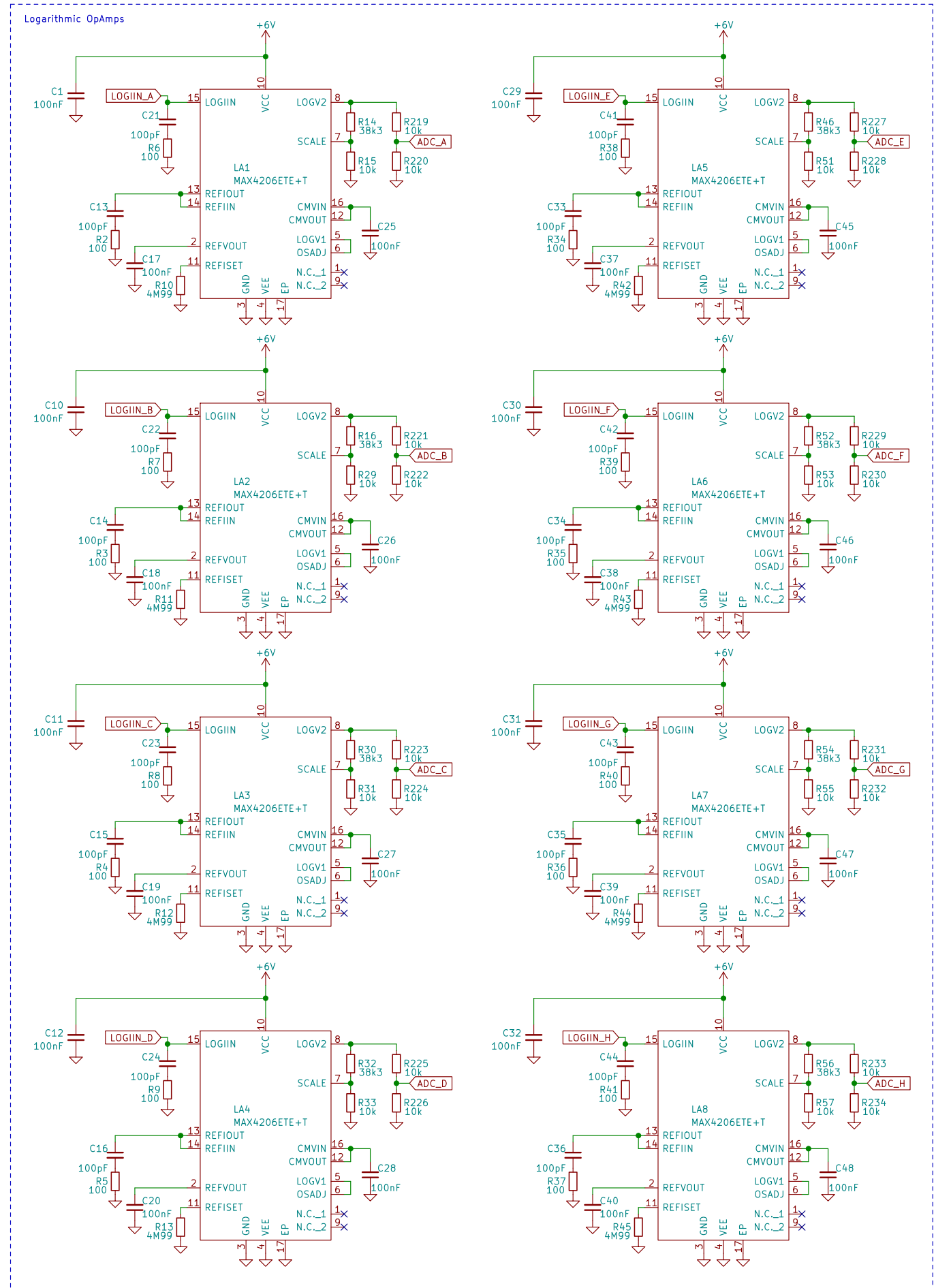
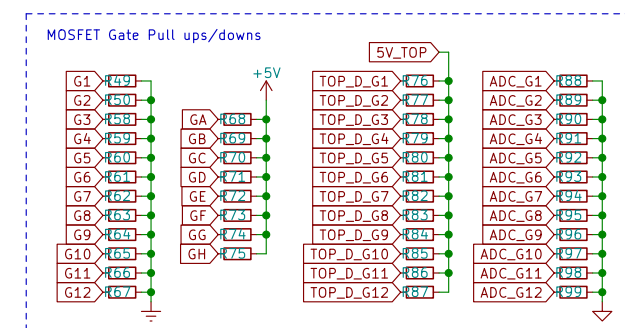
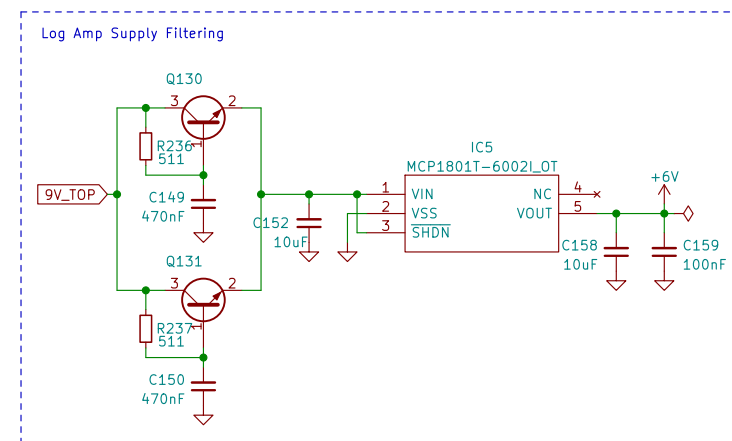
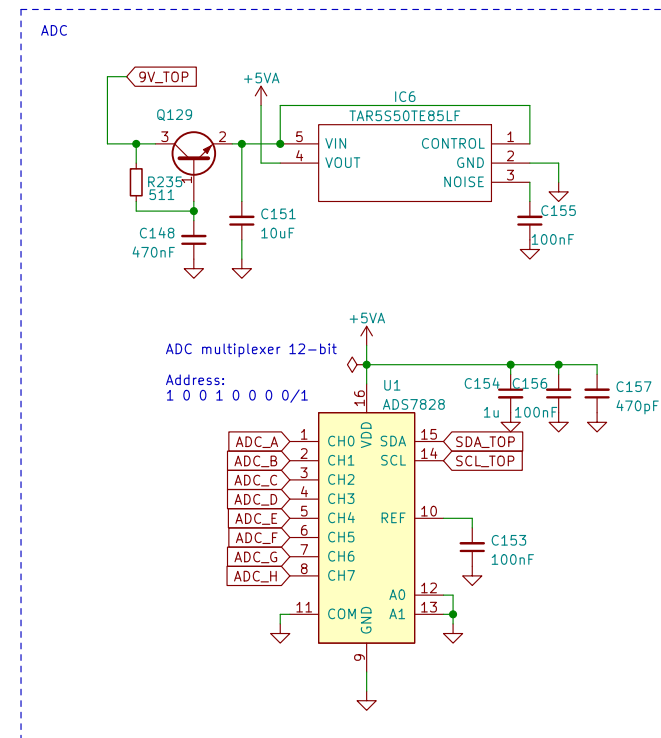
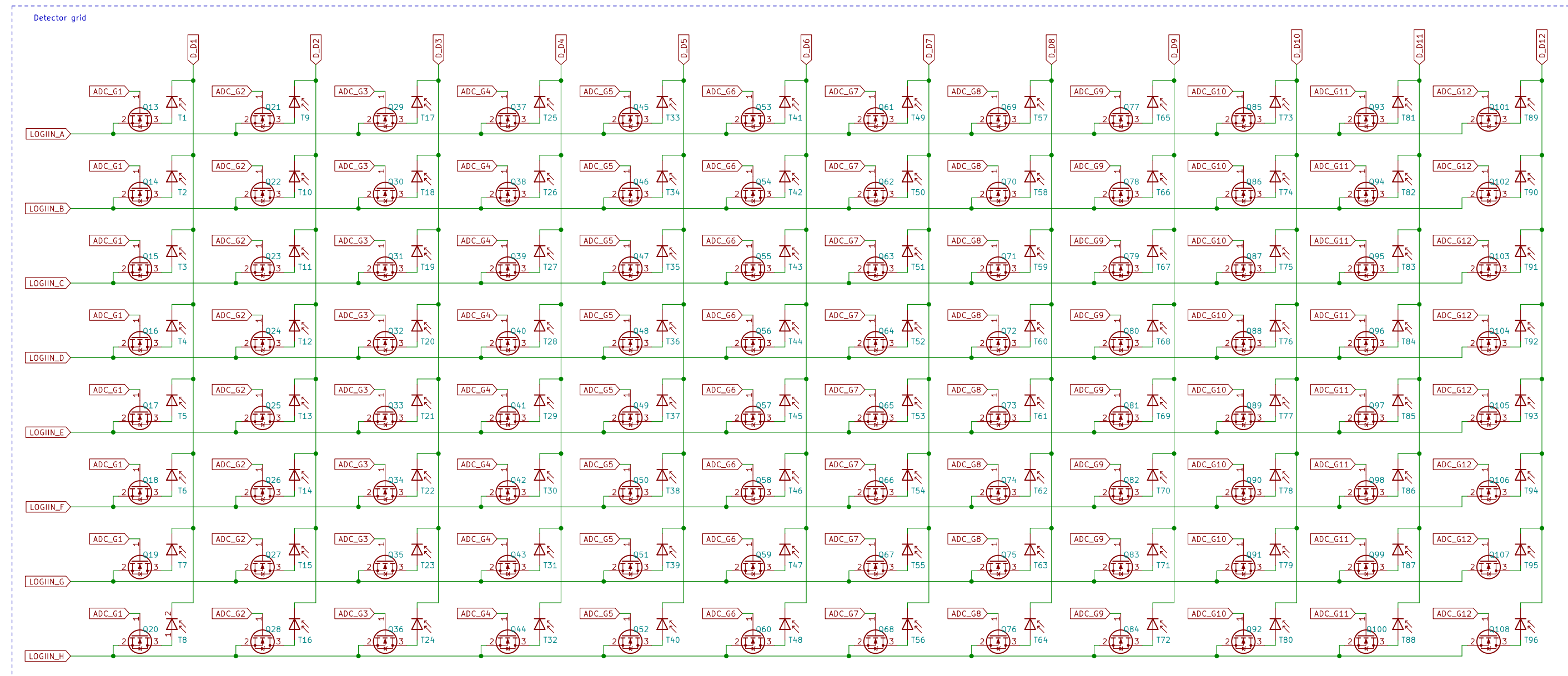
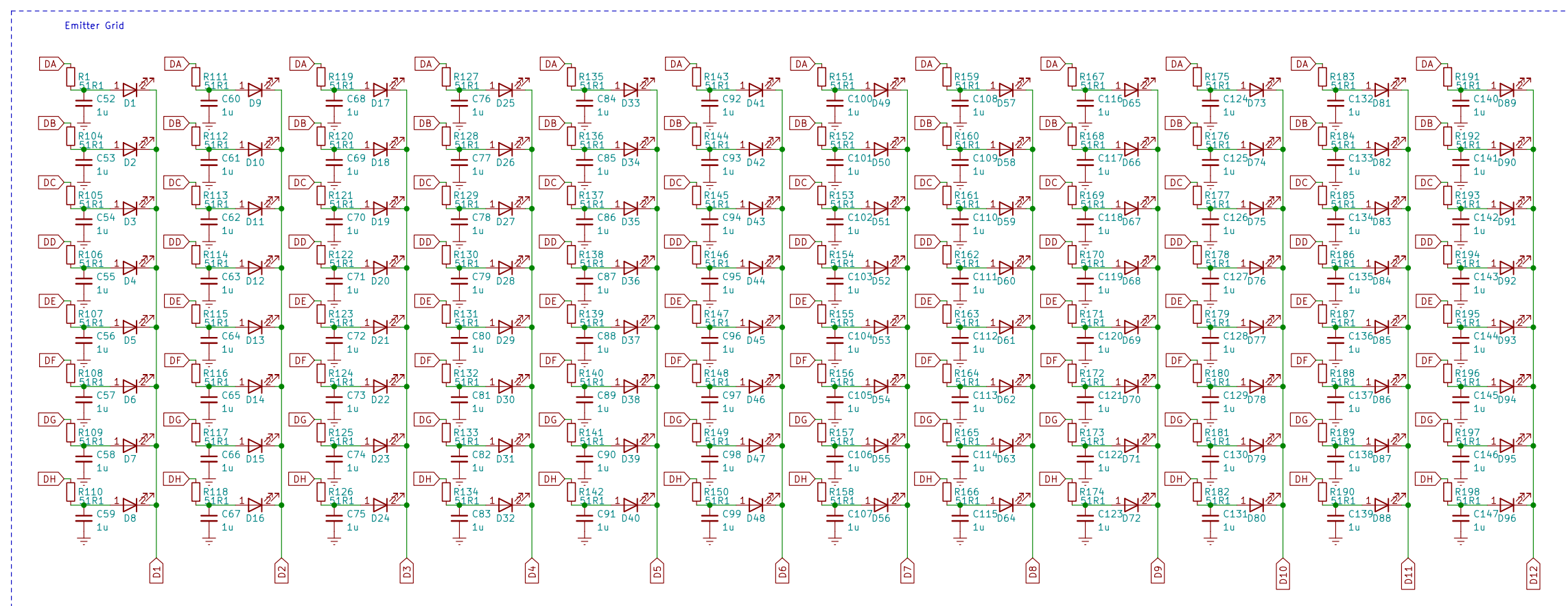
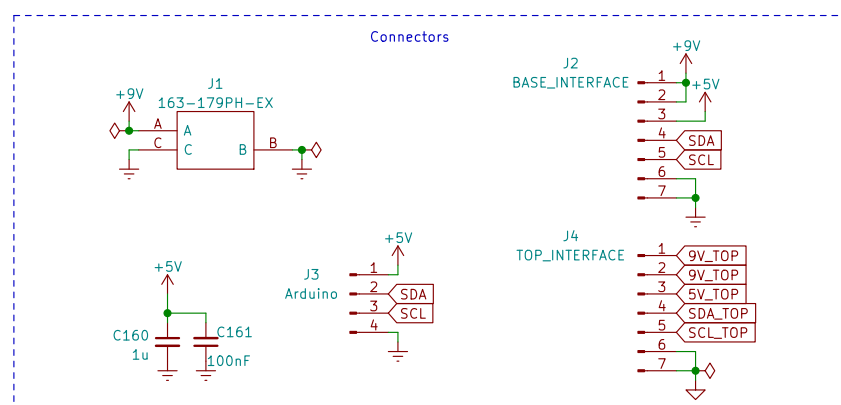
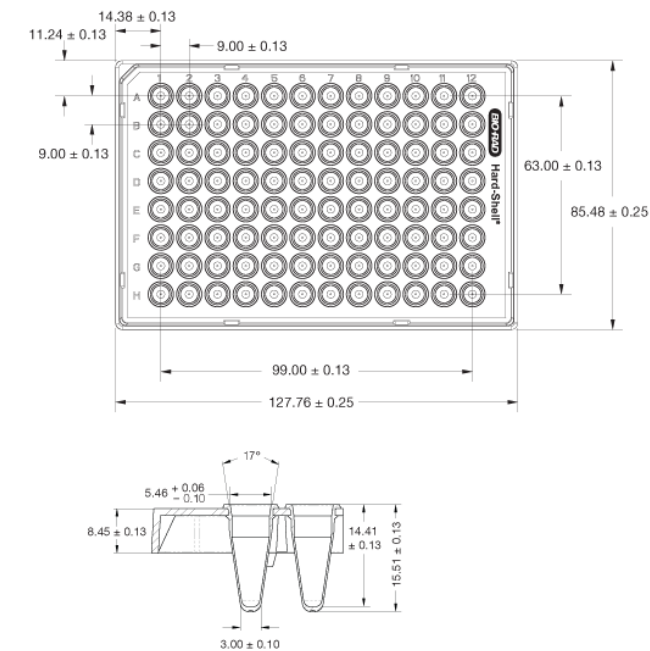


BASE

TOP



LSB for I2C devices:  
0000000000000000



Total I/O:  
12 LED Switches  
12 Detector signal network switches  
12 Detector power grid switches

Improvements

- Add bypass caps for ADC VDD and Vref pins
- Change DC jack footprint
- Add power up muted to control startup current to devices
- Add gate pull-ups/downs for grid MOSFETs
- Disconnect Refoutpin Vin and put regulated 5V from the back into the 5V pin
- CHICK MOSFET Reliability
- Add RC filter to the Voltage ref output going into the Opamp
- Capacitance multiplier to eliminate switching regulator ripple on the 9V line coming from the DC power supply
- Regulate voltage to 5.3

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole
- H5 MountingHole
- H6 MountingHole
- H7 MountingHole
- H8 MountingHole
- H9 MountingHole
- H10 MountingHole
- H11 MountingHole
- H12 MountingHole

Sachit Kahariya

Sheet: 7

File: OPSpec\_v0.1.0.kicad\_sch

Title: OPSpec

Size: A1

Date: 2022-08-14

Kicad E.D.A. Kicad (6.0.0)

Rev: 0.1.0

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