Project Planning

1. Select FPGA - End October
2. Design FPGA at the system level - Mid November
3. Design PCB Circuit - Mid November
4. Model housing for atomic clock - mid/end of November
5. Model circuit in LTSPICE - End of November
6. Conduct structural and thermal analysis on model - end of November/beginning of December
7. Start PCB Layout - Beginning of December
8. Prototype Testbench - Beginning of December