

SDDATA0 SDDATA1 SDDATA2 SDDATA3

SDCMD SDCLK

GCLK4 GCLK5

E_CLKOUT1 E_CLKOUT2

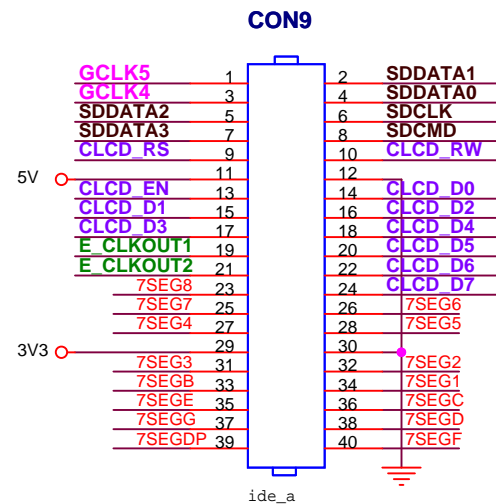
CLCD_D0 CLCD_D1 CLCD_D2 CLCD_D3 CLCD_D4 CLCD_D5 CLCD_D6 CLCD_D7

CLCD_RS CLCD_EN CLCD_RW

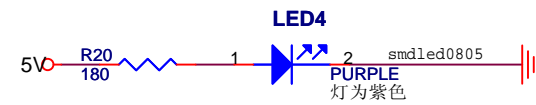
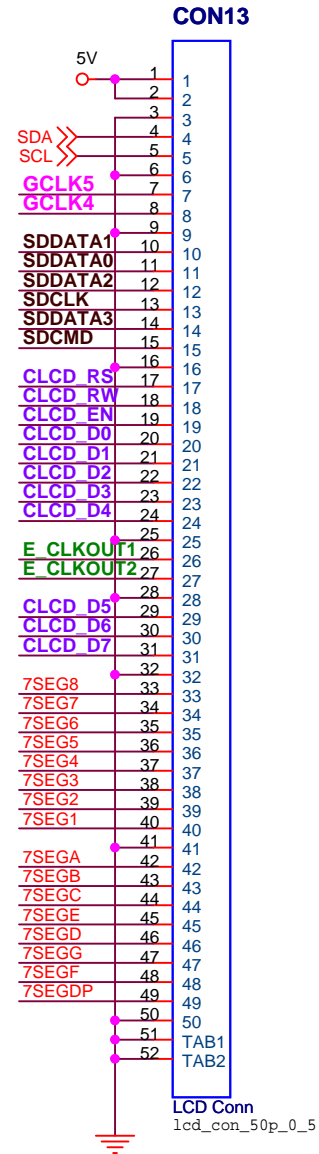
7SEG8 7SEG7 7SEG6 7SEG5

7SEG4 7SEG3 7SEG2 7SEG1

7SEGA 7SEGB 7SEGC 7SEGD 7SEGE 7SEGF 7SEGG 7SEGDP

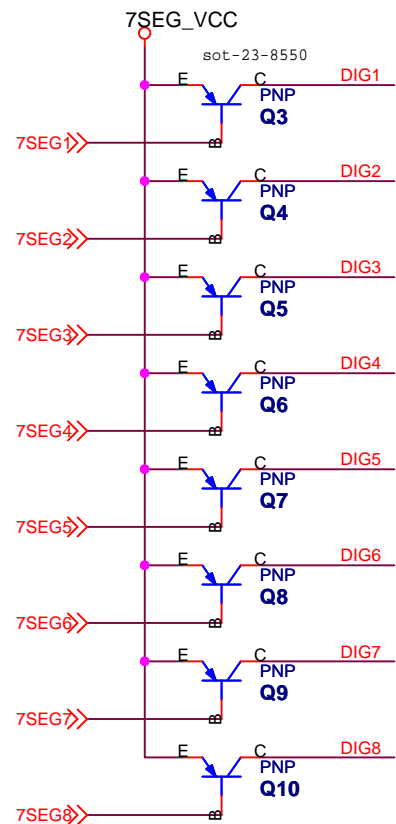
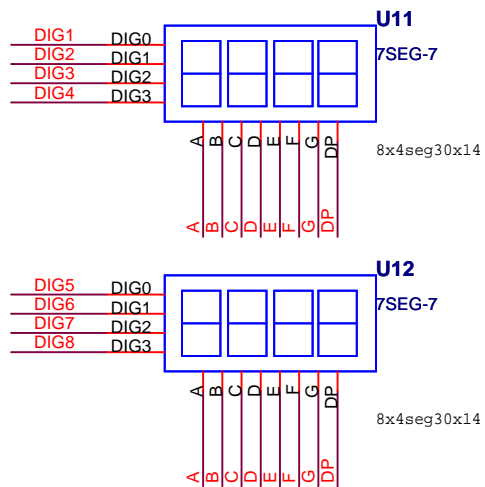


此接口兼容友晶terasic的DE0、DE2接口,可接友晶的外设

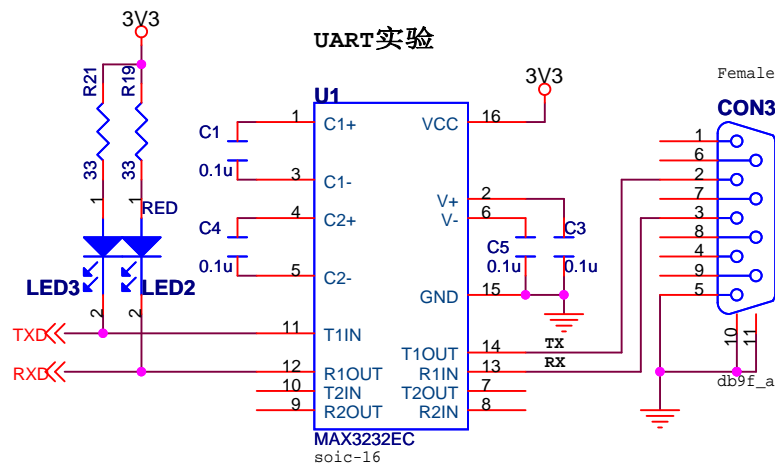


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ASK2CB FPGA学习板原理图			
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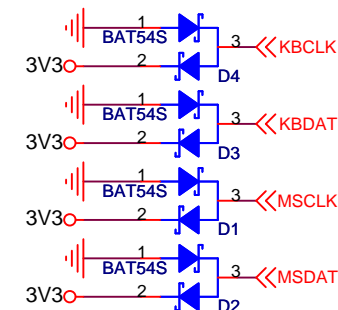
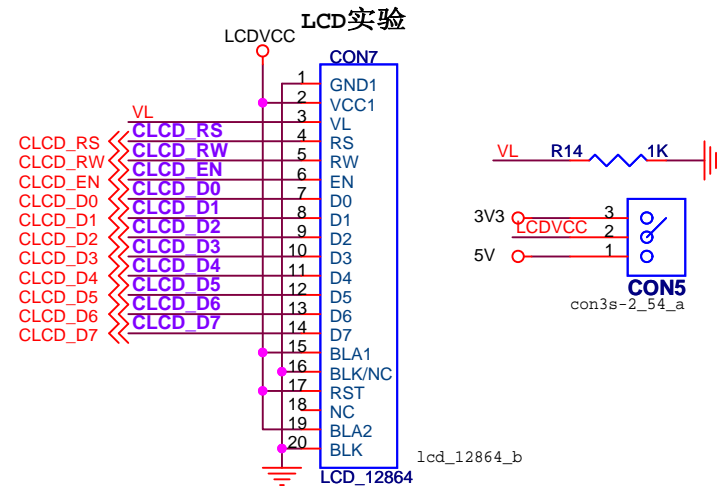
7段数码管实验



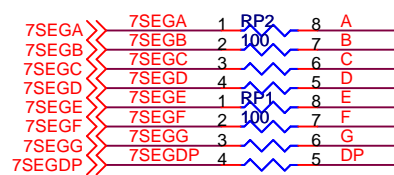
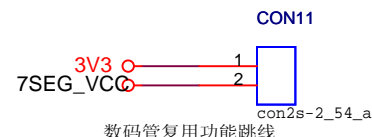
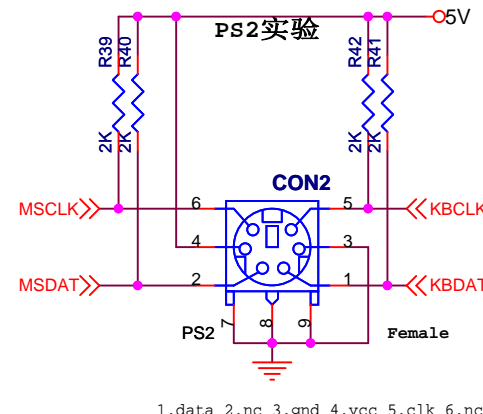
UART实验



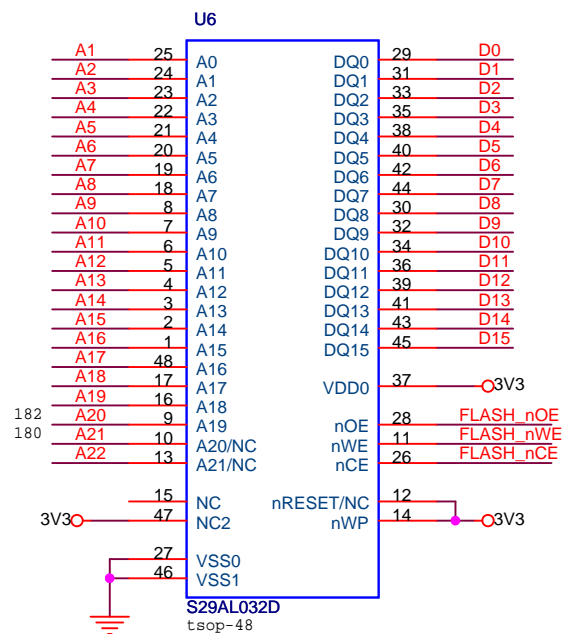
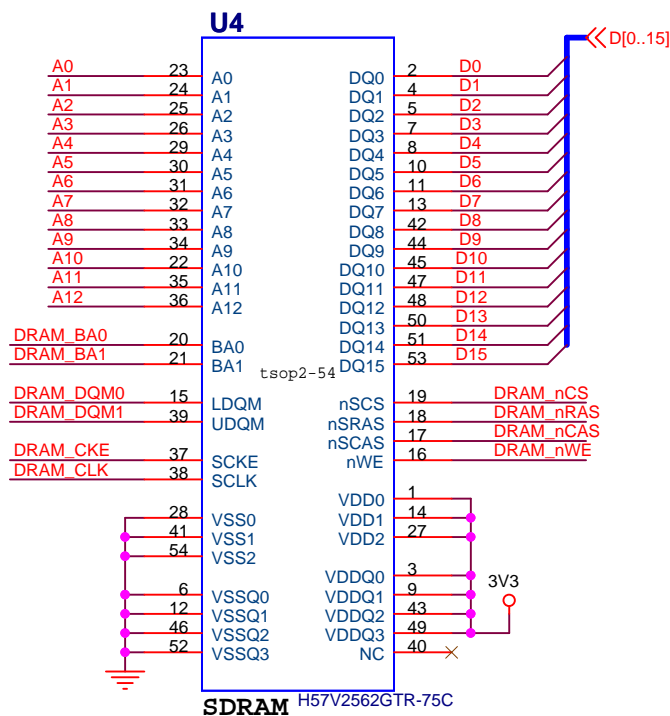
LCD实验



PS2实验



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SRAM_nCE >> SRAM_nCE
SRAM_nWE >> SRAM_nWE
SRAM_nOE >> SRAM_nOE
SRAM_nBHE >> SRAM_nBHE
SRAM_nBLE >> SRAM_nBLE

DRAM_BA0 >> DRAM_BA0
DRAM_BA1 >> DRAM_BA1

DRAM_DQM0 >> DRAM_DQM0
DRAM_DQM1 >> DRAM_DQM1

DRAM_CKE >> DRAM_CKE
DRAM_CLK >> DRAM_CLK

DRAM_nCS >> DRAM_nCS
DRAM_nRAS >> DRAM_nRAS
DRAM_nCAS >> DRAM_nCAS
DRAM_nWE >> DRAM_nWE

FLASH_nOE >> FLASH_nOE
FLASH_nWE >> FLASH_nWE
FLASH_nCE >> FLASH_nCE

