



# **Performance Evaluation - Single and Multi-Core Matrix Multiplication Algorithms**

**(1st Lab Work)**

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## **Students**

Beatriz Ferreira up202205612

Letícia Coelho up202108877

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# 1. Problem Description

In this project, we will analyse the performance differences between several matrix multiplication approaches. We will explore how building an algorithm with smarter cache usage affects execution time and how performance is increased by using parallel programming approaches through the use of the OpenMP C++ library.

## 2. Algorithms

Matrix multiplication performance is influenced by memory access patterns. To analyze this, we implemented and compared different approaches using **C++** and **Python**.

We started with a **basic row-by-column multiplication** in all two languages, followed by an **element-wise multiplication approach** in **C++** and **Python**. Finally, a **block-oriented optimization** was implemented in **C++**. Performance was measured across various matrix sizes, with larger tests conducted in C++ to evaluate scalability. The **Performance API (PAPI)** was used to gather execution metrics, and optimizations were applied where applicable.

The following sections describe each algorithm in detail.

### 2.1 Basic Matrix Multiplication

```
for ( i=0; i<m_ar; i++) {
    for ( j=0; j<m_br; j++) {
        temp = 0;
        for ( k=0; k<m_ar; k++) {
            temp += pha[i*m_ar+k] * phb[k*m_br+j];
        }
        phc[i*m_ar+j] = temp;
    }
}
```

**Image 1** - Basic Matrix Multiplication (Row-by-Column) in C++

This implementation follows the traditional row-by-column approach for matrix multiplication. Each element of the resulting matrix is computed by iterating through the rows of matrix **A** and multiplying them with the corresponding columns of matrix **B**.

### 2.2 Line Matrix Multiplication

```
for (i = 0; i < m_ar; i++) {
    for (k = 0; k < m_ar; k++) {
        temp = pha[i * m_ar + k];
        for (j = 0; j < m_br; j++) {
            phc[i * m_ar + j] += temp * phb[k * m_br + j];
        }
    }
}
```

**Image 2** - Line Matrix Multiplication in C++

This implementation optimizes the traditional row-by-column approach by reusing computed values from matrix **A** more efficiently, reducing redundant memory accesses. Instead of

computing each element of the result matrix individually by iterating over rows and columns, this approach processes **one row at a time**, multiplying it with all columns of matrix **B** before moving to the next row.

### 2.2.1 Parallel Nested Loops with OpenMP (*i-loop* parallelized)

```
#pragma omp parallel for
for (int i = 0; i < m_ar; i++) {
    for (int k = 0; k < m_ar; k++) {
        double temp = pha[i * m_ar + k];
        for (int j = 0; j < m_br; j++) {
            phc[i * m_ar + j] += temp * phb[k * m_br + j];
        }
    }
}
```

**Image 3** - Parallel Nested Loops with OpenMP in C++

This implementation applies OpenMP parallelization to the **outermost loop (i-loop)**, distributing the iterations across multiple threads. Each thread processes a different row of matrix **A**, performing all multiplications for that row in parallel. The num\_threads parameter was also used to study the difference of the usage of different thread counts.

### 2.2.2 Parallel Nested Loops with OpenMP (*j-loop* parallelized)

```
#pragma omp parallel
for (int i = 0; i < m_ar; i++) {
    for (int k = 0; k < m_ar; k++) {
        double temp = pha[i * m_ar + k];

        #pragma omp for
        for (int j = 0; j < m_br; j++) {
            phc[i * m_ar + j] += temp * phb[k * m_br + j];
        }
    }
}
```

**Image 4** - Parallel Nested Loops with OpenMP in C++

This version applies **nested parallelization**, where the **outermost i-loop** is parallelized, and an additional **#pragma omp for** is applied to the **innermost j-loop**. This means that, within each thread, multiple threads are also distributing the work inside the row-wise multiplication process. Similarly to the algorithm above, num\_threads was also used for similar effects.

## 2.3 Block Matrix Multiplication

```
for (int blockY = 0; blockY < blocksPerRow; ++blockY) {
    for (int blockX = 0; blockX < blocksPerRow; ++blockX) {
        for (int block = 0; block < blocksPerRow; ++block) {
            for (int i = 0; i < bkSize; ++i) {
                for (int n = 0; n < bkSize; ++n) {
                    for (int j = 0; j < bkSize; ++j) {
                        phc[blockIndex + (i*m_ar+j)] += pha[blockAindex + (i*m_ar+n)] * phb[blockBindex + (n*m_ar+j)];
                    }
                }
            }
        }
    }
}
```

Image 5 - Block Matrix Multiplication in C++

This implementation optimizes matrix multiplication by **dividing the matrices into smaller sub-blocks**. Instead of processing entire rows or columns at once, computations are performed on blocks, which helps improve **cache efficiency** and **memory locality**. By keeping blocks small enough to fit in **cache memory**, we reduce memory access latency and enhance computational performance.

## 3. Performance Metrics

To assess the performance of the implemented matrix multiplication algorithms, we analyzed key metrics such as **execution time**, **cache misses**, and **floating-point operations per second (FLOP/s)**. These metrics provide insights into the efficiency of different approaches and programming languages, helping to evaluate the impact of memory hierarchy and parallelization.

### Performance Measurement Using PAPI

For the **C++ implementations**, we used the **Performance Application Programming Interface (PAPI)** to measure **hardware performance counters**, specifically:

- **L1 and L2 Data Cache Misses**: Indicates how well memory is being reused (**lower cache misses generally result in better performance** due to reduced memory access latency).
- **Execution Time (seconds)**: The total time taken to execute each algorithm.

### 3.1 Execution Time and Multi-Core Evaluation

The algorithms were implemented in **C++ and Python**, and execution time was measured for both. The **parallelized versions** of the algorithms using **OpenMP** were also evaluated to measure performance improvements due to **multi-core execution**.

To ensure accuracy, all measurements were conducted on the **same system**, running **Ubuntu 22.04**, with an **Intel i5 7200 processor (3.10 GHz)** featuring:

Cache	Total Size	Associativity
L1 Data Cache	64 KiB	8-Way
L1 Information Cache	64 KiB	8-Way
L2 Cache	512 KiB	4-Way
L3 Cache	3 MiB	12-Way

Each experiment was repeated **five times**, and the average execution time was reported to minimize variations due to system background processes.

### 3.1.1 FLOP Calculation and Performance Estimation

For performance evaluation, the **floating-point operations per second (FLOP/s)** metric was computed using:

$$FLOPS = \frac{2 \times (matrix\ size)^3}{execution\ time}$$

### 3.1.2 Speedup and Efficiency in Parallel Implementations

To quantify the performance gain from parallel execution, we computed:

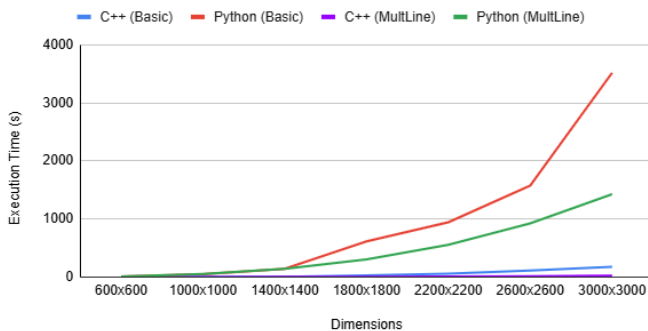
$$Speedup = \frac{T_{sequential}}{T_{parallel}}$$

$$Efficiency = \frac{Speedup}{\text{number of logical processors}}$$

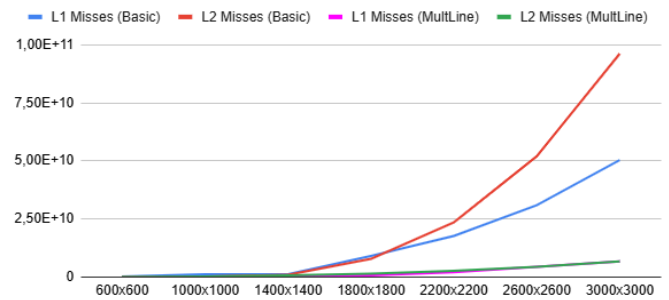
## 4. Results and Analysis

### 4.1 Basic vs Line Multiplication

Execution time comparison for the basic and "multiline" algorithms in Python and C++



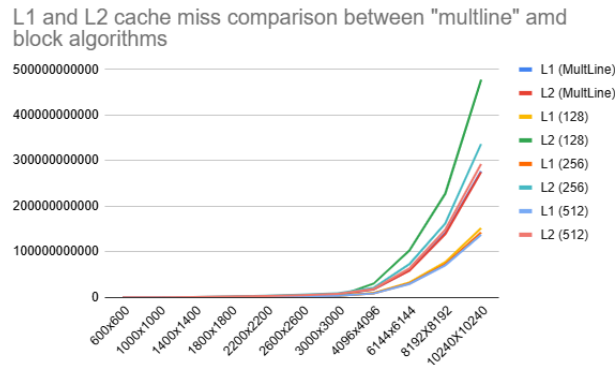
Cache miss comparison between basic and "multiline" algorithm in C++



By looking at the first graph we can see that the execution time in Python is drastically higher than that of the one experienced when using C++. This was expected, as Python is an interpreted programming language while C++ is compiled: there is additional overhead due to code being "translated" at runtime. We can also tell that the line multiplication algorithm is significantly faster than the basic one originally provided by the teachers.

In relation to cache misses, expectedly the basic algorithm experiences more of them. It justifies the relative slowness in execution and shows the effects of poor cache management when building an algorithm.

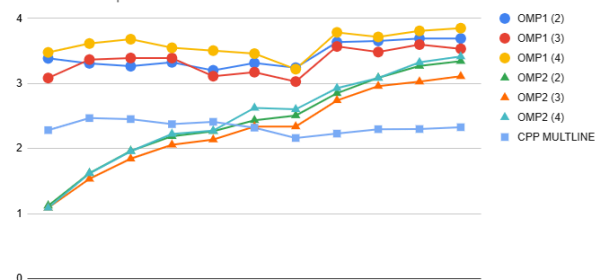
## 4.2 Line Multiplication vs Block Algorithm



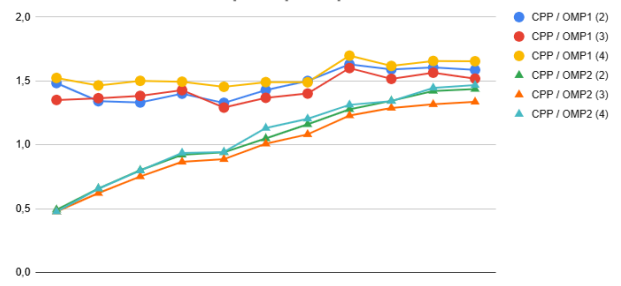
When it comes to execution time, the conclusion was that both algorithms perform quite similarly when analysed in a line graph. The slight timing difference mainly lies in how these algorithms handle cache resources - as you can see, the block approach has slightly more L2 cache misses and slightly less L1 cache misses than the line multiplication approach, with the 512 block resulting in the lowest number of L1 cache misses overall.

## 4.3 Single vs Multi Core Line Multiplication

GFLOPS comparison



Speedup comparison



The computer used for testing only has two physical cores so a maximum of 4 threads was used. The number of FLOPS stayed stable for regular line multiplication and OMP1 and increased over time for OMP2 - this is explained by the need for a larger overhead of thread creation and synchronization, since we're parallelizing the innermost loop in this approach, which ends up being unsustainable especially in smaller matrices. Overall we can tell that the highest speedup was achieved by the 4 threaded OMP1 approach. We were also able to conclude that using 3 threads ends up being less efficient than using 4 or even 2 threads, since there is a workload imbalance between cores that calls for a significant context switching overhead for workload assignment.

## 5. Conclusions

In conclusion, this project allowed us to fully grasp the extent to which successful cache management and smartly built algorithms affect the performance of certain tasks. Furthermore we had the opportunity to try out some parallel programming approaches, which were foreign to us up until this point.

It will definitely serve as a reminder for the remainder of our academic and professional career that we should always take into consideration some of the lower level aspects of the programs we are running in order to optimize performance.

## References

freeCodeCamp. **Interpreted vs Compiled Programming Languages: What's the Difference?**. 2020. Accessed on 20 March 2025.  
<<https://www.freecodecamp.org/news/compiled-versus-interpreted-languages/>>



## Annexes

### A1. Basic Algorithm

#### A1.1. C++ - execution time (s)

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	0,439693	0,419459	0,391727	0,42769	0,407302	0,4171742
1000x1000	2,352758	2,207778	2,310205	2,198498	2,214327	2,2567132
1400x1400	5,890811	6,01915	5,878399	6,22058	5,739422	5,9496724
1800x1800	29,268526	28,574936	29,469361	27,501497	27,289729	28,4208098
2200x2200	59,098902	58,8099	59,10075	58,822947	58,517598	58,8700194
2600x2600	111,589645	109,523708	107,390359	112,599718	108,725018	109,9656896
3000x3000	186,188844	171,556685	171,954471	179,419925	168,992275	175,62244

#### A1.2. Python - execution time (s)

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	9,639553	9,604367	9,67931	9,973183	9,994624	9,7782074
1000x1000	47,732815	48,035698	47,761805	49,535921	48,063445	48,2259368
1400x1400	152,441989	141,919836	146,396799	138,014873	138,019214	143,3585422
1800x1800	635,329777	637,423009	587,58044	577,64713	624,95063	612,5861972
2200x2200	946,874424	967,744301	872,949756	951,401237	975,825248	942,9589932
2600x2600	1628,028695	1587,41334	1546,093184	1530,391618	1589,310286	1576,247425
3000x3000	3519,678407	3574,332479	3541,415252	3476,996507	3473,67521	3517,219571

#### A1.3. C++ - cache misses L1

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	244170603	244545924	244690993	243957461	244298561	244332708,4
1000x1000	1227149425	1228541869	1229378949	1228506174	1228208050	1228356893
1400x1400	1227149425	1228541869	1229378949	1228506174	1228208050	1228356893
1800x1800	9068659941	9086327414	9087193201	9087330712	9069318795	9079766013

2200x2200	1765929085 3	1766032709 7	1764717848 4	17636370466	176352574 12	1764768486 2
2600x2600	3092954585 3	3088209667 0	3088282986 2	30884477028	308836229 49	3089251447 2
3000x3000	5030690839 8	5032120077 3	5032030973 1	50309596231	503414560 50	5031989423 7

## A1.4. C++ - cache misses L2

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	38375124	39428196	38763910	38692234	37894911	38630875
1000x1000	233482975	266676576	242436027	239857155	229334703	242357487,2
1400x1400	670316965	132959212 9	115552075 1	127221104 1	641124775	1013753132
1800x1800	559266096 4	848379997 9	850340361 0	801856092 7	842759039 2	7805203174
2200x2200	234886258 00	240277918 27	237186877 13	232398613 84	233041989 11	23555833127
2600x2600	524280666 23	521604096 99	514831540 59	517580565 97	519875661 46	51963450625
3000x3000	953707735 32	982816026 94	958640697 23	958531650 32	950991735 86	96093756913

## A2. Line Multiplication (Single Core)

### A2.1. C++ - execution time (s)

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	0,166654	0,197872	0,167535	0,214487	0,19953	0,1892156
1000x1000	0,807852	0,812936	0,805502	0,815078	0,811128	0,8104992
1400x1400	2,218192	2,23107	2,219219	2,226751	2,292973	2,237641
1800x1800	4,728846	4,725345	4,711708	5,159114	5,229171	4,9108368
2200x2200	8,929431	8,779584	8,79437	8,797028	8,869663	8,8340152
2600x2600	14,801709	15,750524	14,95215	15,376528	14,856572	15,1474966
3000x3000	22,996702	25,769195	25,620781	26,036189	24,454995	24,9755724
4096x4096	59,10882	61,951388	61,754143	62,294359	63,088056	61,6393532
6144x6144	201,872146	201,034928	201,035997	201,76821	203,788372	201,8999306
8192x8192	472,392815	482,11446	478,736591	478,431713	478,953047	478,1257252

10240x10240	920,239415	930,133287	922,528146	919,882397	919,78131	922,512911
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## A2.2. Python - execution time (s)

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	10,584092	10,650233	10,676091	10,502165	10,631115	10,6087392
1000x1000	52,336577	50,405668	50,983617	50,570455	50,601985	50,9796604
1400x1400	140,599531	141,502348	143,052117	141,578753	141,417281	141,630006
1800x1800	302,866577	302,840571	303,926355	303,256146	305,527085	303,6833468
2200x2200	555,43571	554,718959	555,463223	556,335064	559,351865	556,2609642
2600x2600	923,982903	921,010894	925,529727	925,723786	923,55403	923,960268
3000x3000	1417,615939	1416,803158	1418,985761	1429,85886	1446,47089	1425,946922

## A2.3. C++ - cache misses L1

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	27152125	27154161	27158034	27154541	27154725	27154717,2
1000x1000	125938814	125921227	125929517	126061555	125940697	125958362
1400x1400	347422556	347425491	347210798	348058431	348969448	347817344,8
1800x1800	752769186	750336376	748647236	770724158	779314230	760358237,2
2200x2200	209379536 1	208379845 7	208425018 6	208387622 6	208722652 6	2086589351
2600x2600	441333667 7	441120382 0	441316504 5	441235836 6	441305185 8	4412623153
3000x3000	678039927 9	677744298 0	677721562 5	677680752 9	677800907 4	6777974897
4096x4096	177120723 00	177153846 31	176842765 13	176849778 96	176986012 96	17699062527
6144x6144	597949646 79	598091671 32	598174913 03	597678265 02	597878581 26	59795461548
8192x8192	141726928 358	141679985 501	141119100 769	141152776 476	141115214 768	141358801174
10240x10240	276629570 401	276393085 500	276644828 881	276767536 358	276745646 400	276636133508

## A2.4. C++ - cache misses L2

Dimensions	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	53631041	54441848	54529030	54739825	54566531	54381655
1000x1000	250624630	251865398	246026804	246581623	247044234	248428537,8
1400x1400	685642797	674535541	671529696	670000947	683349204	677011637
1800x1800	147473261 2	144181927 0	146913152 5	147040519 0	144468633 9	1460154987
2200x2200	268764136 0	268660719 9	269653704 4	262791069 2	261771825 9	2663282911
2600x2600	437470405 2	443747305 7	437470251 5	438872781 1	450227552 0	4415576591
3000x3000	673739699 7	680461662 2	679046818 2	682733555 8	670121583 5	6772206639
4096x4096	175825462 34	175537821 11	175582024 02	176482758 95	173735468 72	17543270703
6144x6144	592362868 67	581809012 61	582040222 18	591534832 16	585955052 63	58674039765
8192x8192	139353152 647	140059648 489	139567774 690	137526789 401	138342347 536	138969942553
10240x10240	281551931 080	273606965 054	276029494 217	271409619 268	272823963 343	275084394592

## A3. Block Multiplication

### A3.1. Execution time (s)

Dimensions	Block Size	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	128	0,169692	0,16552 7	0,169001	0,167367	0,16709 1	0,1677356
	256	0,200018	0,19591 3	0,210652	0,209468	0,23580 8	0,2103718
	512	0,177651	0,19179 9	0,195314	0,267	0,17788 2	0,2019292
1000x1000	128	0,743163	0,81775 6	0,831334	0,800828	0,82787 1	0,8041904
	256	0,736131	0,73680 1	0,74509	0,734772	0,73613	0,7377848
	512	0,849685	0,83734	0,845516	0,817946	0,8363	0,837359

			8				
1400x1400	128	2,04857	2,05233 5	2,049205	2,036978	2,07166 7	2,051751
	256	2,041896	2,03640 5	2,028108	2,039485	2,24926 2	2,0790312
	512	2,20949	2,19851 9	2,207738	2,201811	2,21611 2	2,206734
1800x1800	128	4,28972	4,83221 9	4,291887	4,717409	4,29322 6	4,4848922
	256	4,288478	4,31477 4	4,910938	4,821712	4,29566 4	4,5263132
	512	5,128629	4,75746 7	5,464643	5,060843	5,46204 8	5,174726
2200x2200	128	8,187262	8,05781 3	7,870871	7,947953	7,93115 9	7,9990116
	256	7,819376	8,61478 1	8,371213	8,439576	8,51755 2	8,3524996
	512	8,827248	9,69686 1	9,662971	8,709997	9,46602 5	9,2726204
2600x2600	128	12,931972	13,5959 05	13,92568	13,83276	13,7879 16	13,6148466
	256	12,875163	13,8153 14	13,94803 8	14,02857 7	12,9152 17	13,5164618
	512	14,604651	15,6544 14	14,98472 8	15,78872 5	15,1843 53	15,2433742
3000x3000	128	19,83103	19,8190 97	20,67993 3	19,88079 7	20,4246 7	20,1271054
	256	19,735704	19,7419 85	20,63647 9	19,73220 3	19,8720 44	19,943683
	512	22,505544	23,3078 96	23,26875 1	23,43627 7	22,1474 41	22,9331818
4096x4096	128	86,216835	83,9430 95	79,57701	80,39862 9	80,7925 82	82,1856302
	256	70,223737	69,8004 6	69,82764 1	69,93322	70,3058 64	70,0181844
	512	62,128087	61,0131 58	61,19270 1	61,52163 2	61,0630 67	61,383729
6144x6144	128	210,86673 8	220,762 766	214,2157 06	220,1831 99	218,410 988	216,8878794
	256	227,23194 6	227,546 524	226,5771 29	227,0331 24	227,252 658	227,1282762
	512	202,74466	204,958	204,9668	205,0070	204,954	204,526163

		1	278	23	04	049	
8192x8192	128	810,61212 1	790,822 141	803,0880 16	802,3578 99	803,597 817	802,0955988
	256	594,16377 1	594,452 017	615,0765 9	616,2218 92	620,752 189	608,1332918
	512	518,89897 4	505,487 237	513.0881 96	514,7265 68	511,543 329	512,664027
10240x10240	128	1014,2185 88	1027,94 2598	1053,823 548	1042,924 869	1046,16 5015	1037,014924
	256	1036,1547 06	1030,99 2011	1038,562 635	1038,079 011	1048,24 3225	1038,406318
	512	952,94512 2	950,609 856	944,1010 49	935,2433 07	943,157 265	945,2113198

### A3.2. Cache misses

Dimensions	Block Size	Cache	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	128	L1	3144480 8	3140145 2	3140379 1	2853678 7	3140701 0	30838769,6
	128	L2	2868042 4	2493655 0	3122521 4	2853678 7	2728318 7	28132432,4
	256	L1	2976977 6	2977500 0	2977052 7	2977400 5	2977520 5	29772902,6
	256	L2	6881342 2	6751839 4	6528846 0	6637413 4	6619847 5	66838577
	512	L1	2886737 8	2886950 2	2886792 6	2909483 9	2886689 3	28913307,6
	512	L2	6140748 4	6165757 0	6172017 9	6359813 2	6267661 2	62211995,4
1000x1000	128	L1	1435051 57	1435214 31	1435210 38	1435282 47	1435162 08	143518416,2
	128	L2	1280720 59	1337818 81	1388827 35	1495118 40	1399134 09	138032384,8
	256	L1	1351302 27	1350873 38	1350998 52	1350917 88	1350923 53	135100311,6
	256	L2	3496698 28	3487346 01	3484750 28	3389438 28	3388993 28	344944522,6
	512	L1	1302239 01	1303247 74	1303452 97	1303305 06	1303481 69	130314529,4

	512	L2	2889001 51	2871598 04	2930540 43	2874726 80	2861999 63	288557328,2
1400x1400	128	L1	3923316 18	3919754 41	3919824 76	3918989 79	3920334 01	392044383
	128	L2	3918402 45	3839506 71	3976294 70	3503537 99	3852080 45	381796446
	256	L1	3721460 21	3721184 74	3721043 13	3721382 57	3724485 77	372191128,4
	256	L2	9066406 54	9353383 52	9339548 27	9062324 78	9400696 55	924447193,2
	512	L1	3588028 94	3587683 40	3587810 94	3587788 83	3587955 65	358785355,2
	512	L2	8092152 38	7933069 69	7939687 34	8091337 28	8096702 43	803058982,4
1800x1800	128	L1	8268489 10	8268550 10	8262554 60	8275646 71	8263422 60	826773262,2
	128	L2	1628929 46	4756126 93	1762587 94	2817452 21	2621906 33	271740057,4
	256	L1	7874747 19	7874871 32	7882187 91	7881866 25	7873614 88	787745751
	256	L2	2015066 545	1948514 619	2015448 471	1992644 160	1947127 349	1983760229
	512	L1	7644497 93	7641141 92	7652858 38	7643814 20	7652542 98	764697108,2
	512	L2	1741196 334	1733187 895	1731282 252	1738462 698	1735852 139	1735996264
2200x2200	128	L1	1515233 790	1514818 320	1514574 572	1514547 717	1514750 164	1514784913
	128	L2	1017319 652	9613562 85	8633665 04	8259383 45	8881623 39	911228625
	256	L1	1445113 232	1446177 597	1445801 933	1445957 120	1446113 309	1445832638
	256	L2	3706792 349	3636756 644	3620036 920	3634688 811	3601307 109	3639916367
	512	L1	1399878 668	1401459 657	1401396 476	1399743 382	1400892 269	1400674090
	512	L2	3198717 873	3156288 535	3168276 998	3141836 321	3189348 212	3170893588
2600x2600	128	L1	2544591 349	2545238 814	2546046 177	2546020 251	2544583 818	2545296082
	128	L2	1545525 348	1662937 357	2009978 035	2116460 446	2075342 897	1882048817

	256	L1	2388207 092	2389428 301	2389733 472	2389484 516	2388313 823	2389033441
	256	L2	5765525 896	5948081 676	5917271 457	5865677 974	5921656 044	5883642609
	512	L1	2311274 983	2313078 885	2311848 160	2313533 979	2312154 810	2312378163
	512	L2	5059467 339	5115871 723	5140701 183	5088867 062	5133359 604	5107653382
3000x3000	128	L1	3873571 192	3873476 962	3875726 262	3873515 220	3874356 160	3874129159
	128	L2	2740434 242	2790803 576	2874522 104	2656938 223	2872989 333	2787137496
	256	L1	3647906 882	3647942 066	3649419 830	3647857 161	3648330 570	3648291302
	256	L2	9424787 125	9430000 701	9127361 124	9379230 429	9357237 376	9343723351
	512	L1	3528785 195	3530488 617	3530958 595	3530569 818	3528237 257	3529807896
	512	L2	7781647 836	7819509 621	7802203 291	7772971 501	7776560 576	7790578565
4096x4096	128	L1	9720265 732	9805629 743	9828947 101	9824304 460	9823611 613	9800551730
	128	L2	3055063 9850	3039610 5260	3031136 6584	3074617 3163	3071504 0493	30543865070
	256	L1	9117358 007	9115646 816	9116573 636	9117286 207	9117456 975	9116864328
	256	L2	2144949 5375	2073443 3131	2120049 9585	2132665 7980	2104127 2067	21150471628
	512	L1	8795040 074	8784904 715	8784143 871	8782520 134	8779541 156	8785229990
	512	L2	1861616 4363	1853623 5666	1894893 5013	1861110 6022	1896817 4943	18736123201
6144x6144	128	L1	3283108 2955	3281395 1502	3280500 6265	3281126 4674	3282062 1964	32816385472
	128	L2	1023230 35163	1035401 69110	1035810 47041	1031514 20296	1031296 21290	103145058580
	256	L1	3078602 0324	3078595 7678	3078990 4906	3078806 6187	3078676 0332	30787341885
	256	L2	7342276 2323	7344355 0173	7335643 8168	7315641 0755	7315784 3185	73307400921
	512	L1	2964160 5157	2969786 2231	2969448 2728	2969675 3573	2969841 2479	29685823234



	512	L2	6419025 6377	6409446 3800	6324133 8565	6335660 0374	6380978 1971	63738488217
8192x8192	128	L1	7743080 5798	7797784 2306	7763670 4872	7765264 2798	7762054 1153	77663707385
	128	L2	2290208 58001	2252196 01937	2284129 32908	2283053 04408	2281857 03496	227828880150
	256	L1	7311187 3945	7310839 3148	7303028 4747	7301937 8229	7299612 0952	73053210204
	256	L2	1611303 37931	1620895 46167	1608851 16548	1639167 15151	1640977 20711	162423887302
	512	L1	7076824 6281	7052851 3212	7065527 9178	7068946 5189	7062856 5984	70654013969
	512	L2	1489959 35900	1475069 02514	1494091 40046	1497543 41774	1480587 79033	148745019853
10240x1024 0	128	L1	1520302 65097	1522890 66162	1518898 12347	1521298 83663	1519983 48810	152067475216
	128	L2	4766051 71466	4766797 53965	4808113 51217	4762843 61463	4788004 35779	477836214778
	256	L1	1423016 39236	1423546 37247	1424907 91914	1424341 95336	1425145 16633	142419156073
	256	L2	3327241 39022	3352951 90652	3381638 31088	3371905 05131	3383720 09137	336349135006
	512	L1	1373364 85620	1373119 00989	1371299 36497	1370467 45624	1370802 90444	137181071835
	512	L2	2915897 69375	2938106 21151	2935447 79850	2914694 38725	2928333 49182	292649591657

## A4. Multi-Core Line Multiplication

### A4.1. OMP1

#### A4.1.1. Execution time (s)

Dimensions	Threads	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	2	0,133173	0,12682 5	0,127318	0,12552	0,12501 9	0,127571
	3	0,14065	0,14016	0,138799	0,136753	0,14394 6	0,1400616
	4	0,121696	0,12006 5	0,120519	0,121076	0,13768 7	0,1242086
1000x1000	2	0,605229	0,60219	0,607021	0,610244	0,59670	0,6042788

			9			1	
	3	0,59231	0,59401 7	0,590376	0,597523	0,59535 7	0,5939166
	4	0,548989	0,56518 2	0,545893	0,555952	0,55053 4	0,55331
1400x1400	2	1,72948	1,64176 7	1,716196	1,643082	1,67222 9	1,6805508
	3	1,593967	1,60380 7	1,637393	1,614101	1,64045 6	1,6179448
	4	1,493687	1,49244 1	1,488367	1,493782	1,48704 5	1,4910644
1800x1800	2	3,590526	3,48950 2	3,494568	3,483293	3,47863 3	3,5073044
	3	3,413368	3,40402	3,527402	3,398753	3,45524 4	3,4397574
	4	3,245397	3,23690 4	3,28394	3,30898	3,35934	3,2869122
2200x2200	2	6,789767	6,71304	6,581465	6,588806	6,57679 4	6,6499744
	3	7,4101	6,74810 5	6,684831	6,469265	6,89639 9	6,84174
	4	6,029528	6,14589 9	6,066905	6,062828	6,07102 9	6,0752378
2600x2600	2	10,496629	10,4974 3	10,58984 5	10,68509 3	10,7527 99	10,6043592
	3	11,130943	10,4500 68	10,59896 2	12,26678 4	10,9224 82	11,0738478
	4	10,108168	10,1295 39	10,13312 1	10,16716 9	10,2715 54	10,1619102
3000x3000	2	16,109718	16,1437 45	16,73209 7	16,95023 2	17,3184 46	16,6508476
	3	17,059422	17,7849 51	17,77858 7	17,73858 7	18,7658 54	17,8254802
	4	16,46765	16,6362 79	16,85797 9	16,86688 9	17,0046 35	16,7666864
4096x4096	2	38,982241	36,9638 2	37,18229 1	38,71778 8	37,2080 12	37,8108304
	3	39,117728	38,6401 17	37,04560 4	38,61379 4	39,1224 89	38,5079464
	4	36,172141	36,2487 72	36,39423 8	36,39264 5	36,3290 71	36,3073734
6144x6144	2	126,87527	126,665	127,1059	127,0127	127,115	126,9551096

		9	833	58	28	75	
	3	133,88734 9	133,481 616	132,7457 52	131,3757 52	134,310 556	133,160205
	4	124,70368 7	125,201 713	124,7352 06	124,8497 23	124,724 87	124,8430398
8192x8192	2	298,84136 3	296,884 687	297,5562 9	297,2086 29	298,743 09	297,8468118
	3	311,09469 1	300,887 257	303,0973 57	299,1138 01	313,728 929	305,584407
	4	286,73368 5	288,988 119	289,7570 55	289,6070 59	288,340 651	288,6853138
10240x10240	2	582,11966 3	581,843 125	579,8978 47	584,2772 91	579,793 055	581,5861962
	3	614,49645 6	603,560 405	605,6306 41	607,1909 75	608,106 79	607,7970534
	4	559,74920 7	558,392 613	548,7608 62	561,8613 82	560,172 214	557,7872556

#### A4.1.2. Cache misses

Dimensions	Threads	Cache	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	2	L1	136127 19	135942 62	135981 60	135898 10	135977 11	13598532,4
	2	L2	277670 61	276087 34	275758 87	275986 95	276550 33	27641082
	3	L1	669293 7	922300 9	635421 6	921239 5	758313 5	7813138,4
	3	L2	137568 62	188552 31	126196 79	187216 72	155854 48	15907778,4
	4	L1	558298 9	489061 2	560023 1	524737 5	584019 8	5432281
	4	L2	114194 65	994689 1	112775 01	106913 39	116440 50	10995849,2
1000x1000	2	L1	630683 27	629926 40	622145 54	631476 70	629773 78	62880113,8
	2	L2	126856 280	126537 172	125390 557	126502 537	126538 573	126365023,8
	3	L1	386320 96	361038 98	303381 11	452541 13	324047 23	36546588,2
	3	L2	783396 60	730580 00	629711 89	902545 83	668059 41	74285874,6

	4	L1	232324 74	252449 93	243743 29	272563 54	227818 20	24577994
	4	L2	473331 24	514796 93	496726 22	556531 49	465728 39	50142285,4
1400x1400	2	L1	178381 731	173524 213	176207 422	173627 759	173890 081	175126241,2
	2	L2	353663 152	350738 190	356165 074	346912 155	341610 199	349817754
	3	L1	115953 689	130617 825	131821 208	130017 582	118515 650	125385190,8
	3	L2	230861 827	183937 854	180322 270	182601 368	232712 971	202087258
	4	L1	114054 805	152875 072	149199 273	143012 906	148238 998	141476210,8
	4	L2	128826 633	167428 102	162890 547	157075 371	161137 473	155471625,2
1800x1800	2	L1	388486 811	374533 783	375999 526	374896 850	375740 519	377931497,8
	2	L2	739731 495	733705 130	735219 566	741541 306	744721 158	738983731
	3	L1	252043 912	252327 069	266028 098	255121 035	364119 150	277927852,8
	3	L2	491334 790	490827 595	500400 555	506939 610	450475 736	487995657,2
	4	L1	333417 137	338646 586	351762 844	359927 064	346113 890	345973504,2
	4	L2	325405 277	335656 823	368233 082	383182 607	359091 909	354313939,6
2200x2200	2	L1	106793 4826	106478 7815	105983 5402	105896 4979	105898 2115	1062101027
	2	L2	136766 2891	134390 1046	133638 1061	137105 6594	137486 2507	1358772820
	3	L1	846628 481	774020 057	819160 204	791844 719	763314 151	798993522,4
	3	L2	940022 971	843740 189	916034 132	892039 214	918103 193	901987939,8
	4	L1	631126 469	654761 529	643178 927	659853 466	650874 415	647958961,2
	4	L2	629613 384	690325 676	654932 319	688328 213	683030 798	669246078
2600x2600	2	L1	220964 5563	220972 1434	220973 7100	221002 2104	220953 8386	2209732917

	2	L2	220575 4212	220914 0473	220780 9742	220674 7966	221985 9031	2209862285
	3	L1	143314 6242	130259 8335	134763 6323	144612 1791	142641 4971	1391183532
	3	L2	140440 2501	117953 2501	127794 4137	149667 1553	145732 3014	1363174741
	4	L1	110134 4958	108904 8820	110022 5621	108846 5611	106933 8434	1089684689
	4	L2	119414 9457	116532 9946	119183 6541	116496 1675	110460 1368	1164175797
3000x3000	2	L1	339375 4204	339367 1982	339345 2051	339375 1167	339353 4011	3393632683
	2	L2	349447 4423	350347 7654	352685 2298	344330 3479	345112 7133	3483846997
	3	L1	226255 9887	218743 3833	225568 8832	226517 9036	223120 7065	2240413731
	3	L2	230746 1230	221739 9728	233078 2990	240658 9341	232536 2243	2317519106
	4	L1	166641 2031	168848 1159	167283 3316	169452 9375	167791 6906	1680034557
	4	L2	175315 5823	180636 5926	178923 1279	183911 6356	179809 6646	1797193206
4096x4096	2	L1	875271 9079	876815 4559	876971 3853	878442 5038	877631 7309	8770265968
	2	L2	806578 7720	798512 8669	809762 4550	800356 6977	806459 7294	8043341042
	3	L1	586445 6194	583760 7850	561010 2481	584404 5886	583022 4713	5797287425
	3	L2	600327 6443	600161 5191	531373 3334	584653 2702	584209 4263	5801450387
	4	L1	436980 7291	434184 3783	437165 7950	437894 0633	438689 2722	4369828476
	4	L2	458030 3128	444928 1399	455121 3656	456020 5037	460898 6183	4549997881
6144x6144	2	L1	295662 95259	295899 91186	296442 73581	295991 84737	295986 02222	29599669397
	2	L2	272267 69956	274997 66011	279795 98065	279110 25756	285024 86186	27823929195
	3	L1	197024 59915	197631 97087	197676 63643	197855 91067	198176 79810	19767318304
	3	L2	195432 97901	203793 57359	202774 96512	207578 59700	211711 78412	20425837977

	4	L1	148397 00420	148625 15590	148612 52477	148405 20523	148289 43116	14846586425
	4	L2	164004 77384	163379 46733	164888 93332	164637 78859	165177 35010	16441766264
8192x8192	2	L1	693853 32677	694061 70015	694085 25949	696049 55139	694026 87539	69441534264
	2	L2	659029 50129	659067 24229	665947 81605	678253 42070	668631 40114	66618587629
	3	L1	461436 29676	462262 51832	451232 19405	444326 67401	461078 18181	45606717299
	3	L2	503219 74190	443548 82478	489989 00874	467026 22920	514170 07595	48359077611
	4	L1	345409 53537	343795 53323	345442 54262	345176 94885	344409 23409	34484675883
	4	L2	410662 94437	414374 45822	406718 03728	406965 62233	415482 03384	41084061921
10240x10240	2	L1	137350 994016	137353 318168	137215 543092	137258 500420	137014 169688	137238505077
	2	L2	132491 162753	130815 410155	131472 450010	133469 808979	130856 562904	131821078960
	3	L1	908812 58986	909924 16828	908468 11640	913580 10505	911787 69296	91051453451
	3	L2	126664 310820	131359 770690	131827 182425	108984 862723	125874 989655	124942223263
	4	L1	684043 42201	680642 68458	681398 55366	685327 65003	683269 36632	68293633532
	4	L2	117200 084760	115575 126225	115778 546318	117756 129817	115765 433829	116415064190

#### A4.1.3. GFLOPS

Dimensions	OMP1 ( 2 )	OMP1 (3)	OMP1 (4)
600	3,386350	3,084357	3,478020
1000	3,309731	3,367476	3,614610
1400	3,265596	3,391958	3,680592
1800	3,325631	3,390937	3,548619
2200	3,202418	3,112658	3,505377
2600	3,314863	3,174326	3,459192
3000	3,243078	3,029371	3,220672
4096	3,634910	3,569106	3,785428
6144	3,653705	3,483447	3,715517

8192	3,691534	3,598062	3,808686
10240	3,692460	3,533225	3,850005

#### A4.1.4. Speedup

Speedup (CPP / OMP 1)		
1,483218	1,350946	1,523370
1,341267	1,364668	1,464819
1,331493	1,383014	1,500700
1,400174	1,427670	1,494058
1,328428	1,291194	1,454102
1,428422	1,367862	1,490615
1,499958	1,401116	1,489595
1,630204	1,600692	1,697709
1,590325	1,516218	1,617230
1,605274	1,564627	1,656218
1,586202	1,517798	1,653880

#### A4.1.5. Efficiency

Efficiency OMP 1	Logical Processors		
Dimensions	2	3	4
600	0,741609	0,450315	0,380842
1000	0,670633	0,454889	0,366205
1400	0,665746	0,461005	0,375175
1800	0,700087	0,475890	0,373514
2200	0,664214	0,430398	0,363525
2600	0,714211	0,455954	0,372654
3000	0,749979	0,467039	0,372399
4096	0,815102	0,533564	0,424427
6144	0,795163	0,505406	0,404308
8192	0,802637	0,521542	0,414054
10240	0,793101	0,505933	0,413470

## A4.2. OMP2

### A4.2.1. Execution time (s)

Dimensions	Threads	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	2	0,38163	0,39273 <sub>3</sub>	0,379815	0,379744	0,39033 <sub>8</sub>	0,384852
	3	0,397561	0,39466 <sub>8</sub>	0,400662	0,400201	0,39240 <sub>8</sub>	0,3971
	4	0,397735	0,39957 <sub>5</sub>	0,39723	0,397645	0,39429 <sub>7</sub>	0,3972964
1000x1000	2	1,248541	1,22838 <sub>2</sub>	1,233716	1,22875	1,22560 <sub>6</sub>	1,232999
	3	1,300675	1,31213	1,302337	1,308675	1,29756 <sub>4</sub>	1,3042762
	4	1,218565	1,22044 <sub>8</sub>	1,227221	1,259262	1,25301 <sub>7</sub>	1,2357026
1400x1400	2	2,792621	2,80403 <sub>2</sub>	2,792309	2,791545	2,79325 <sub>8</sub>	2,794753
	3	2,934621	3,05181 <sub>9</sub>	2,927747	2,973609	2,99860 <sub>5</sub>	2,9772802
	4	2,755612	2,77310 <sub>5</sub>	2,803406	2,856783	2,83051 <sub>6</sub>	2,8038844
1800x1800	2	5,312119	5,33332 <sub>1</sub>	5,321964	5,352766	5,32603	5,32924
	3	5,666931	5,74577 <sub>5</sub>	5,67773	5,727682	5,52776 <sub>7</sub>	5,669177
	4	5,273973	5,22803 <sub>5</sub>	5,198614	5,212823	5,30623 <sub>5</sub>	5,243936
2200x2200	2	10,030336	9,17489 <sub>6</sub>	9,179749	9,19493	9,40821 <sub>6</sub>	9,3976254
	3	11,010673	9,77500 <sub>9</sub>	9,712424	9,613366	9,70816 <sub>9</sub>	9,9639282
	4	9,298742	9,30368 <sub>3</sub>	9,205017	9,461139	9,55616 <sub>1</sub>	9,3649484
2600x2600	2	14,488955	14,3082 <sub>79</sub>	14,58885	14,35973	14,4253 <sub>65</sub>	14,4342358
	3	14,71801	14,9207 <sub>42</sub>	14,91526 <sub>1</sub>	15,53372 <sub>4</sub>	15,0537 <sub>26</sub>	15,0282926
	4	13,529372	13,5600 <sub>38</sub>	13,59123 <sub>3</sub>	13,14984 <sub>2</sub>	13,1353 <sub>86</sub>	13,3931742
3000x3000	2	22,581963	21,1753 <sub>99</sub>	21,35833	21,27286 <sub>4</sub>	21,2663 <sub>45</sub>	21,5309802



	3	23,267658	22,8337 69	22,93049 7	23,67309 9	22,7534 68	23,0916982
	4	20,035861	20,5852 45	20,94112	21,00776 8	21,1706 57	20,7481302
4096x4096	2	47,491802	47,3211 94	50,96233 8	47,70922 8	47,6660 66	48,2301256
	3	49,563646	50,5763 77	49,80341 1	50,08179 7	50,6952 48	50,1440958
	4	46,703251	47,6543 19	46,87915 6	46,74238	46,7482 51	46,9454714
6144x6144	2	149,70232 6	150,156 668	150,2736 97	150,3885 95	150,552 286	150,2147144
	3	157,13912 6	157,093 076	156,9457 81	155,6288 54	156,713 785	156,7041244
	4	150,53907 6	150,728 752	149,8023 98	150,9698 13	150,219 092	150,4518262
8192x8192	2	336,05389 4	335,618 405	336,5939 93	337,4125 98	336,230 105	336,381799
	3	363,25603 2	362,968 359	361,7449 41	364,8516 38	362,889 184	363,1420308
	4	331,77254	331,339 003	329,2861 61	332,1594 49	329,241 117	330,759654
10240x10240	2	641,75185 5	641,748 247	642,2511 78	642,8172 21	642,834 293	642,2805588
	3	689,66342 6	694,615 393	690,2619 08	688,6019 3	689,272 338	690,482999
	4	625,50844 2	625,992 264	629,1862 4	631,1845 14	632,690 873	628,9124666

#### A4.2.2. Cache misses

Dimensions	Threads	Cache	Exp 1	Exp 2	Exp 3	Exp 4	Exp 5	Average
600x600	2	L1	1793477 7	1799019 6	1790048 6	1794314 2	1795430 0	17944580,2
	2	L2	4666772 5	4696387 3	4655630 8	4680177 7	4677299 8	46752536,2
	3	L1	1335422 8	1061288 7	1327561 4	1252794 5	1046994 6	12048124
	3	L2	4071779 5	2622083 2	4044676 9	2724028 2	2436603 9	31798343,4
	4	L1	7988640	7979790	8000542	7992331	8026984	7997657,4

	4	L2	1841748 1	1825382 8	1847385 5	1847950 0	1898699 2	18522331,2
1000x1000	2	L1	7562072 1	7561576 5	7567522 2	7577275 8	7576501 4	75689896
	2	L2	1754880 89	1759109 57	1760821 69	1764417 78	1764794 49	176080488,4
	3	L1	4593441 2	5128264 3	4669725 9	4716757 1	4612793 7	47441964,4
	3	L2	9512636 0	1178342 53	9925520 0	1026801 47	9879427 3	102738046,6
	4	L1	3486702 6	3490416 9	3483820 3	3480452 8	3489603 1	34861991,4
	4	L2	6478299 9	6530742 6	6471885 1	6454291 4	6547751 0	64965940
1400x1400	2	L1	1971655 79	1970896 62	1970496 13	1970737 25	1970424 26	197084201
	2	L2	4378897 01	4371763 10	4371313 34	4290764 16	4286369 12	433982134,6
	3	L1	1334306 09	1197053 11	1333184 12	1403242 43	1247079 16	130297298,2
	3	L2	2915728 77	2387250 54	2903146 18	3239027 77	2599526 59	280893597
	4	L1	9232034 6	9223509 3	1083800 00	1083162 53	1084084 84	101932035,2
	4	L2	1649448 15	1657440 47	2654531 10	2657137 67	2650373 05	225378608,8
1800x1800	2	L1	4102901 55	4104594 61	4102568 88	4106478 41	4100833 32	410347535,4
	2	L2	8711240 28	8689040 82	8649356 21	8697107 70	8670575 34	868346407
	3	L1	2873159 69	2593776 86	2873634 43	2588501 58	2821098 12	275003413,6
	3	L2	6280887 38	4724722 39	6348261 24	4603551 01	6154725 39	562242948,2
	4	L1	2233876 23	2234033 13	2232433 67	2233035 13	2231928 73	223306137,8
	4	L2	5412478 05	5409919 14	5408313 91	5418749 61	5418767 51	541364564,4
2200x2200	2	L1	7629563 83	7380310 47	7371294 10	7380776 14	7405135 18	743341594,4
	2	L2	1535857 146	1520649 298	1525994 357	1535439 984	1539730 307	1531534218

	3	L1	5059419 71	5174895 90	4967069 87	5372520 75	4962657 77	510731280
	3	L2	9815089 63	1051066 833	8929762 96	1039065 585	8764410 20	968211739,4
	4	L1	3848699 44	3694680 30	3695136 21	3850221 48	3753788 13	376850511,2
	4	L2	9073031 14	7866238 26	7847629 36	9017549 19	8377529 40	843639547
2600x2600	2	L1	1211421 660	1203120 500	1208861 290	1204814 707	1208580 693	1207359770
	2	L2	2427999 705	2433526 698	2430402 276	2428151 569	2423068 742	2428629798
	3	L1	9489544 66	9159813 70	9406030 80	8631864 46	8279186 54	899328803,2
	3	L2	1747087 599	1563264 682	1695303 061	1674656 223	1743861 565	1684834626
	4	L1	6447373 27	6444353 04	6454805 22	6341077 61	6328552 53	640323233,4
	4	L2	1504724 964	1505435 764	1506357 732	1348038 392	1345181 651	1441947701
3000x3000	2	L1	1930120 184	1859812 952	1858569 484	1848272 391	1846852 159	1868725434
	2	L2	3705189 645	3705673 249	3656428 785	3639458 406	3640081 243	3669366266
	3	L1	1736530 697	1254527 857	1253922 995	1708391 329	1761754 727	1543025521
	3	L2	2462842 165	2621504 586	2703924 176	2315774 839	2631788 106	2547166774
	4	L1	9058467 75	9576324 29	1046399 505	1047636 463	1047008 788	1000904792
	4	L2	1668152 493	2029079 223	2252678 161	2225345 878	2226998 237	2080450798
4096x4096	2	L1	5204574 060	5136114 436	5455944 146	5151830 367	5145957 473	5218884096
	2	L2	9181310 336	8907427 120	9181747 211	8975390 300	8973244 651	9043823924
	3	L1	5988239 676	3251776 458	6046026 871	5767509 820	3965259 880	5003762541
	3	L2	6064302 670	6334522 496	5801353 321	5903714 678	6295252 574	6079829148
	4	L1	2811095 098	2711734 365	2735351 785	2772040 053	2821107 156	2770265691

	4	L2	5003717 207	5213006 300	5125317 866	4970852 607	4930451 387	5048669073
6144x6144	2	L1	3019733 1804	3019235 0349	3019854 4785	3019808 9847	3020167 6794	30197598716
	2	L2	2915203 7769	2889900 8807	2943659 6548	2914868 7533	2935358 1122	29197982356
	3	L1	1804422 3484	1906602 1533	1905450 7387	1716040 2653	1792405 4987	18249842009
	3	L2	2065173 7983	2059611 7234	2057877 4820	2050065 5136	2071867 8637	20609192762
	4	L1	1538524 3634	1539421 5465	1540670 4798	1530943 4747	1531572 3686	15362264466
	4	L2	1646558 1201	1649974 6578	1650546 3109	1575613 5478	1581365 4994	16208116272
8192x8192	2	L1	7077843 4314	7072928 0637	7078347 9056	7087802 4480	7115644 9771	70865133652
	2	L2	7041575 5889	7080873 5314	7013521 2063	6953477 0754	6912711 3918	70004317588
	3	L1	4747100 1831	4744056 2169	4740235 8968	4746141 4059	4738751 7226	47432570851
	3	L2	5061453 0874	5014216 0867	4890696 0082	4914854 7212	4823980 4742	49410400755
	4	L1	3590991 4770	3576307 4521	3576268 5589	3576688 1422	3598897 2708	35838305802
	4	L2	3833984 7757	3674304 1951	3770837 5167	3686358 1233	3697993 0693	37326955360
10240x10240	2	L1	1392607 55119	1390431 62331	1392886 18587	1393030 43929	1390250 62021	139184128397
	2	L2	1316780 43326	1341538 28495	1304741 22403	1330455 39070	1325069 20917	132371690842
	3	L1	9364058 3500	9348456 6928	9375207 9215	9366003 2919	9365083 3122	93637619137
	3	L2	9562142 7104	9667366 4445	9556239 5142	9703506 2093	9392375 0828	95763259922
	4	L1	7047943 7459	7044906 8230	7022734 0798	7024737 6418	7036524 7048	70353693991
	4	L2	7482395 4937	7411531 1990	7422231 1104	7379880 8896	7220491 8511	73833061088

#### A4.2.3. GFLOPS

Dimensions	OMP2 ( 2 )	OMP2 (3)	OMP2 (4)
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600	1,122509	1,087887	1,087349
1000	1,622061	1,533418	1,618512
1400	1,963680	1,843293	1,957285
1800	2,188680	2,057441	2,224283
2200	2,266104	2,137310	2,274011
2600	2,435321	2,339055	2,624621
3000	2,508014	2,338503	2,602644
4096	2,849650	2,740880	2,927630
6144	3,087956	2,960078	3,083090
8192	3,268642	3,027773	3,324201
10240	3,343529	3,110118	3,414599

#### A4.2.4. Speedup

Speedup (CPP / OMP 2)		
0,491658	0,476494	0,476258
0,657340	0,621417	0,655902
0,800658	0,751572	0,798050
0,921489	0,866235	0,936479
0,940026	0,886600	0,943306
1,049415	1,007932	1,130986
1,159983	1,081582	1,203751
1,278026	1,229244	1,312999
1,344076	1,288415	1,341957
1,421378	1,316636	1,445538
1,436308	1,336040	1,466838

#### A4.2.5. Efficiency

Efficiency OMP 2	Logical Processors		
Dimensions	2	3	4
600	0,245829	0,158831	0,119065
1000	0,328670	0,207139	0,163975
1400	0,400329	0,250524	0,199513
1800	0,460745	0,288745	0,234120
2200	0,470013	0,295533	0,235827
2600	0,524707	0,335977	0,282747
3000	0,579992	0,360527	0,300938

4096	0,639013	0,409748	0,328250
6144	0,672038	0,429472	0,335489
8192	0,710689	0,438879	0,361385
10240	0,718154	0,445347	0,366710