

Reasoning with Happens-Before Relations about Concurrent Programs in the Theta Framework

Short toolpaper

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András Vörös, István Majzik

December 7, 2025



Agenda

Introduction

Context: what is the problem?
What do we do? **Why** do we care?

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Verification approaches (existing & novel)
Optimization techniques (novel)

Techniques

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Experiment and data analysis
Evaluation of impact

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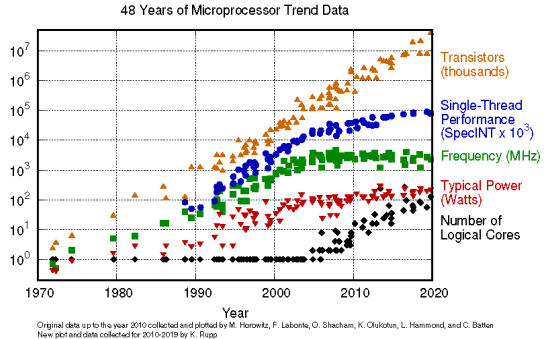
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Theta as a Verifier
Theta as a Framework

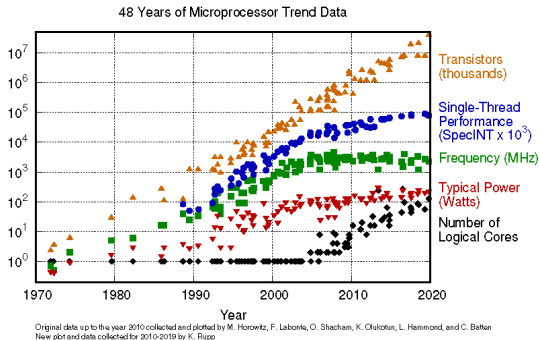
Availability

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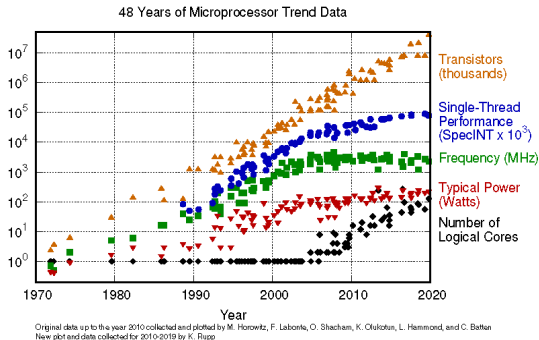
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- ▶ Further performance: **relaxed** memory models



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We need new
formal techniques!

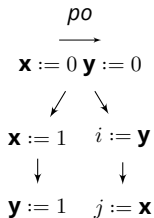


Introduction

- ▶ Emerging method for verifying concurrency: **Happens-before relation**
 - ▶ Decouples data- and control-flow
 - ▶ Enables semi-modular verification

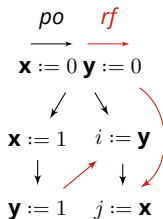
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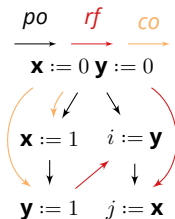
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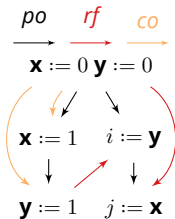
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- ▶ Causality and memory semantics: happens-before (*hb*-) order
- ▶ **Verification idea:** encode threads *and* happens-before constraints
- ▶ **Error property:** reachable thread state, and \exists consistent *hb*-order

Verification Approach (Overview)

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POSIX/C11 threads
Nondeterministic inputs
Thread-local assertions

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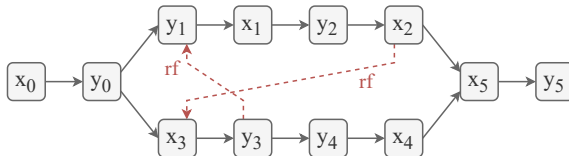
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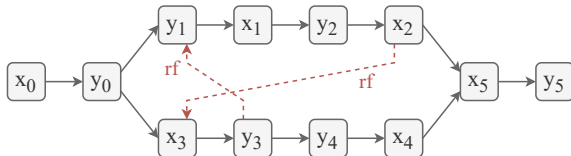
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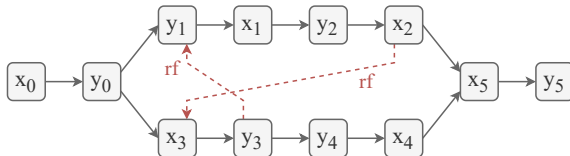
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CSSA



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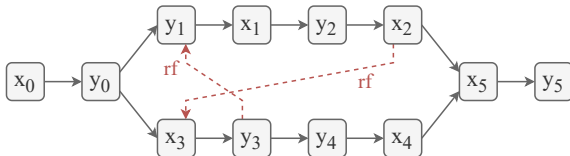
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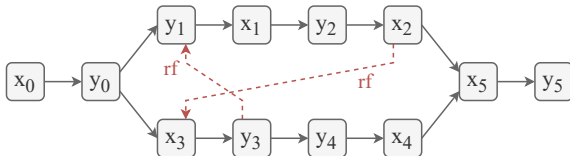
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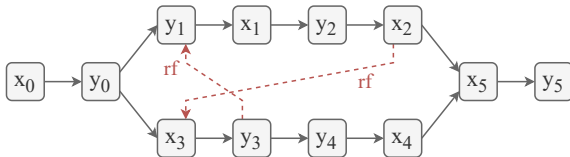
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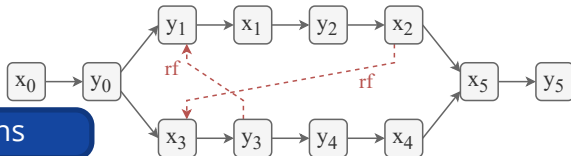
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Optimizations



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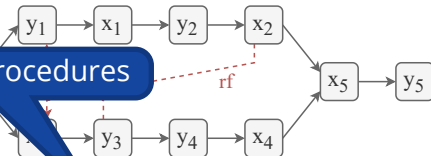
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Decision procedures

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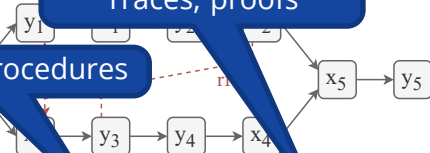
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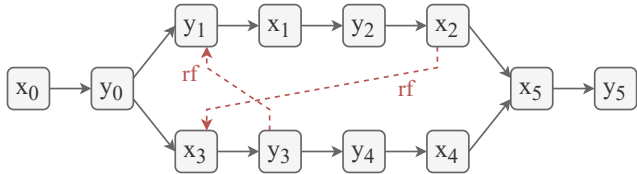
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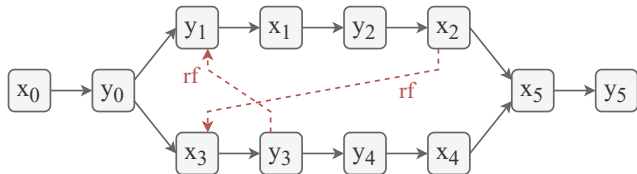
Traces, proofs



Techniques - Integer Difference Logic

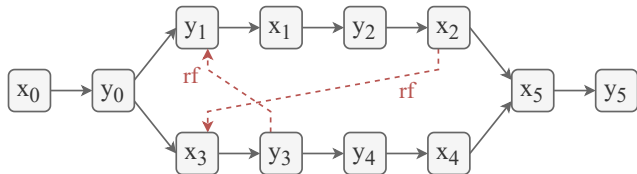


Techniques - Integer Difference Logic



- ▶ Each event has a *clock* integer variable
- ▶ A happens-before order implies an integer order

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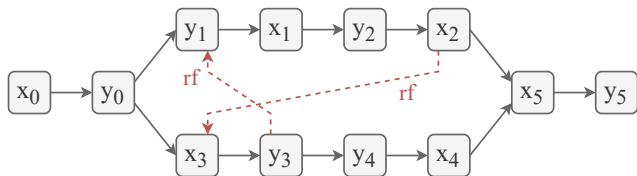


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Easy encoding, any solver can be used

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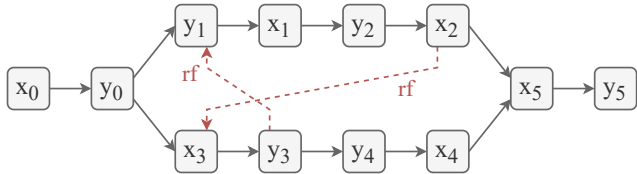
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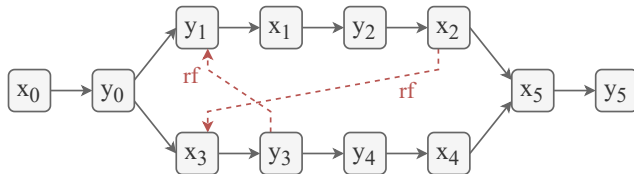
Disadvantages

Overly specific, thus suboptimal

Techniques - Refinement Step-by-Step

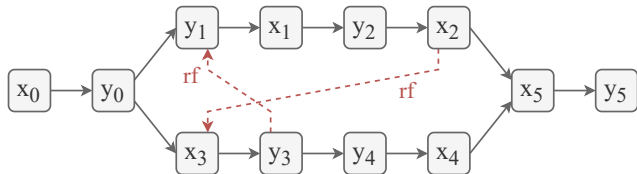


Techniques - Refinement Step-by-Step



- ▶ We track *happens-before* orders externally to the solver
- ▶ Any model is validated, and potentially *refined*
- ▶ The SMT solver does not propagate, we do

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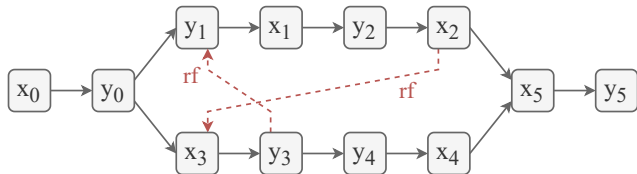


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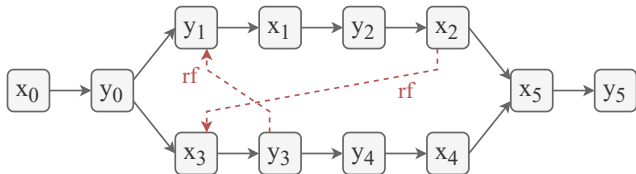
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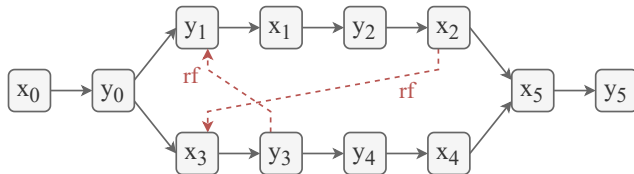
Disadvantages

Number of SMT queries increase

Techniques - User Propagator

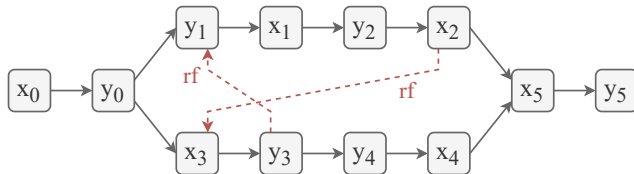


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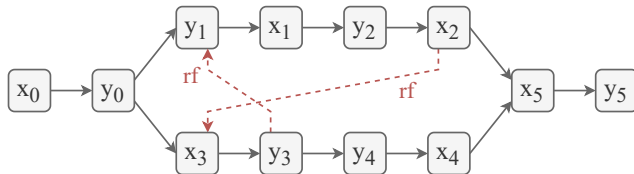


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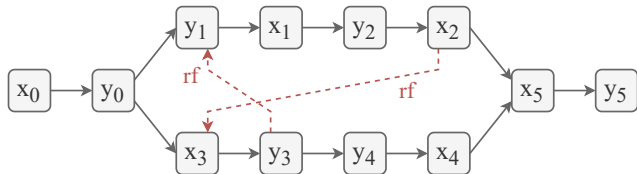
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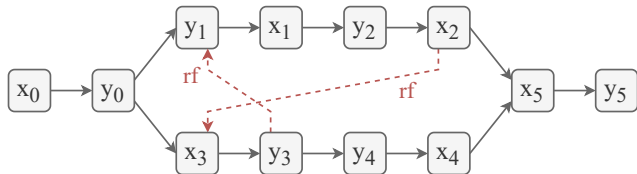
Disadvantages

Only certain SMT solvers are usable

Optimization: Automatic Conflict Avoidance



Optimization: Automatic Conflict Avoidance



- ▶ **Idea:** we enumerate some potential cycles explicitly
- ▶ This is sound (we don't overconstrain the state space)
- ▶ This helps the solvers (based on empirical results)

Experimental Evaluation

Research Question 1

How do the decision procedures compare?

Research Question 2

How much does the optimization help?

Research Question 3

How does Theta compare to other tools?

Research Question 4

Does solver choice influence performance?

Results - RQ1

Research Question 1

How do the decision procedures compare?

Results - RQ1

Research Question 1

How do the decision procedures compare?

- ▶ SV-Benchmarks tasks: 725 concurrent C programs
- ▶ 15 CPU-minute timeout, 15GB RAM

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Solved tasks

	IDL	RFN	PROP
Solved	398	409	410
Time (s)	26000	4150	5770

Results - RQ1

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Solved tasks

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Solved	398	409	410
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- ▶ Integer difference logic is worse and slowest
- ▶ Step-by-step refinement is quickest and almost best
- ▶ User propagation is best but not quickest

Results - RQ2

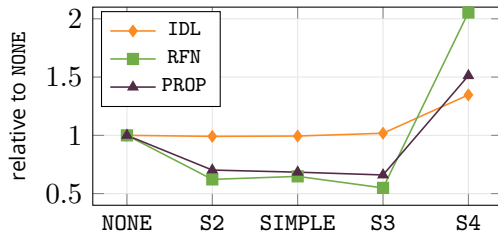
Research Question 2

How much does the optimization help?

Results - RQ2

Research Question 2

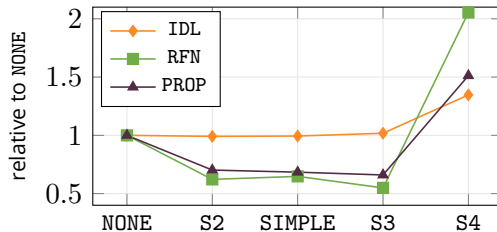
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Results - RQ2

Research Question 2

How much does the optimization help?



- ▶ SN : Length of cycle hardcoded in SMT
- ▶ "Simple": 2, plus one derivation rule

Results - RQ3: Dartagnan

Research Question 3

How does Theta compare to other tools?

Results - RQ3: Dartagnan

Research Question 3

How does Theta compare to other tools?

	Theta (complete)			Dartagnan	
	IDL	RFN	PROP	Eager	Lazy
Solved/	398	409	410	456	457
filtered	398	409	410	434	433
Time (s)	26000	4150	5770	11200	6370

Results - RQ3: Deagle

Research Question 3

How does Theta compare to other tools?

	Theta (bounded)			Deagle
	IDL	RFN	PROP	
Solved/	538	554	553	623
filtered	538	554	553	577
Time (s)	35000	6020	8690	2020

- Bounded safety: unsound results

Results - RQ4

Research Question 4

Does solver choice influence performance?

Results - RQ4

Research Question 4

Does solver choice influence performance?

	IDL	RFN
z3	538	544
cvc5	537	529
MathSAT	307	528
Princess	280	184

► Few new tasks solved



github.com/ftsrg/theta

- ▶ Model checking framework originally designed for verification using abstraction and refinement (CEGAR)
- ▶ **Modular, extensible architecture**, supporting different verification engines (CEGAR, BMC, etc.).
- ▶ Emphasis on **reusability** across domains (control-flow automata, transition systems, statecharts, CHCs, ...).
- ▶ Supports **reusability** through regular updates on maven
- ▶ Full verifier packages via Docker, and Zenodo releases

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Optimization techniques (novel)

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Experiment and data analysis
Evaluation of impact

Theta as a Verifier
Theta as a Framework

Availability