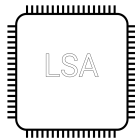


Libre Silicon Alliance

leviathan/talamon

December 15, 2017



What we do

- Breaking the monopoly of big semiconductor manufacturers
- Eliminating the vendor lock-in to big semiconductor manufacturers
- Making semiconductor development super quick and inexpensive (a few weeks and 50-100 USD per run)

Community projects



Icarus Verilog

Yosys Open SYnthesis Suite



Companies and institutions

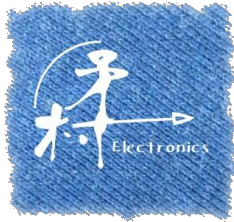


香港科技大學
THE HONG KONG
UNIVERSITY OF SCIENCE
AND TECHNOLOGY

NFF

Nanoelectronics Fabrication Facility

efabless.com



How we do it

- Introducing an open source chip manufacturing process standard specification
- Introducing an fully integrated free EDA for ASIC design¹

¹<https://github.com/leviathanch/qtflow>

Help needed

- Help developing QtFlow²
- Help with the LibreSilicon process³
- Work on the standard logic cells
- Work on developing FreeFLASH
- Work on developing FreeDRAM
- Donations

¹<https://github.com/leviathanch/qtflow>

²<https://github.com/leviathanch/libresiliconprocess>

Thank you!