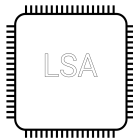# Libre Silicon Alliance

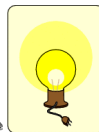leviathan/talamon

December 15, 2017

LSA

What we do

- Breaking the monopoly of big semiconductor manufacturers
- Eliminating the vendor lock-in to big semiconductor manufacturers
- Making semiconductor development super quick and inexpensive
  (a few weeks and 50-100 USD per run)

Community projects

Icarus Verilog

Yosys Open SYnthesis Suite

Companies and institutions

How we do it

- Introducing an open source chip manufacturing process standard specification
- Introducing an fully integrated free EDA for ASIC design[1]

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[1]https://github.com/leviathanch/qtflow

Help needed

- Help developing QtFlow[2]
- Help with the LibreSilicon process[3]
- Work on the standard logic cells
- Work on developing FreeFLASH
- Work on developing FreeDRAM
- Donations

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[1]https://github.com/leviathanch/qtflow
[2]https://github.com/leviathanch/libresiliconprocess

Thank you!