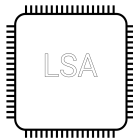


## Libre Silicon Alliance

leviathan/talamon

December 15, 2017



## What we do

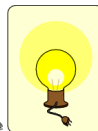
- Breaking the monopoly of big semiconductor manufacturers
- Eliminating the vendor lock-in to big semiconductor manufacturers
- Making semiconductor development super quick and inexpensive (a few weeks and 50-100 USD per run)

## Community projects



*Icarus Verilog*

Yosys Open SYnthesis Suite



## Companies and institutions



## How we do it

- Introducing an open source chip manufacturing process standard specification

# Help needed

- Help with QtFlow<sup>1</sup>
- Help with the LibreSilicon process<sup>2</sup>

---

<sup>1</sup><https://github.com/leviathanch/qtflow>

<sup>2</sup><https://github.com/leviathanch/libresiliconprocess>