Libre Silicon Alliance

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What we do

- Breaking the monopoly of big semiconductor manufacturers
- Eliminating the vendor lock-in to big semiconductor manufacturers
- Making semiconductor development super quick and inexpensive (a few weeks and 50-100 USD per run)

Community projects





Yosys Open SYnthesis Suite

Companies and institutions









How we do it

- Introducing an open source chip manufacturing process standard specification
- Introducing an fully integrated free EDA for ASIC design¹

¹https://github.com/leviathanch/qtflow

Why are we doing this?

- MPWs cost around 20'000 USD nowadays
- MPWs take around 2-9 months nowadays
- No manufacturer provides the GDS2 files in order to manufacture the designs in your basement

Help needed

- Help developing QtFlow²
- Help with the LibreSilicon process³
- Work on the standard logic cells
- Work on developing FreeFLASH
- Work on developing FreeDRAM
- Donations

¹https://github.com/leviathanch/qtflow

²https://github.com/leviathanch/libresiliconprocess

Thank you!