K1
TS-501 2P

FU1
Fuse-PTF15

RT1
20R

C6
1nF

L3
DLF-302U-2A

Br1
DB104-S

C8
150uF

R2
5%
33k

C5
2.7n

R4
1%
6R8

D3
1N4937

R6
1%
1M

R8
1%
1M

C1
2.2n

Tr1

1
10
9
6
8
7
5
4

24V2
24V
5V

EF25-5V-24V-24V

R1
1%
470k

C2
22p

D1
BYV26B

C3
50uF

L1
3.3uH

C4
100uF

+24V2

R3
1%
470k

C7
22p

D2
BYV26B

C9
220uF

L2
3.3uH

GND2

R5
1%
22

C11
470p

D4 SB180

C12
470uF

L4
3.3uH

C13
100uF

GND

D5
1N914

C14
1uF

R7
1%
100

DA1
LTV817

R9
1%
1k

R10
5%
5k1

U1
TOP245Y

HS1

D
Con
S   X   F

C16
100nF

R12
1%
6R8

C17
47u

U2
TL431

C15
680nF

R11
5%
5k1

B1 ⊳ B2 ⊳ B3 ⊳

GND

+24V   +5V

J1
+5V   +24V

1
2
3
4

GND

Valve4_gate

Подключение клапанов

J3
1
2
3
4

+24V

D6
SS34

T1
IRLR2905

R15
1%
100

Valve3_gate

R18
1%
18k

GND   GND

+24V

D7
SS34

T2
IRLR2905

R16
1%
100

Valve2_gate

R19
1%
18k

GND   GND

+24V

D8
SS34

T3
IRLR2905

R17
1%
100

Valve1_gate

R20
1%
18k

GND   GND

CPU board

J2
+24V2   +5V

1
2
3
4
5
6
7
8
9
10

Valve1_gate
Valve2_gate
Valve3_gate
Valve4_gate

GND   GND2

+24V2   +24V

LD1
Led

LD2
Led

R13
1%
5k6

R14
1%
5k6

GND2   GND

Title

Size
A4

Number

Revision

Date: 17.01.2017
File: D:\info\..\PS_Board_p1.SchDoc

Sheet of
Drawn By:

K1
RT1
L3
C5
R2
C1
B2
C2
C3
L1
J3
Valves
CPU brd
T1
T2
T3
R1
D1
C6
C8
R4
Tr1
C11
R5
J2
R15
R18
R16
R19
R17
R20
Spirt controller v 2.0
FU1
D3
R7
C12
D4
L4
C13
R6
R8
HS1
D5
DA1
R9
D2
C9
R10
C4
U1
C14
U2
R11
R3
C10
L2
C7
Control module
PS PCB v 1.0
LD2
R14
R13
LD1
B3
Nikopol (C)
R12
J1
B1

K1 RT1 L3 C5 R2 C1 B2 C2 C3 L1 J3 Valves

CPU brd

R1 D1 T1 T2 T3

Spirt controller v 2.0

C6 C8 R4 Tr1 C11 R5 D4 R15 R18 R16 R19 R17 R20 J2

FU1 D3 R7 C12 L4 C13 C4

R6 R8 HS1 D5 DA1 R9 R11 R3 D2 L4 C9 R10

U1 C17 U2 C14 C7 C10

Nikopol (C) R12 B3 C9 L2

Control module PS PCB v 1.0 LD2 R14 R13 LD1 J1 B1

D8 D7 D6

Br1

C16

C15

C16