

Levi Kaplan EECE2160	Embedded Design: Enabling Robotics Prelab Assignment 4
-------------------------	---

Prelab Assignment 4

Introduction to Hardware

Levi Kaplan
kaplan.l@northeastern.edu

Submit Date: 10/15/20
Due Date: 10/15/20

Digit Table

INPUTS				Character	OUTPUTS						
D3	D2	D1	D0	#	a	b	c	d	e	f	g
0	0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	1	0	1	1	0	0	0	0
0	0	1	0	2	1	1	0	1	1	0	1
0	0	1	1	3	1	1	1	1	0	0	1
0	1	0	0	4	0	1	1	0	0	1	1
0	1	0	1	5	1	0	1	1	0	1	1
0	1	1	0	6	1	0	1	1	1	1	1
0	1	1	1	7	1	1	1	0	0	0	0
1	0	0	0	8	1	1	1	1	1	1	1
1	0	0	1	9	1	1	1	0	0	1	1
1	0	1	0	A	1	1	1	0	1	1	1
1	0	1	1	b	0	0	1	1	1	1	1
1	1	0	0	c	1	0	0	1	1	1	0
1	1	0	1	d	0	1	1	1	1	0	1
1	1	1	0	E	1	0	0	1	1	1	1
1	1	1	1	F	1	0	0	0	1	1	1

Boolean equation

Prelab 4

$$A = \begin{array}{cccc|c} \overline{D_0} & \overline{D_1} & \overline{D_2} & \overline{D_3} & + \\ \overline{D_0} & D_1 & \overline{D_2} & \overline{D_3} & + \\ D_0 & D_1 & \overline{D_2} & \overline{D_3} & + \\ D_0 & \overline{D_1} & D_2 & \overline{D_3} & + \\ \overline{D_0} & D_1 & D_2 & \overline{D_3} & + \\ D_0 & D_1 & D_2 & \overline{D_3} & + \\ \overline{D_0} & \overline{D_1} & \overline{D_2} & D_3 & + \\ D_0 & \overline{D_1} & \overline{D_2} & D_3 & + \\ \overline{D_0} & D_1 & \overline{D_2} & D_3 & + \\ \overline{D_0} & \overline{D_1} & D_2 & D_3 & + \\ \overline{D_0} & D_1 & D_2 & D_3 & + \\ D_0 & D_1 & D_2 & D_3 & \end{array} \begin{array}{c} 0 \\ 2 \\ 3 \\ 5 \\ 6 \\ 7 \\ 8 \\ 9 \\ A \\ C \\ E \\ F \end{array}$$

$$b = \begin{array}{cccc|c} \overline{D_0} & \overline{D_1} & \overline{D_2} & \overline{D_3} & + \\ D_0 & \overline{D_1} & \overline{D_2} & \overline{D_3} & + \\ \overline{D_0} & D_1 & \overline{D_2} & \overline{D_3} & + \\ D_0 & D_1 & \overline{D_2} & \overline{D_3} & + \\ \overline{D_0} & \overline{D_1} & D_2 & \overline{D_3} & + \\ D_0 & D_1 & D_2 & \overline{D_3} & + \\ \overline{D_0} & \overline{D_1} & \overline{D_2} & D_3 & + \\ D_0 & \overline{D_1} & \overline{D_2} & D_3 & + \\ \overline{D_0} & D_1 & \overline{D_2} & D_3 & + \\ D_0 & \overline{D_1} & D_2 & D_3 & \end{array} \begin{array}{c} 0 \\ 1 \\ 2 \\ 3 \\ 4 \\ 7 \\ 8 \\ 9 \\ A \\ d \end{array}$$

$$C = \begin{array}{cccc|c} \overline{D_0} & \overline{D_1} & \overline{D_2} & \overline{D_3} & + & 0 \\ D_0 & \overline{D_1} & \overline{D_2} & \overline{D_3} & + & 1 \\ D_0 & D_1 & \overline{D_2} & \overline{D_3} & + & 3 \\ \overline{D_0} & \overline{D_1} & D_2 & \overline{D_3} & + & 4 \\ D_0 & \overline{D_1} & D_2 & \overline{D_3} & + & 5 \\ \overline{D_0} & D_1 & D_2 & \overline{D_3} & + & 6 \\ D_0 & D_1 & D_2 & \overline{D_3} & + & 7 \\ \overline{D_0} & \overline{D_1} & \overline{D_2} & D_3 & + & 8 \\ D_0 & \overline{D_1} & \overline{D_2} & D_3 & + & 9 \\ \overline{D_0} & D_1 & \overline{D_2} & D_3 & + & A \\ D_0 & D_1 & \overline{D_2} & D_3 & + & b \\ D_0 & \overline{D_1} & D_2 & D_3 & & d \end{array}$$

$$d = \begin{array}{cccc|c} \overline{D_0} & \overline{D_1} & \overline{D_2} & \overline{D_3} & + & 0 \\ D_0 & D_1 & \overline{D_2} & \overline{D_3} & + & 2 \\ D_0 & D_1 & \overline{D_2} & \overline{D_3} & + & 3 \\ D_0 & \overline{D_1} & D_2 & \overline{D_3} & + & 5 \\ \overline{D_0} & D_1 & D_2 & \overline{D_3} & + & 6 \\ \overline{D_0} & \overline{D_1} & \overline{D_2} & D_3 & + & 8 \\ \overline{D_0} & D_1 & \overline{D_2} & D_3 & + & A \\ D_0 & D_1 & \overline{D_2} & D_3 & + & b \\ \overline{D_0} & \overline{D_1} & D_2 & D_3 & + & c \\ D_0 & \overline{D_1} & D_2 & D_3 & + & d \\ \overline{D_0} & D_1 & D_2 & D_3 & + & E \\ D_0 & D_1 & D_2 & D_3 & & F \end{array}$$

$$\begin{aligned}
 e = & \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} + & 0 \\
 & \overline{D_0} \overline{D_1} \overline{D_2} D_3 + & 2 \\
 & \overline{D_0} \overline{D_1} D_2 \overline{D_3} + & 6 \\
 & \overline{D_0} \overline{D_1} D_2 D_3 + & 4 \\
 & \overline{D_0} D_1 \overline{D_2} \overline{D_3} + & A \\
 & \overline{D_0} D_1 \overline{D_2} D_3 + & b \\
 & \overline{D_0} \overline{D_1} D_2 D_3 + & C \\
 & D_0 \overline{D_1} D_2 D_3 + & d \\
 & \overline{D_0} D_1 D_2 D_3 + & E \\
 & D_0 D_1 D_2 D_3 & F
 \end{aligned}$$

$$\begin{aligned}
 f = & \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} + & 0 \\
 & \overline{D_0} \overline{D_1} D_2 \overline{D_3} + & 4 \\
 & \overline{D_0} \overline{D_1} D_2 D_3 + & 5 \\
 & \overline{D_0} D_1 D_2 \overline{D_3} + & 6 \\
 & \overline{D_0} \overline{D_1} \overline{D_2} D_3 + & 8 \\
 & \overline{D_0} \overline{D_1} \overline{D_2} D_3 + & 9 \\
 & \overline{D_0} D_1 \overline{D_2} \overline{D_3} + & A \\
 & \overline{D_0} D_1 \overline{D_2} D_3 + & b \\
 & \overline{D_0} \overline{D_1} D_2 D_3 + & C \\
 & \overline{D_0} D_1 D_2 D_3 + & E \\
 & D_0 D_1 D_2 D_3 & F
 \end{aligned}$$

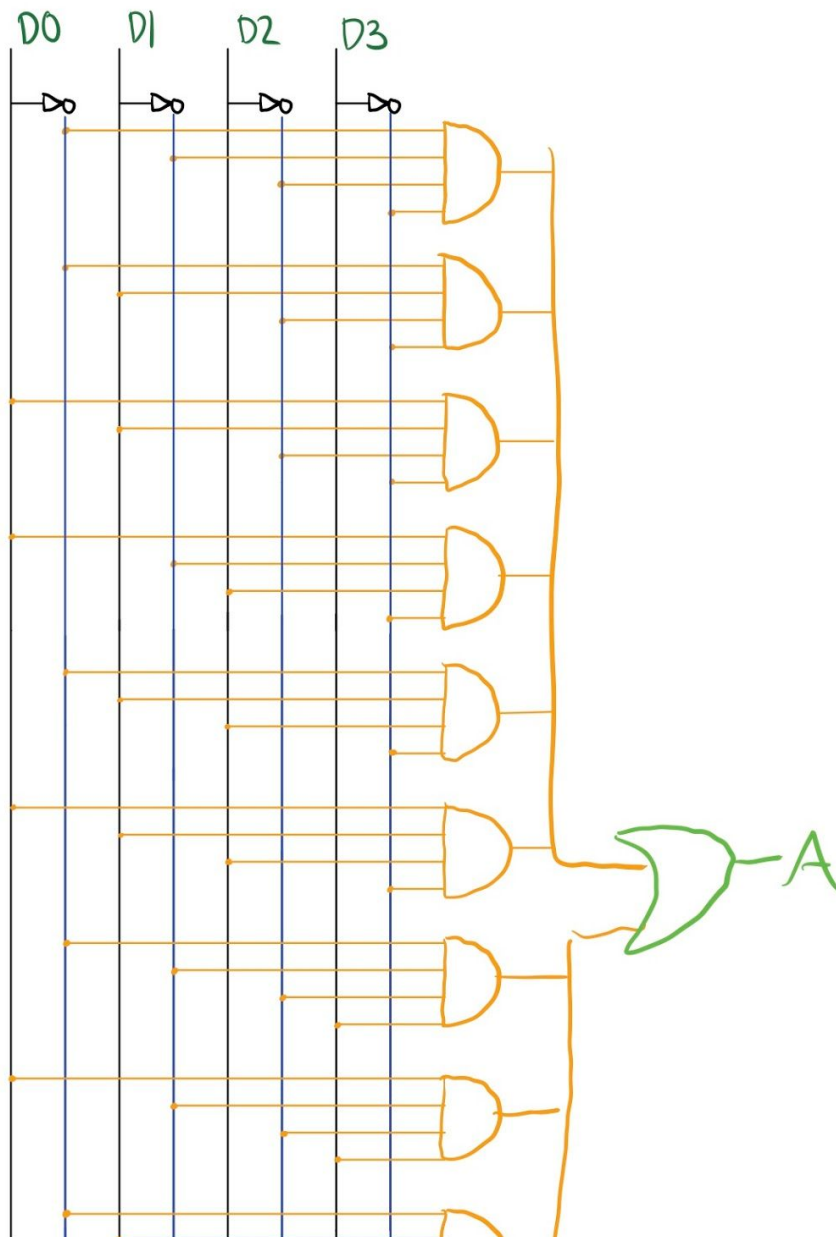
$$g = \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} + 2$$

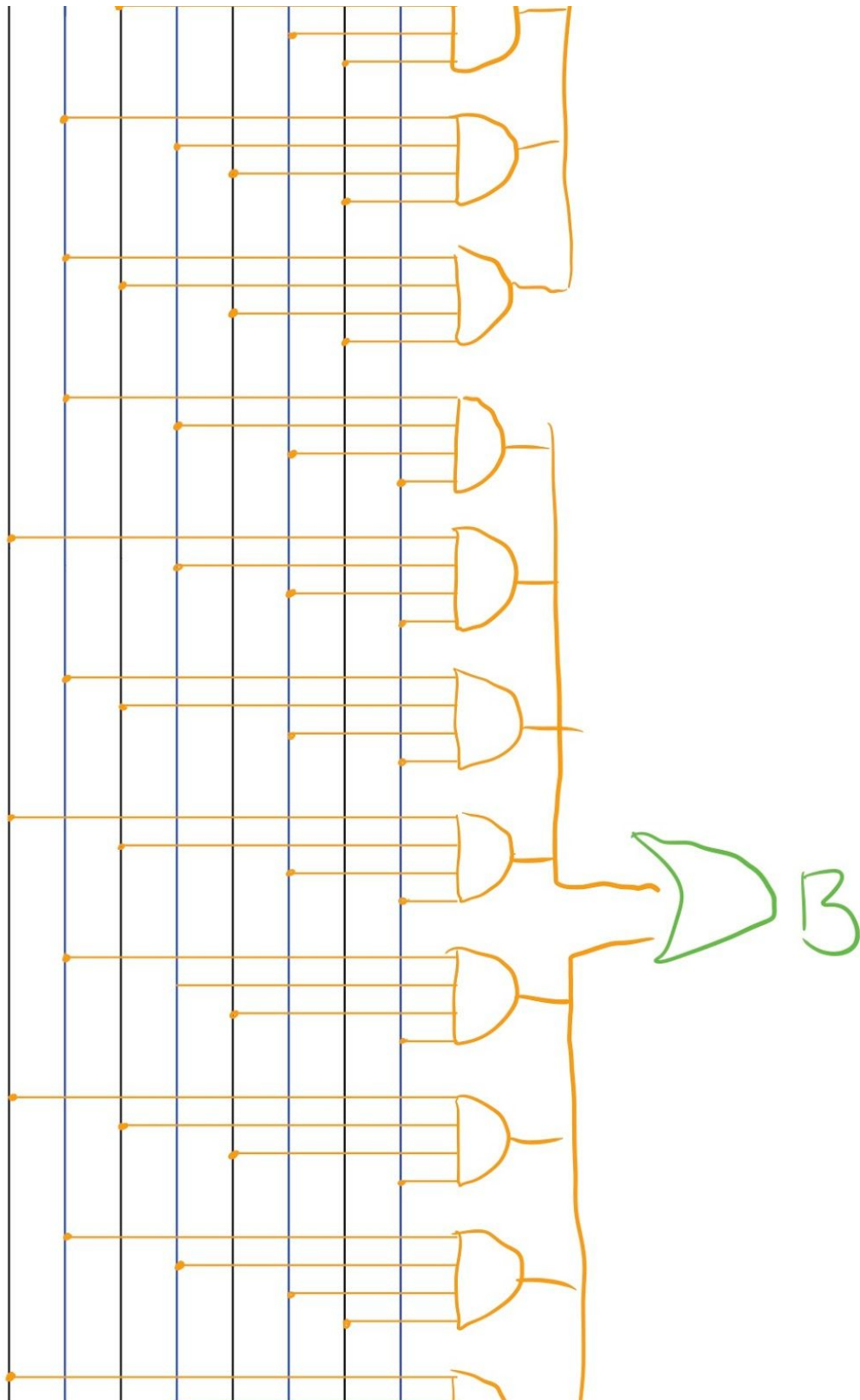
$\overline{D_0}$	$\overline{D_1}$	$\overline{D_2}$	$\overline{D_3}$	+	3
$\overline{D_0}$	$\overline{D_1}$	D_2	$\overline{D_3}$	+	4
D_0	$\overline{D_1}$	D_2	$\overline{D_3}$	+	5
$\overline{D_0}$	D_1	D_2	$\overline{D_3}$	+	6
$\overline{D_0}$	$\overline{D_1}$	$\overline{D_2}$	D_3	+	8
D_0	$\overline{D_1}$	$\overline{D_2}$	D_3	+	9
$\overline{D_0}$	D_1	$\overline{D_2}$	D_3	+	A
D_0	D_1	$\overline{D_2}$	D_3	+	b
D_0	$\overline{D_1}$	D_2	D_3	+	d
$\overline{D_0}$	D_1	D_2	D_3	+	E
D_0	D_1	D_2	D_3		F

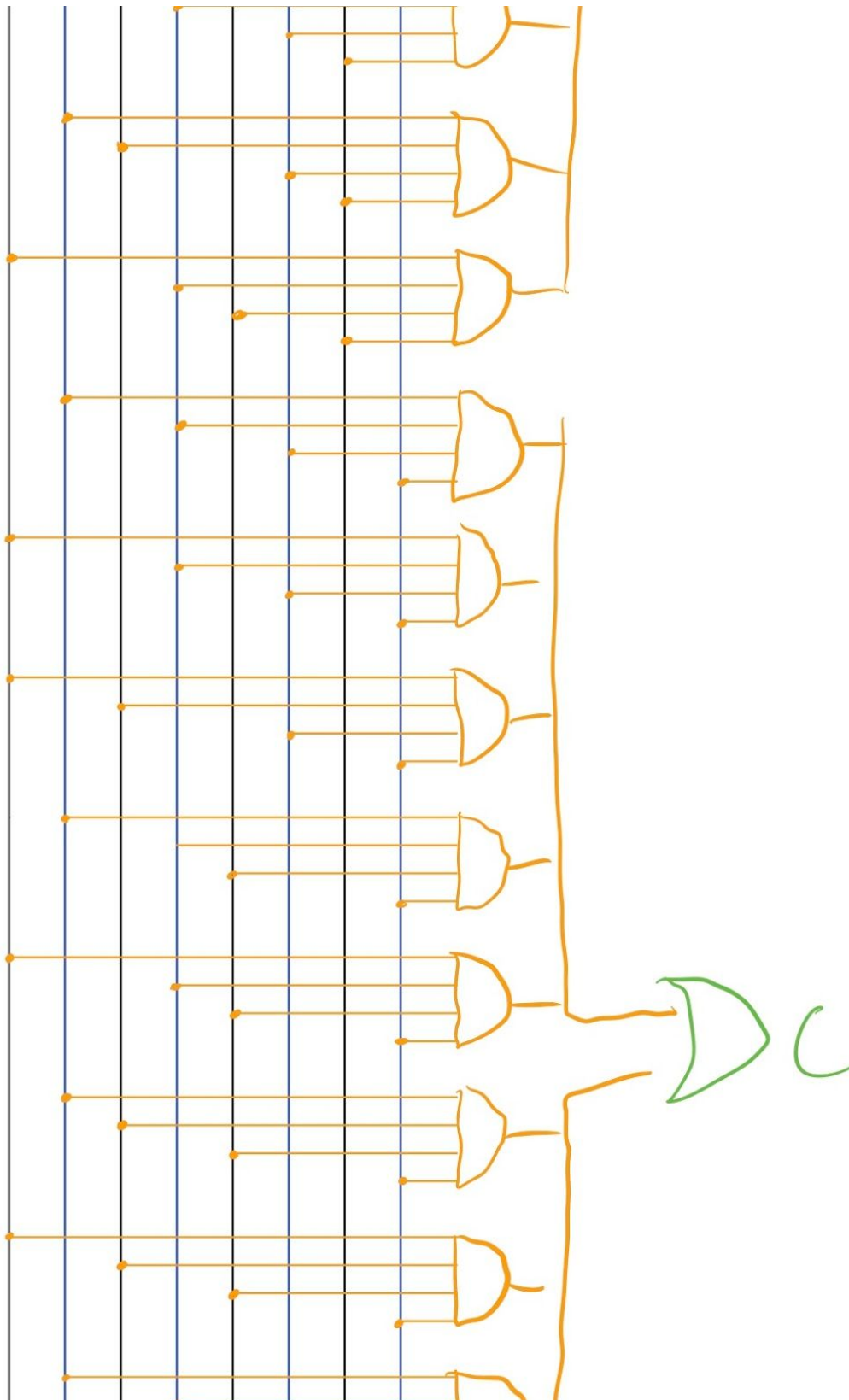
Q:	$\overline{D_0}$	$\overline{D_1}$	$\overline{D_2}$	$\overline{D_3}$
1:	D_0	$\overline{D_1}$	$\overline{D_2}$	$\overline{D_3}$
2:	$\overline{D_0}$	D_1	$\overline{D_2}$	$\overline{D_3}$
3:	D_0	D_1	$\overline{D_2}$	$\overline{D_3}$
4:	$\overline{D_0}$	$\overline{D_1}$	D_2	$\overline{D_3}$
5:	D_0	$\overline{D_1}$	D_2	$\overline{D_3}$
6:	$\overline{D_0}$	D_1	D_2	$\overline{D_3}$
7:	D_0	D_1	D_2	$\overline{D_3}$
8:	$\overline{D_0}$	$\overline{D_1}$	$\overline{D_2}$	D_3
9:	D_0	$\overline{D_1}$	$\overline{D_2}$	D_3
A:	$\overline{D_0}$	D_1	$\overline{D_2}$	D_3
b:	D_0	D_1	$\overline{D_2}$	D_3
C:	$\overline{D_0}$	$\overline{D_1}$	D_2	D_3
d:	D_0	$\overline{D_1}$	D_2	D_3
E:	$\overline{D_0}$	D_1	D_2	D_3
F:	D_0	D_1	D_2	D_3

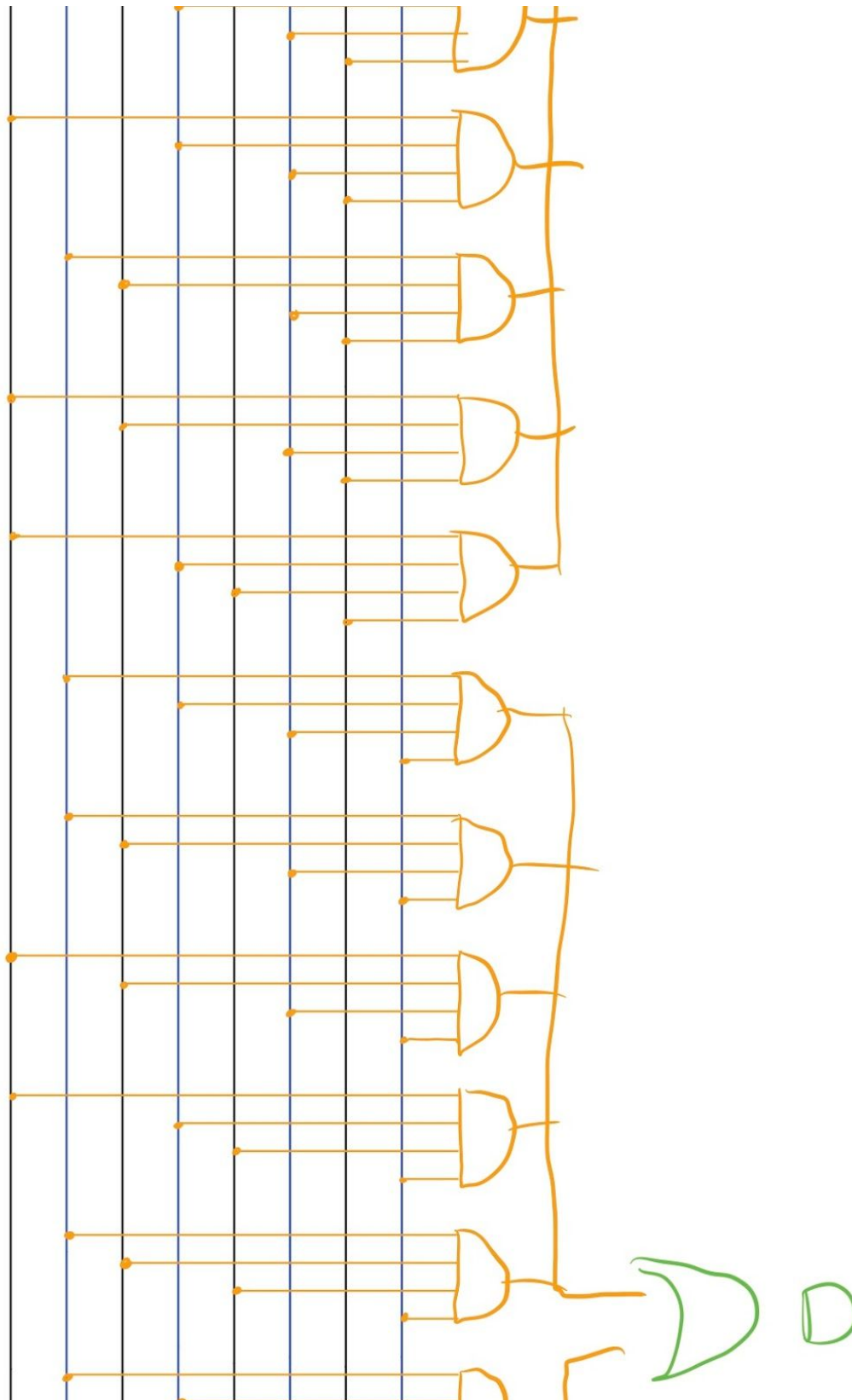
Full Schematic

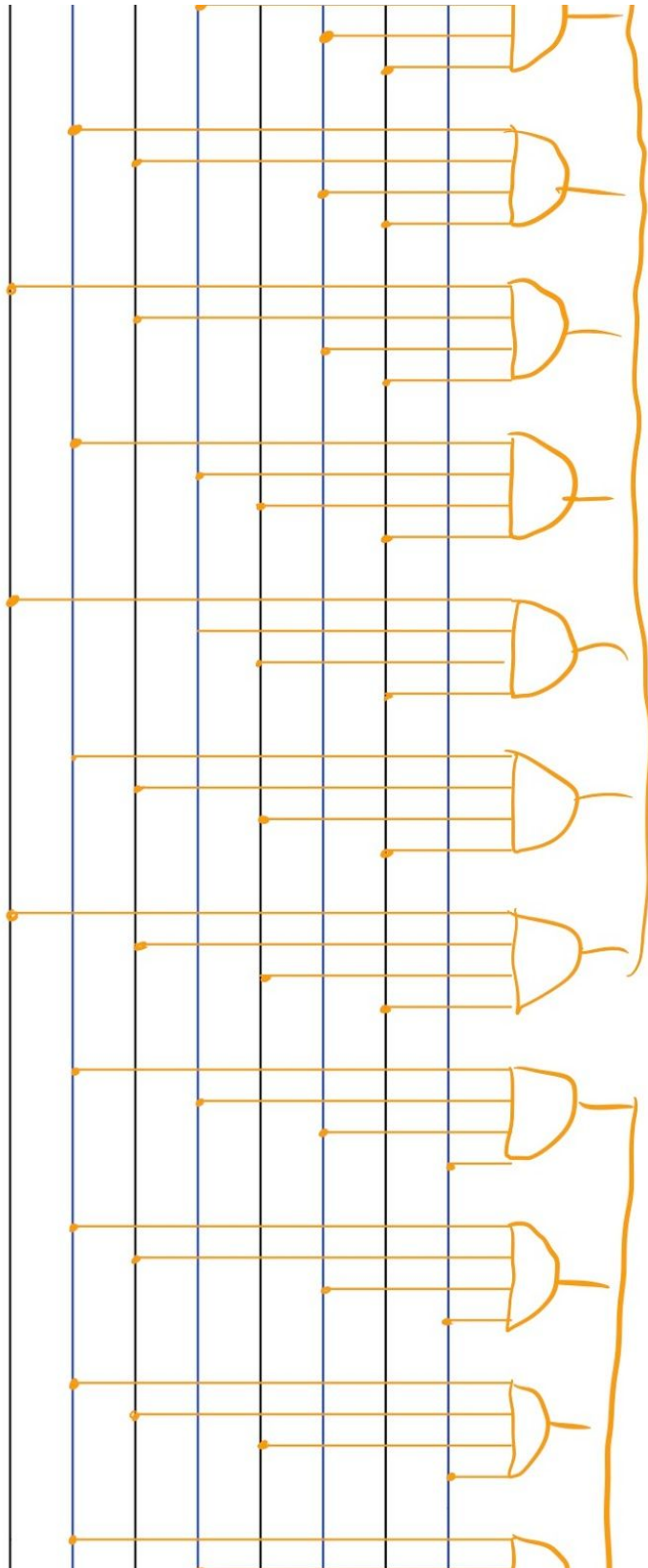
Prelab 4 Schematic

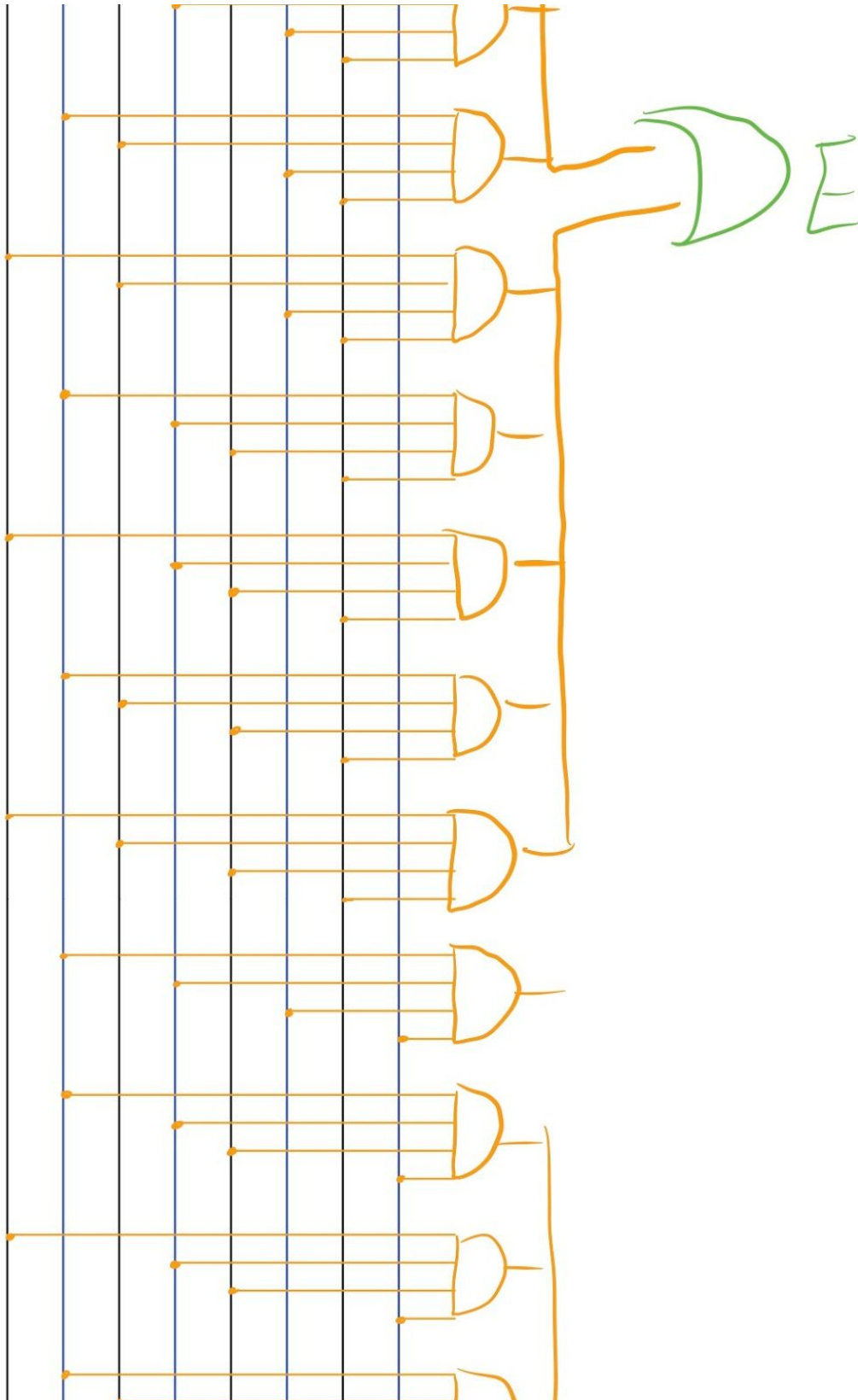


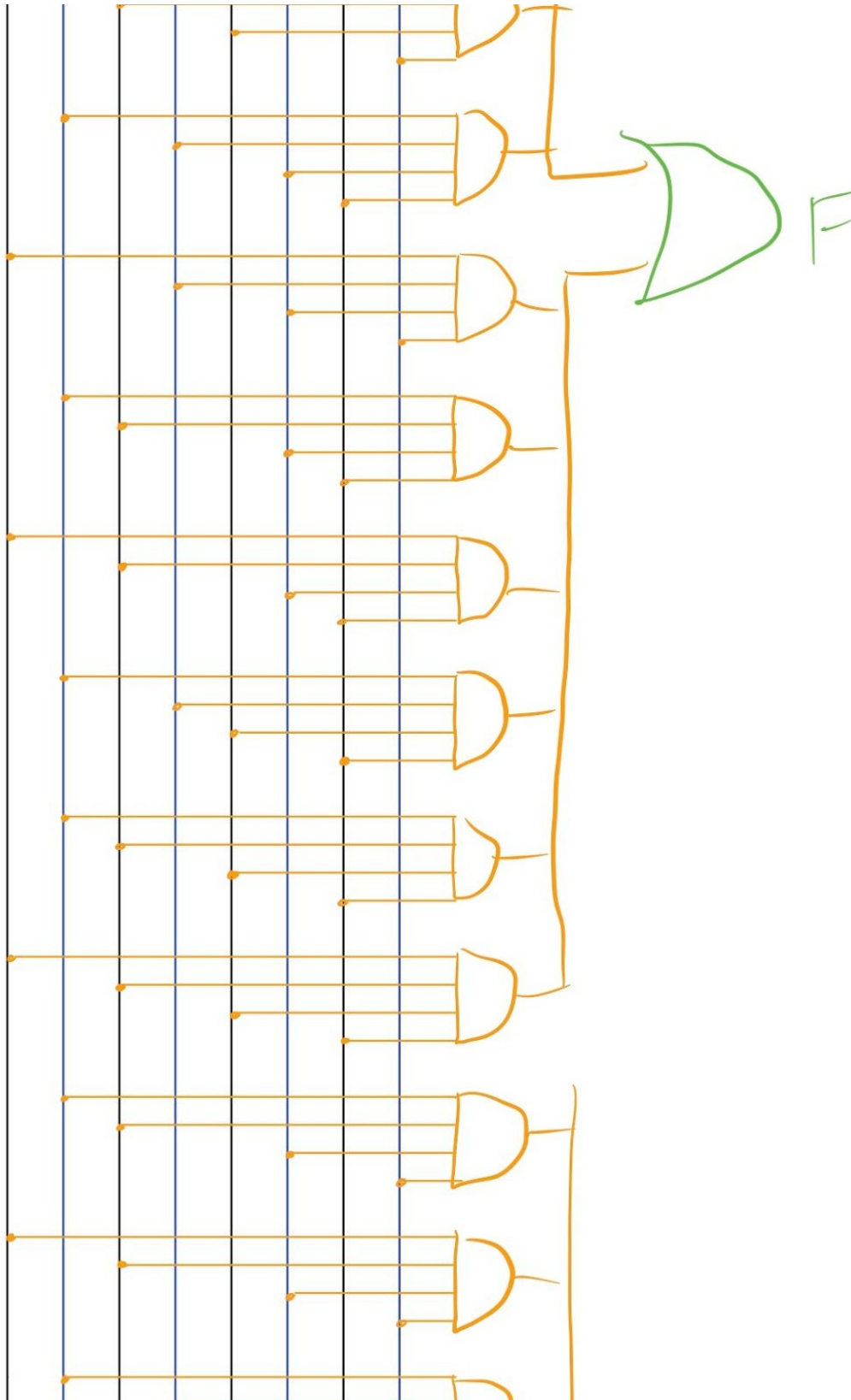


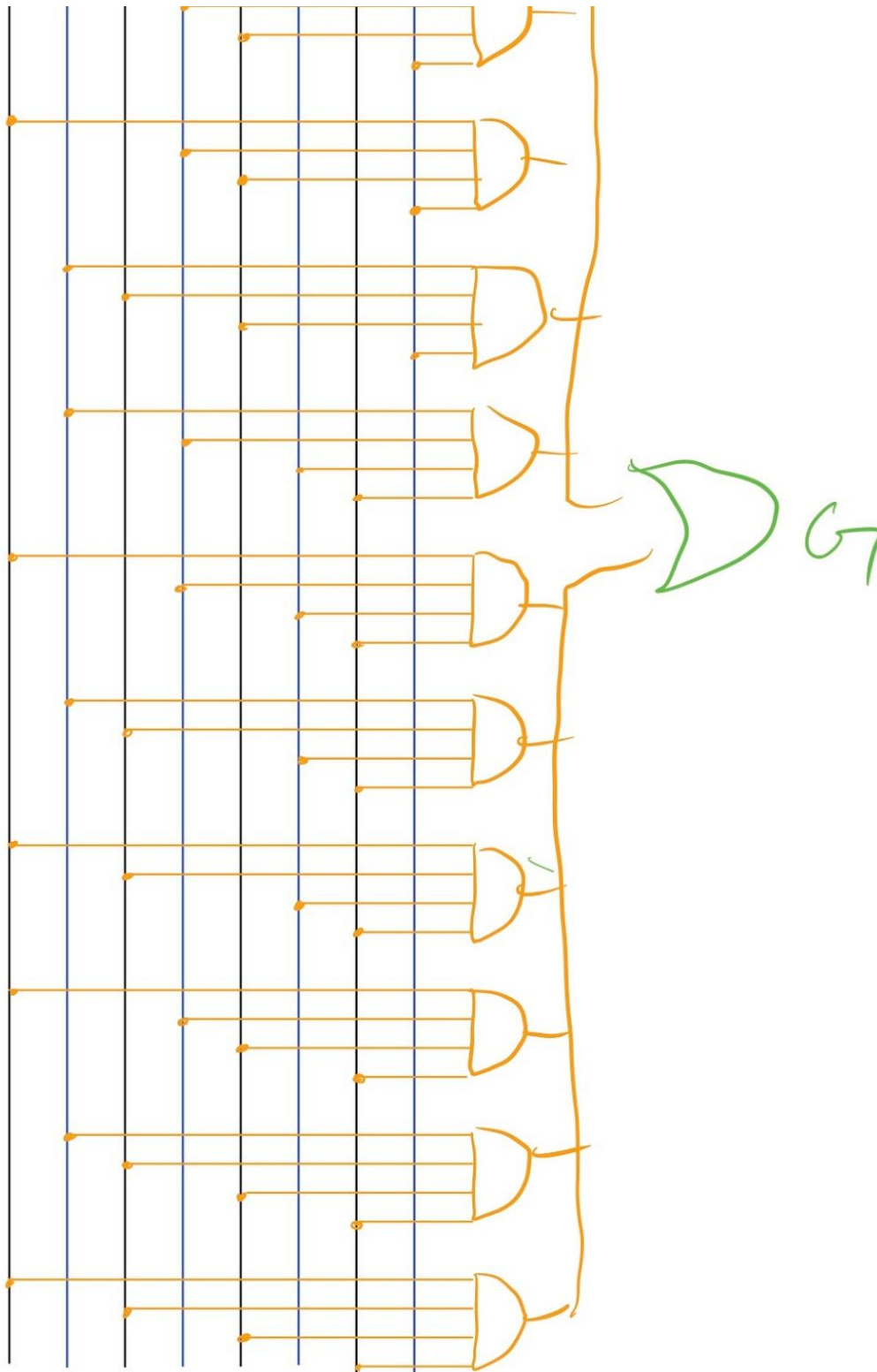








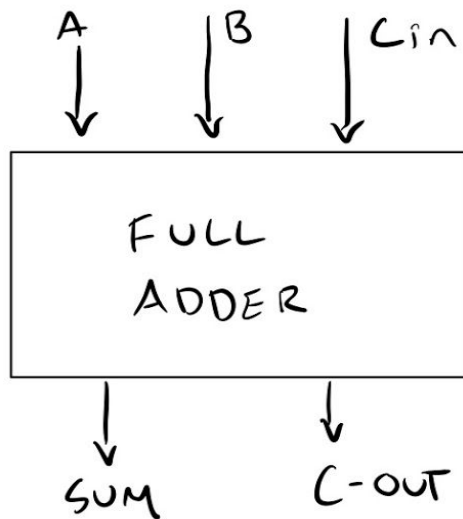




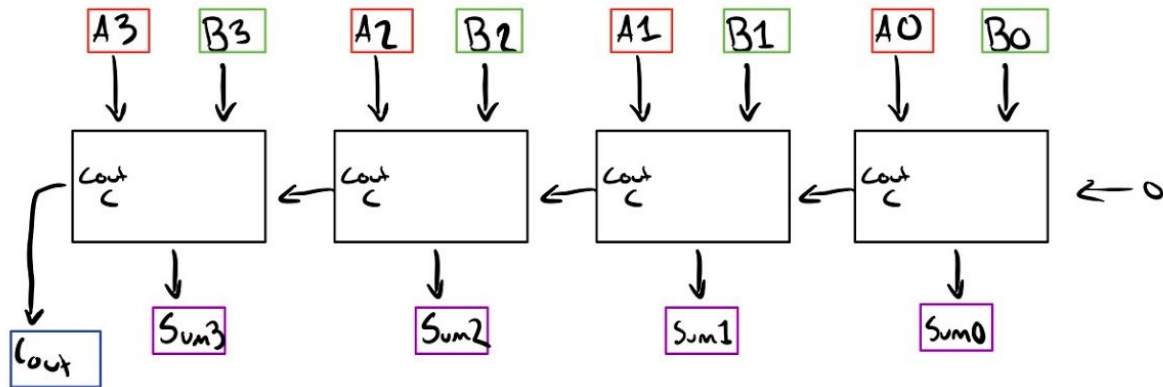
Truth table for full adder, schematic

d)

Input A	Input B	Input C	OUTPUT C-OUT	OUTPUT SUM
0	0	0	—	—
0	0	1	—	$\bar{A}\bar{B}C$
0	1	0	—	$\bar{A}B\bar{C}$
0	1	1	$\bar{A}BC$	—
1	0	0	—	$A\bar{B}\bar{C}$
1	0	1	$A\bar{B}C$	—
1	1	0	$AB\bar{C}$	—
1	1	1	ABC	ABC



e)



Levi Kaplan EECE2160	Embedded Design: Enabling Robotics Prelab Assignment 4
-------------------------	---

Levi Kaplan EECE2160	Embedded Design: Enabling Robotics Prelab Assignment 4
-------------------------	---

Levi Kaplan EECE2160	Embedded Design: Enabling Robotics Prelab Assignment 4
-------------------------	---