

CS3375: Computer Architecture Spring 2020

Quiz #5 Solution

- Full name only: _____
- Release date: Apr 3rd, 2020 (Friday)
- Due date: **Apr 3rd, 2020 (Friday) before midnight**
- Total 5 points

[True/False] A single cycle data implementation is not efficient because the length of clock cycle time depends on the fastest instruction, such as lw.

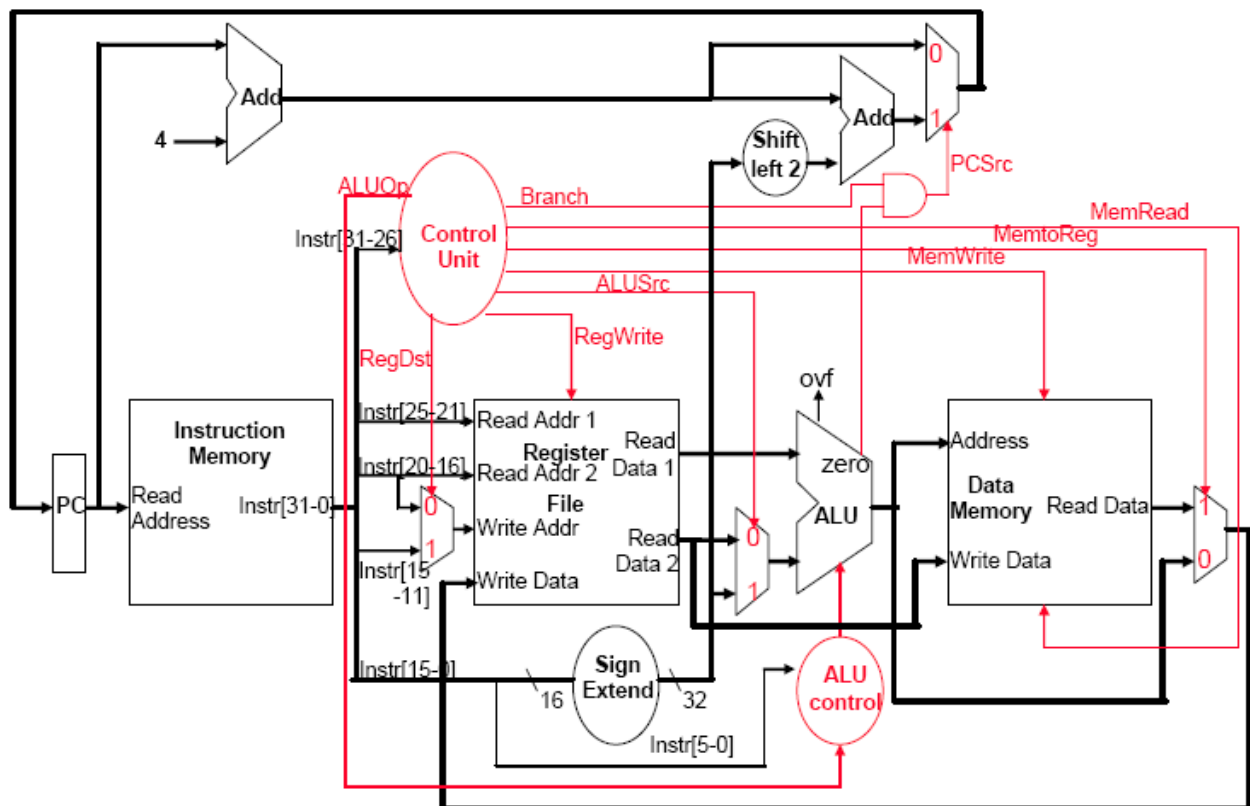
[1 pt]

- False

2. Based on the following instruction, fill out the control signal table. If a control signal value cannot be determined, mark it as "X".

beq \$t1, \$t2, offset

[4 pts]



RegDst	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	ALUOp1	ALUOp0
X	0	x	0	0	0	1	0	1