

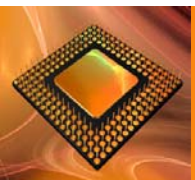


# Modern Digital System Design

ECE 2372 / Fall 2018 / Lecture 10

Texas Tech University  
Dr. Tooraj Nikoubin

Coding

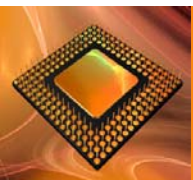


# Binary-coded decimal



$2^3$	$2^2$	$2^1$	$2^0$
A	B	C	D

BCD				
	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1



# Binary-coded decimal



2	4	2	1
A	B	C	D

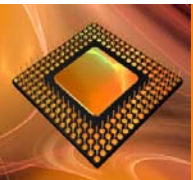
Aiken

	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

$2^3$	$2^2$	$2^1$	$2^0$
A	B	C	D

BCD

	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1



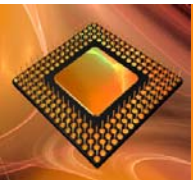
# Aiken



**self-complementary**

	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

2	4	2	1
A	B	C	D



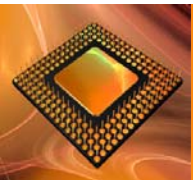
# Aiken



**self-complementary**

	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

2	4	2	1
A	B	C	D

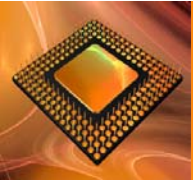


# Aiken



	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

2	4	2	1
A	B	C	D

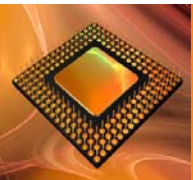


# Aiken



	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

2	4	2	1
A	B	C	D



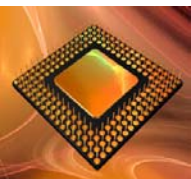
# self-complementary



	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

2	4	2	1
A	B	C	D



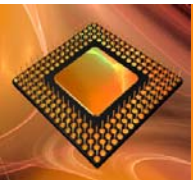


	A	B	C	D

Excess_3				
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0

	$2^3$	$2^2$	$2^1$	$2^0$
	A	B	C	D

BCD				
	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1

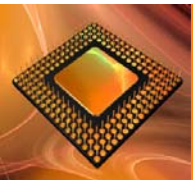


# self-complementary



**self-complementary**

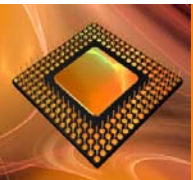
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0



# self-complementary



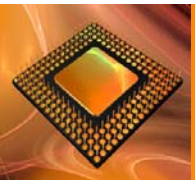
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0



# self-complementary



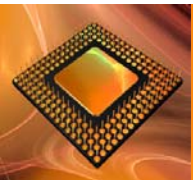
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0



# self-complementary



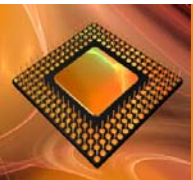
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0



# self-complementary



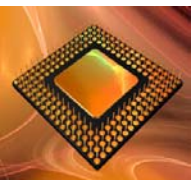
	A	B	C	D
0	0	0	1	1
1	0	1	0	0
2	0	1	0	1
3	0	1	1	0
4	0	1	1	1
5	1	0	0	0
6	1	0	0	1
7	1	0	1	0
8	1	0	1	1
9	1	1	0	0



# Johnson Code



	A	B	C	D	
Johnson					
	A	B	C	D	E
0	0	0	0	0	0
1	0	0	0	0	1
2	0	0	0	1	1
3	0	0	1	1	1
4	0	1	1	1	1
5	1	1	1	1	1
6	1	1	1	1	0
7	1	1	1	0	0
8	1	1	0	0	0
9	1	0	0	0	0

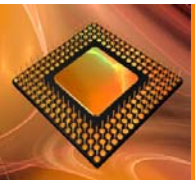


# 2\_Out\_of\_5



	A	B	C	D	E
	2_Out_of_5				
	A	B	C	D	E
0	1	1	0	0	0
1	0	0	0	1	1
2	0	0	1	0	1
3	0	0	1	1	0
4	0	1	0	0	1
5	0	1	0	1	0
6	0	1	1	0	0
7	1	0	0	0	1
8	1	0	0	1	0
9	1	0	1	0	0

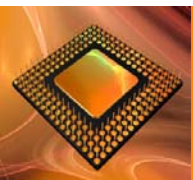




# Gray Code



Gray				
	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	1
3	0	0	1	0
4	0	1	1	0
5	0	1	1	1
6	0	1	0	1
7	0	1	0	0
8	1	1	0	0
9	1	1	0	1
10	1	1	1	1
11	1	1	1	0
12	1	0	1	0
13	1	0	1	1
14	1	0	0	1
15	1	0	0	0



# K\_map for BCD, EX-3 and Aiken



BCD

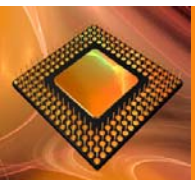
		A		D
C	0	4	x	8
	1	5	x	9
	3	7	x	x
	2	6	x	x
		B		

Aiken

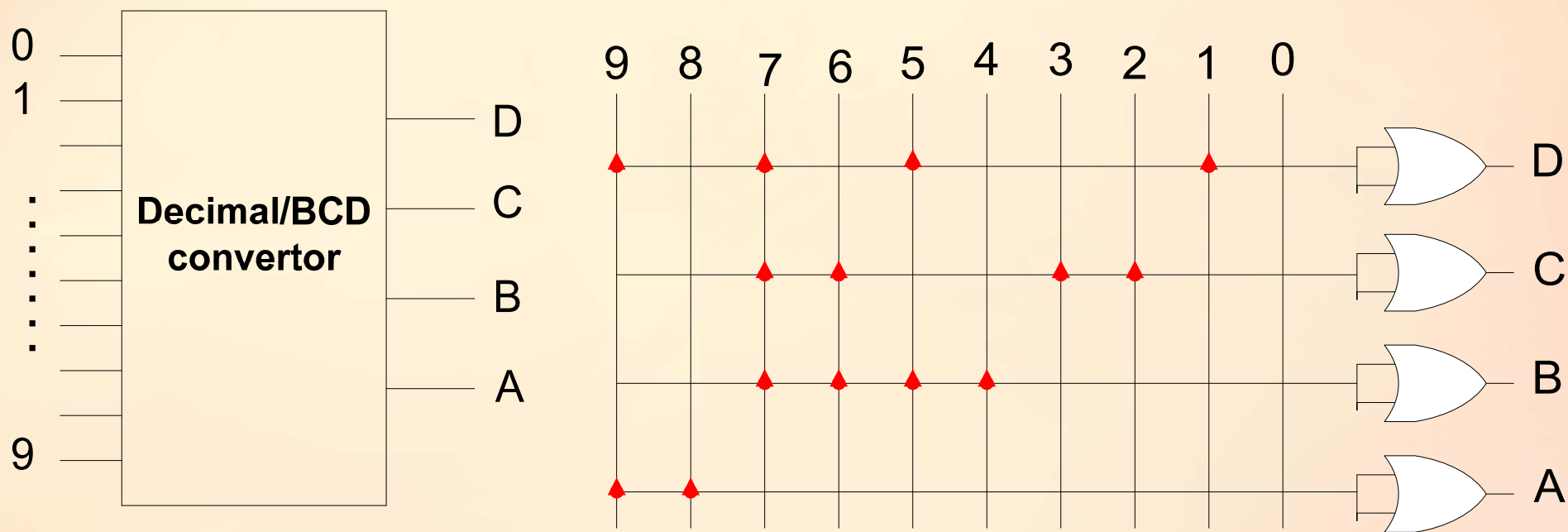
		A		D
C	0	4	6	x
	1	x	7	x
	3	x	9	5
	2	x	8	x
		B		

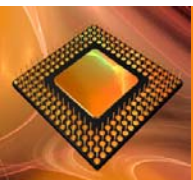
Ex-3

		A		D
C	x	1	9	5
	x	2	x	6
	0	4	x	8
	x	3	x	7
		B		

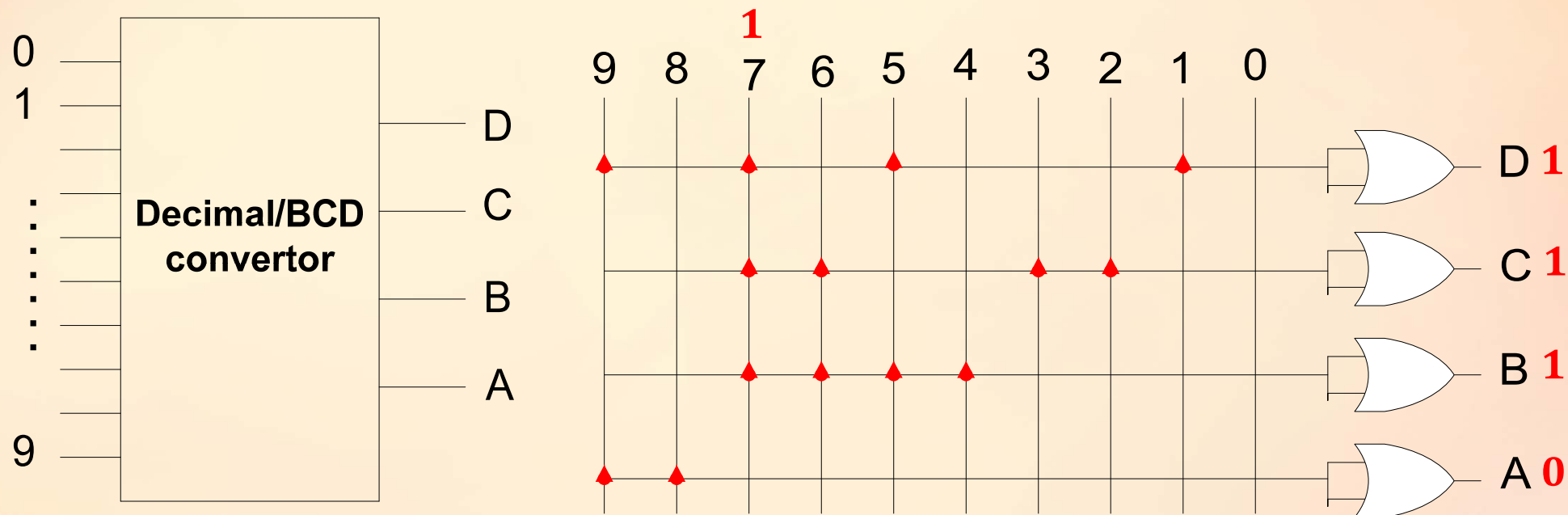


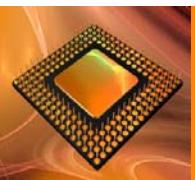
# Decimal to BCD Converter



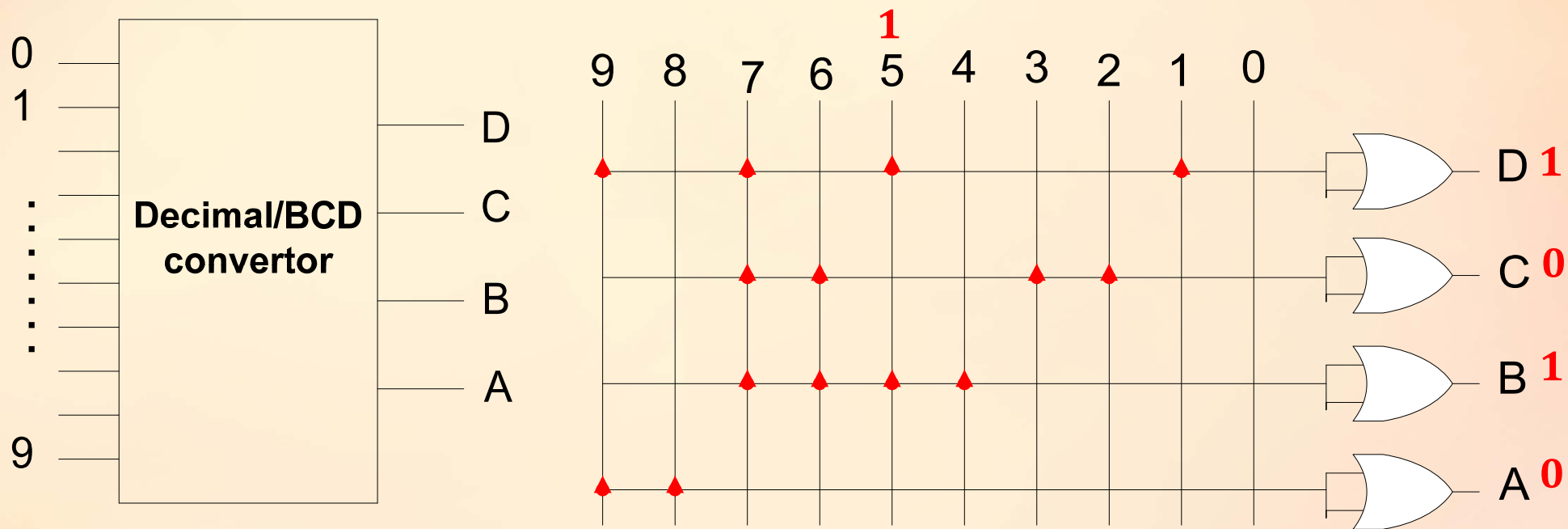


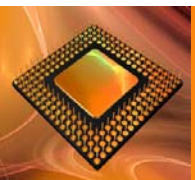
# Decimal to BCD Converter



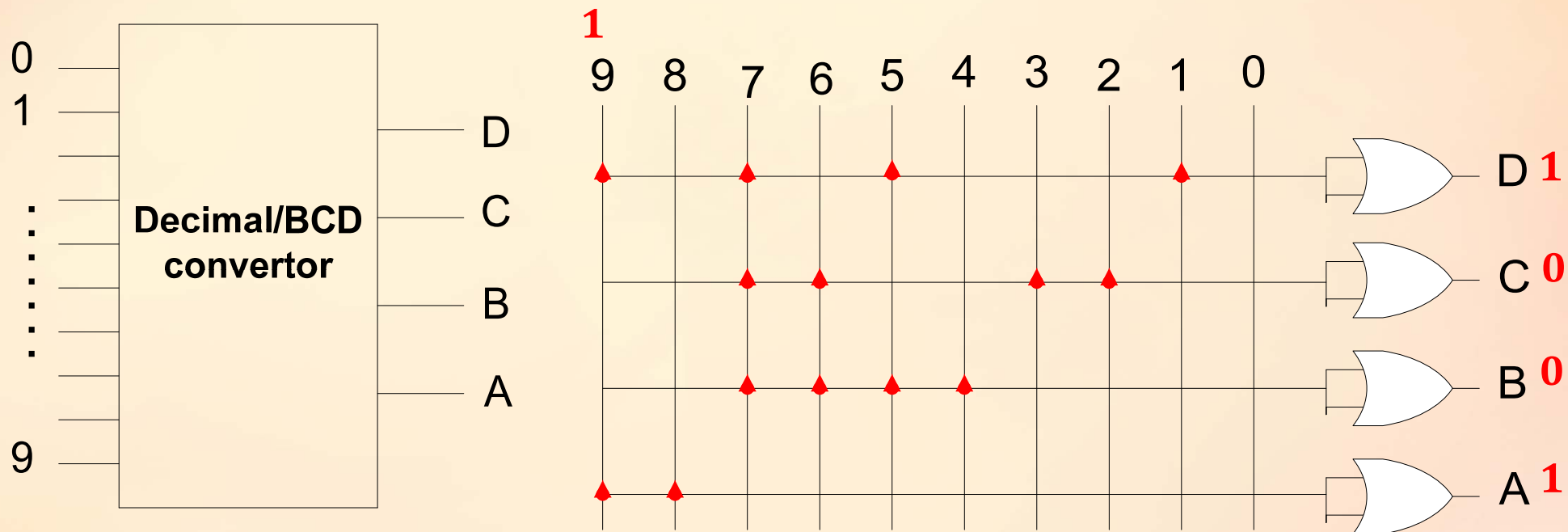


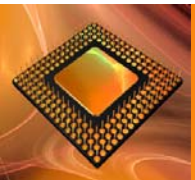
# Decimal to BCD Converter



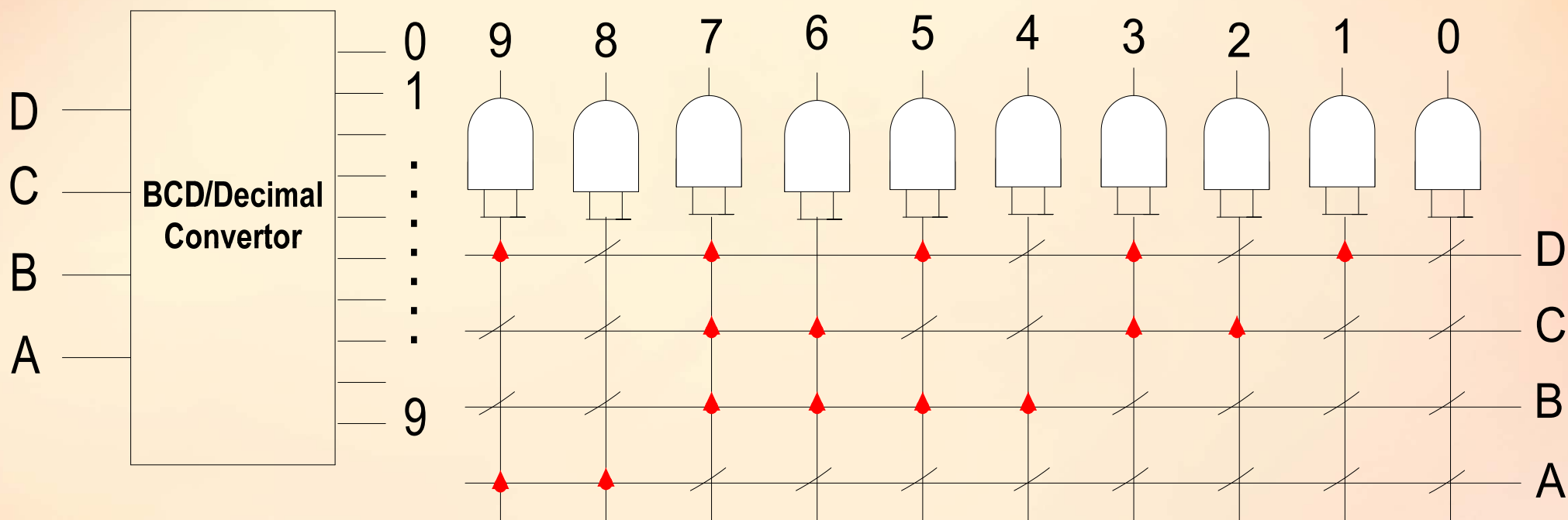


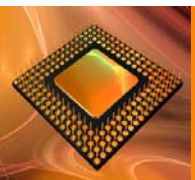
# Decimal to BCD Converter



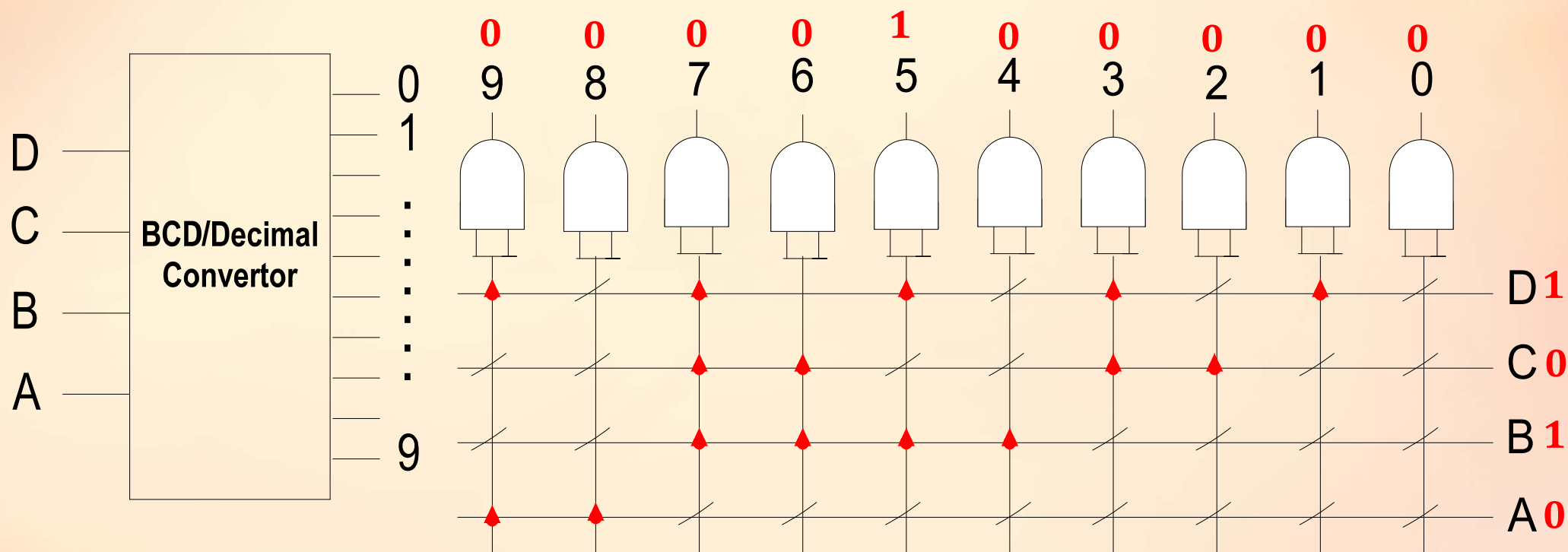


# BCD to Decimal Converter

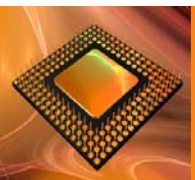




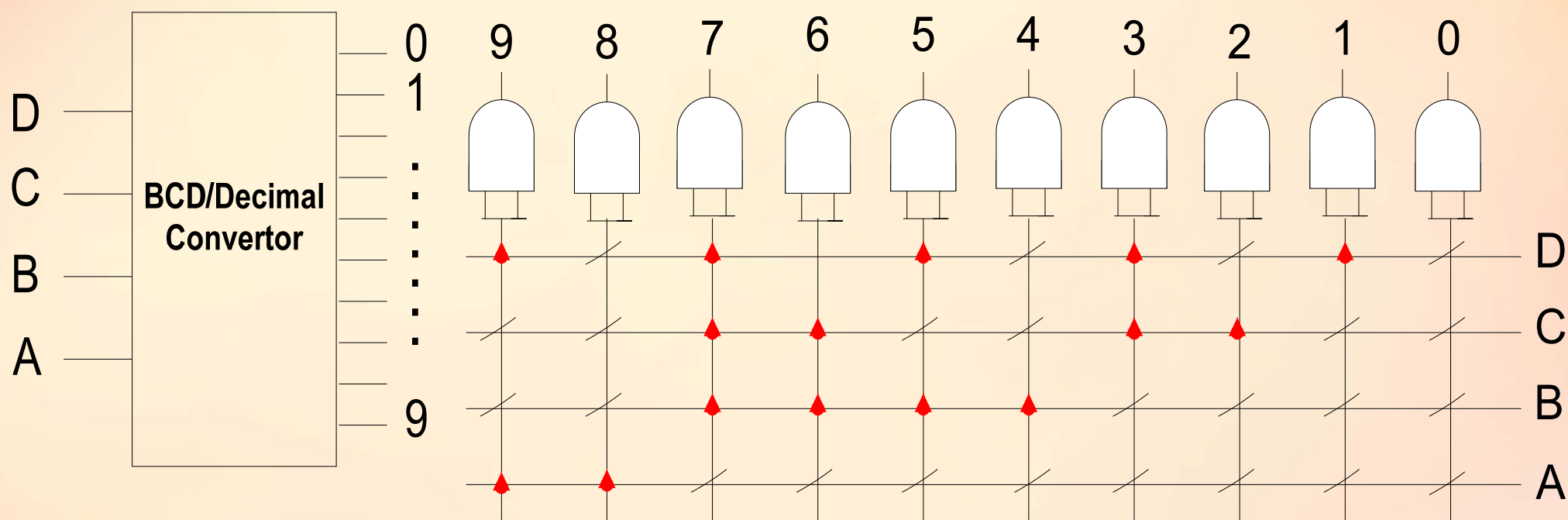
# BCD to Decimal Converter

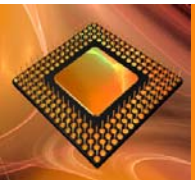




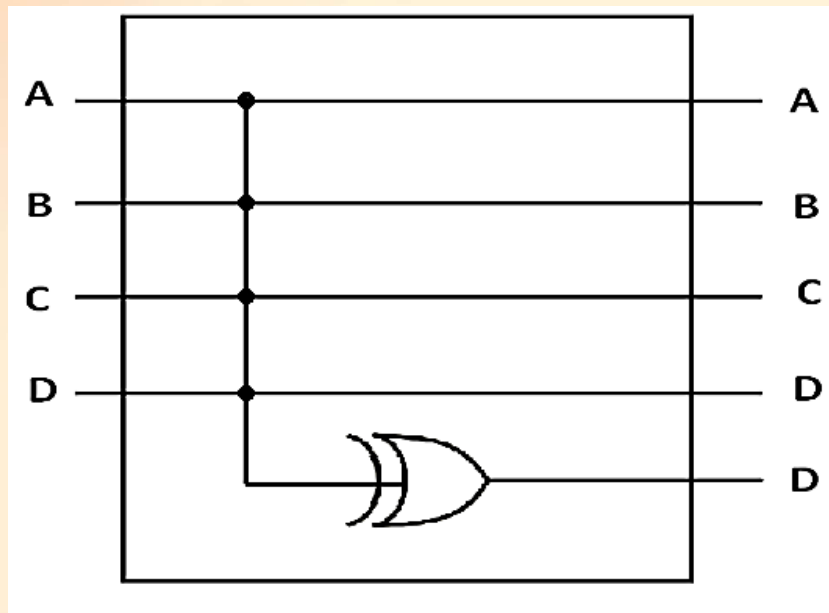


# BCD to Decimal Converter

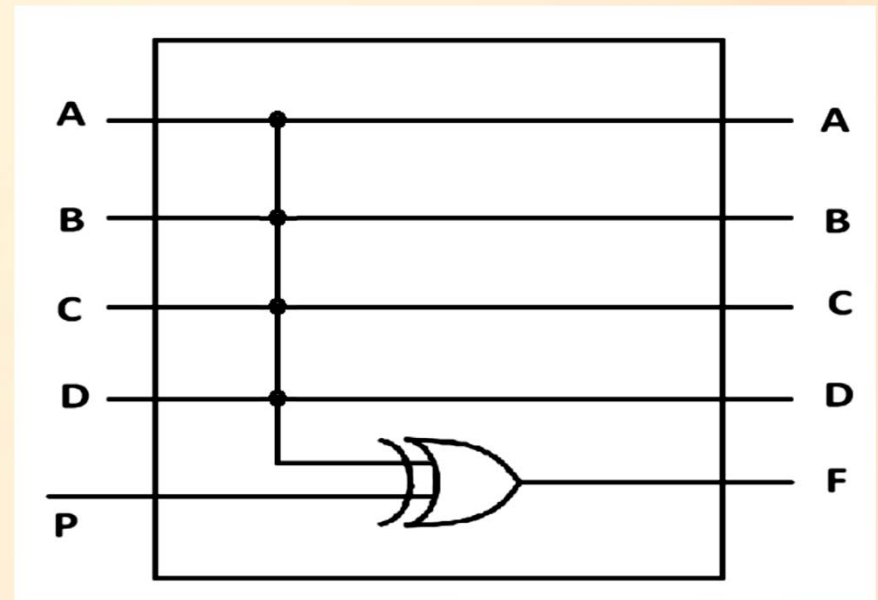




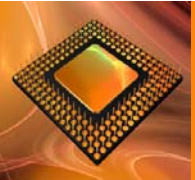
# Even Parity



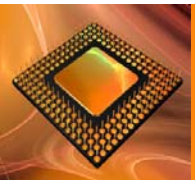
Source Circuit



Destination Circuit



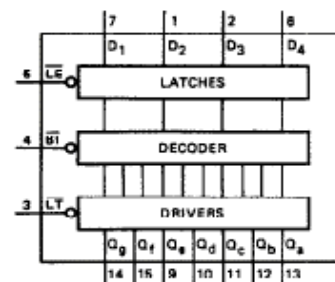
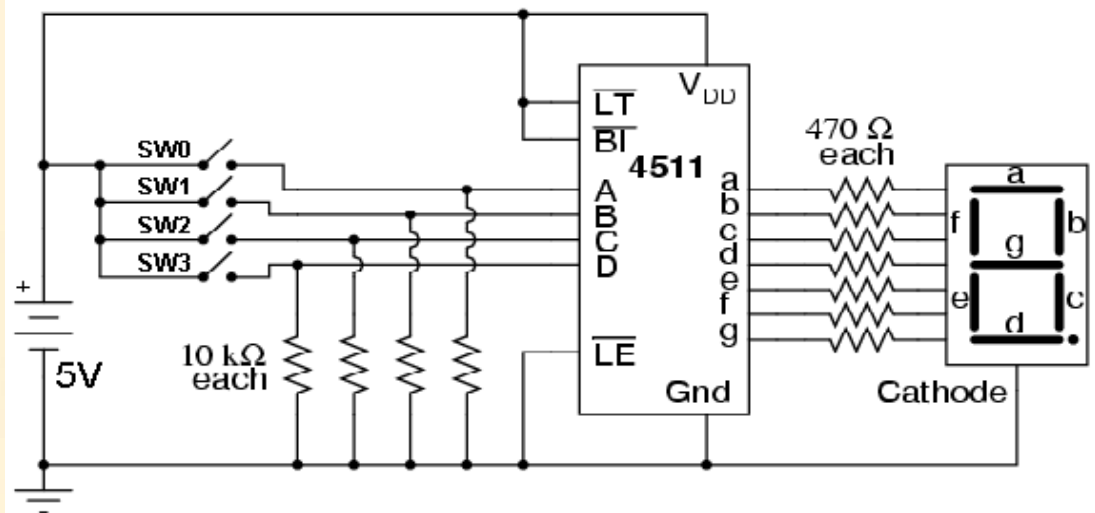
# ADD & SUB IN CODE



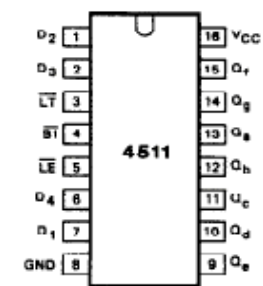
# Input and Output



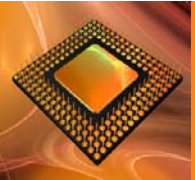
SCHEMATIC DIAGRAM



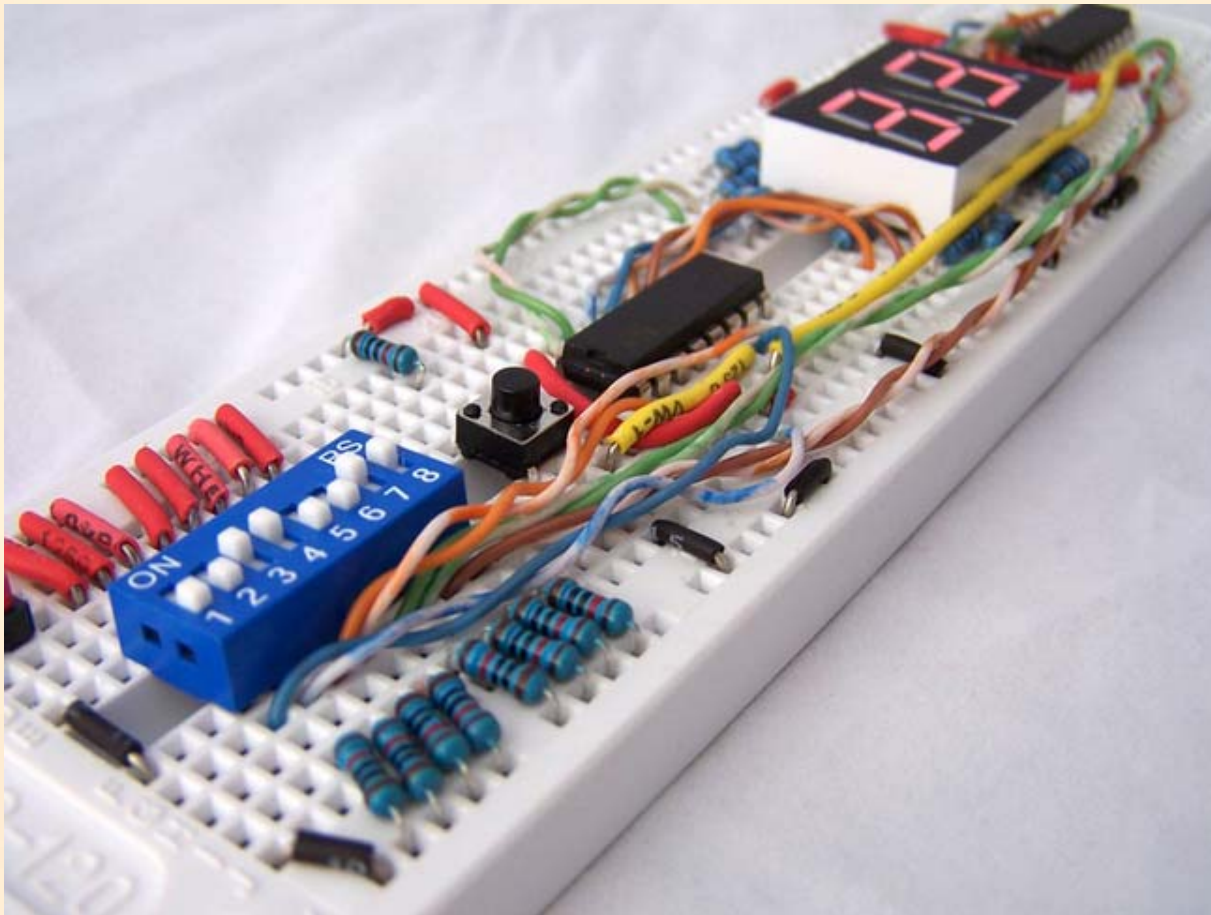
4511 Functional Diagram

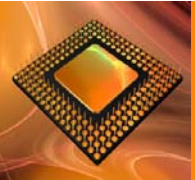


4511 Pinout



# Sample of Digital Circuit on the breadboard



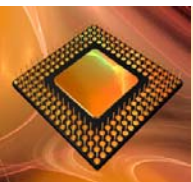


# Add in BCD code



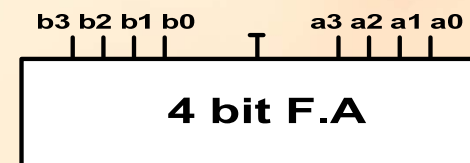
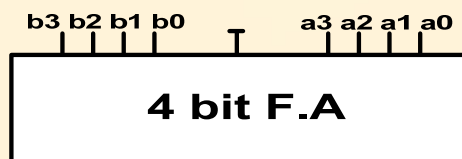
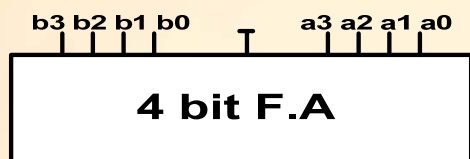
A+  
B

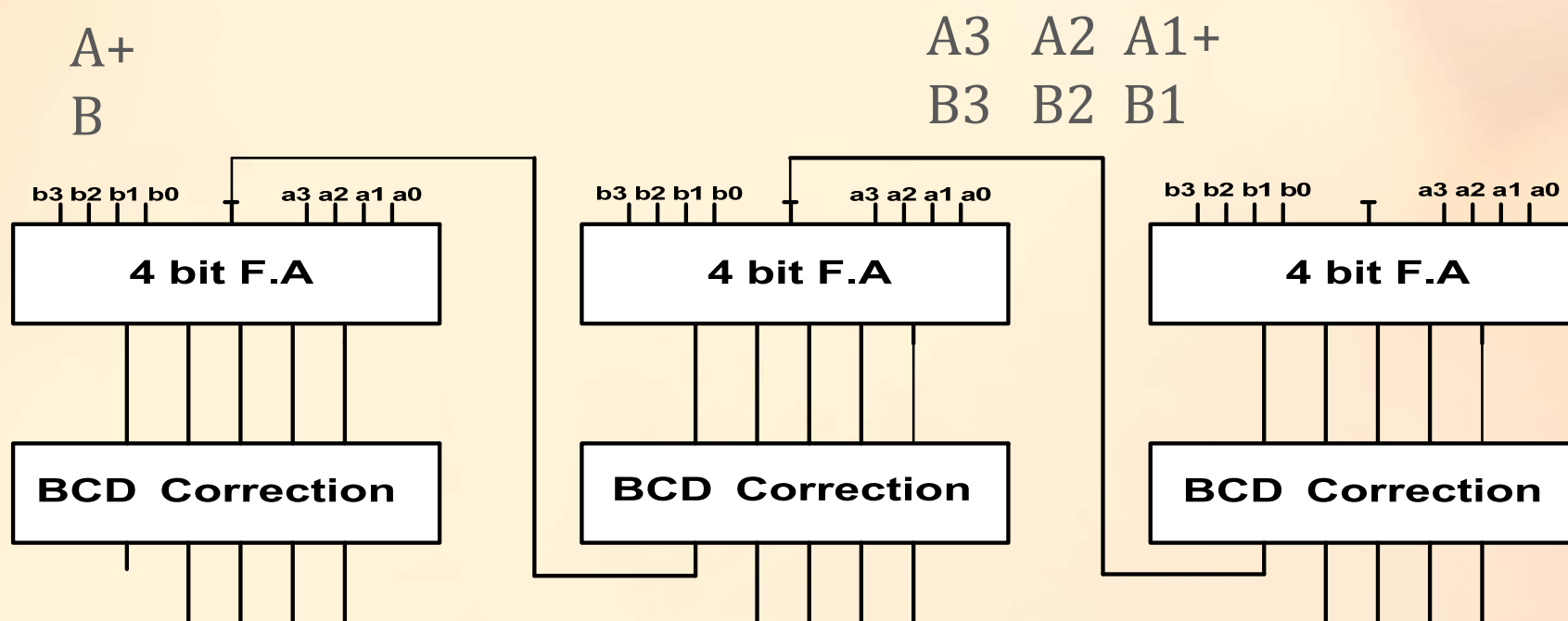
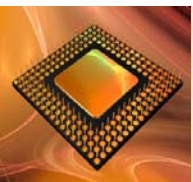
A3 A2 A1+  
B3 B2 B1



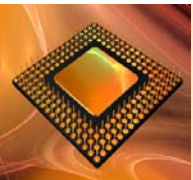
A+  
B

A3 A2 A1+  
B3 B2 B1



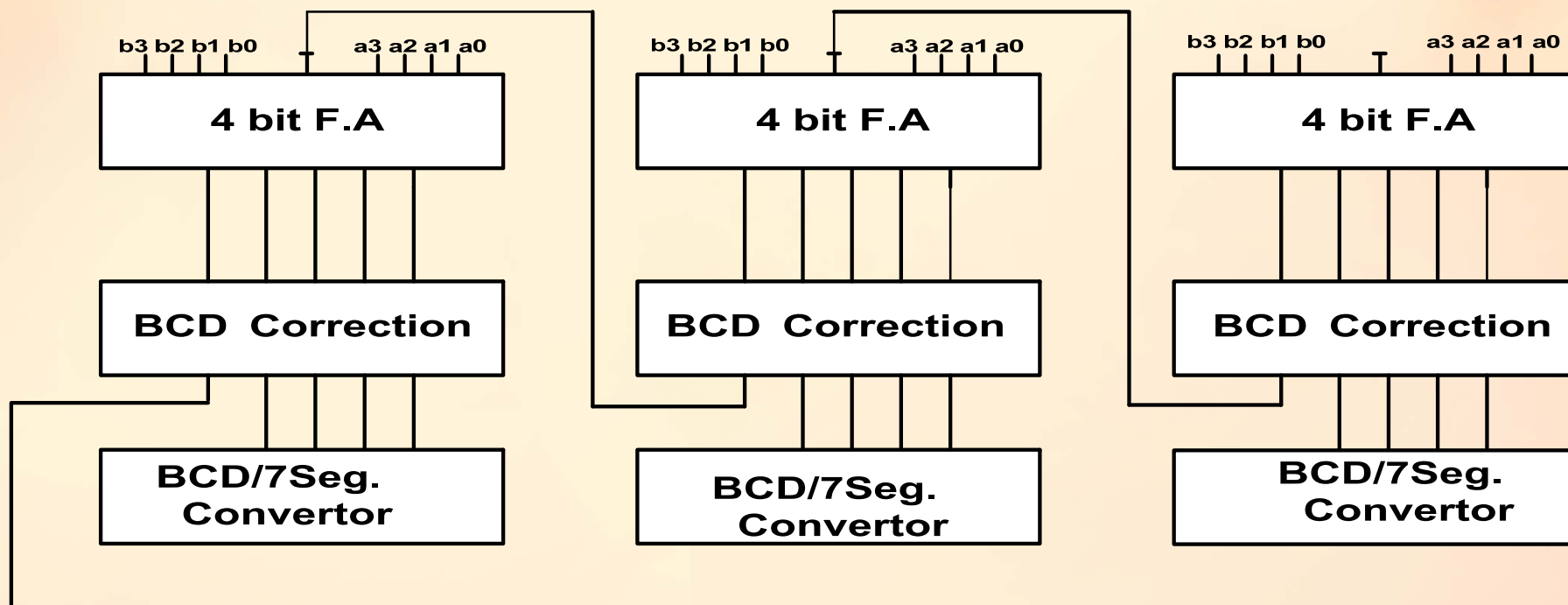


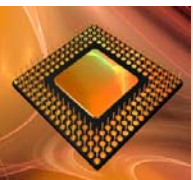




A+  
B

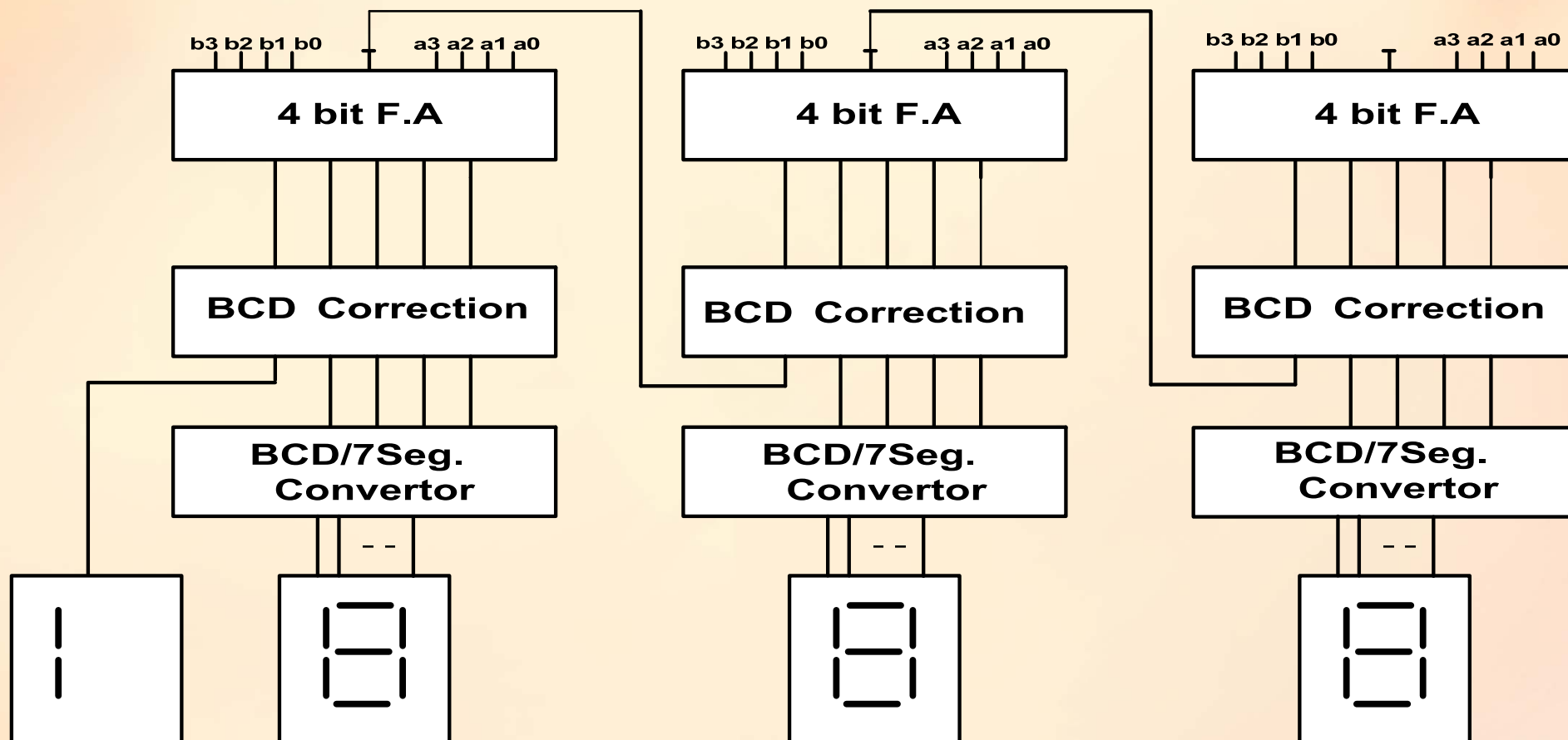
A3 A2 A1+  
B3 B2 B1

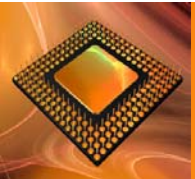




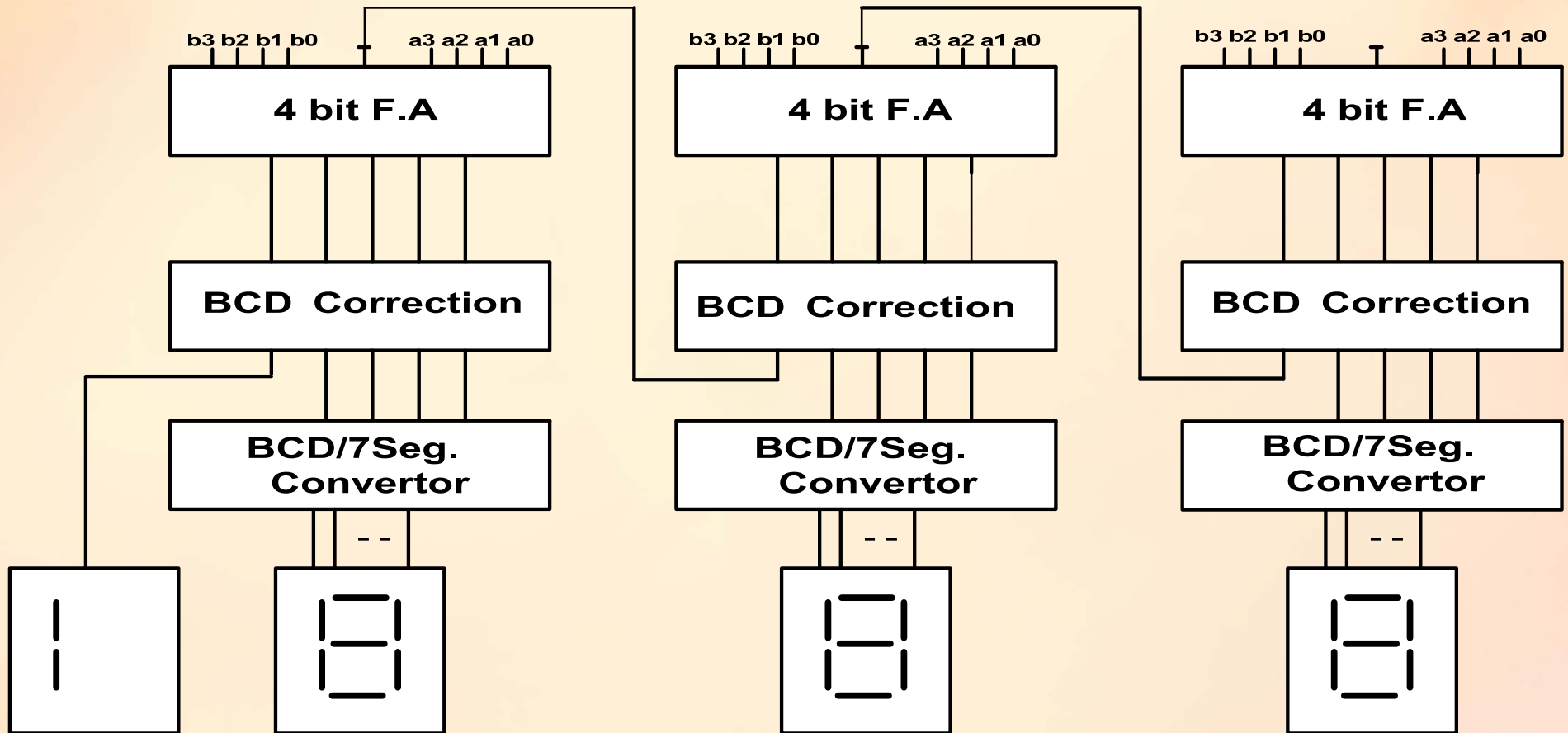
A+  
B

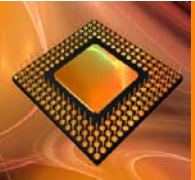
A3 A2 A1+  
B3 B2 B1



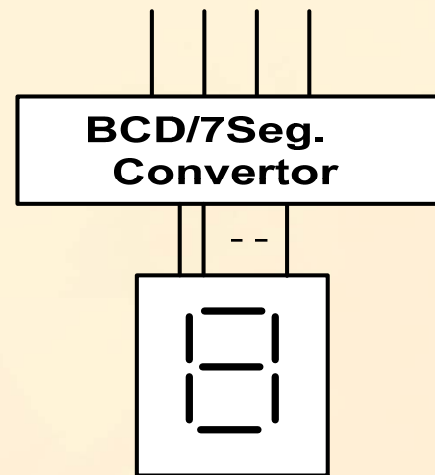


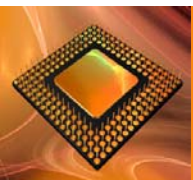
# Add in BCD code





# BCD/7Seg. Convertor

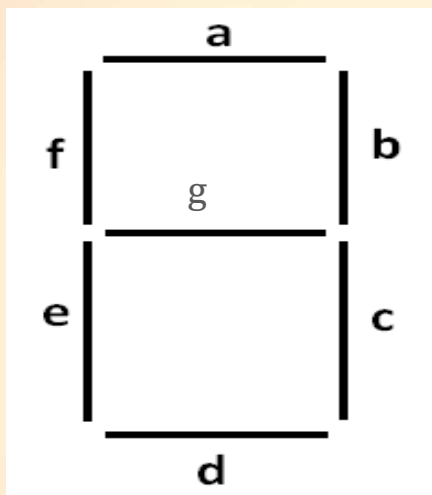




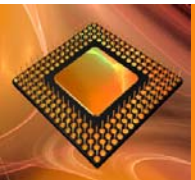
# 7 Seg.



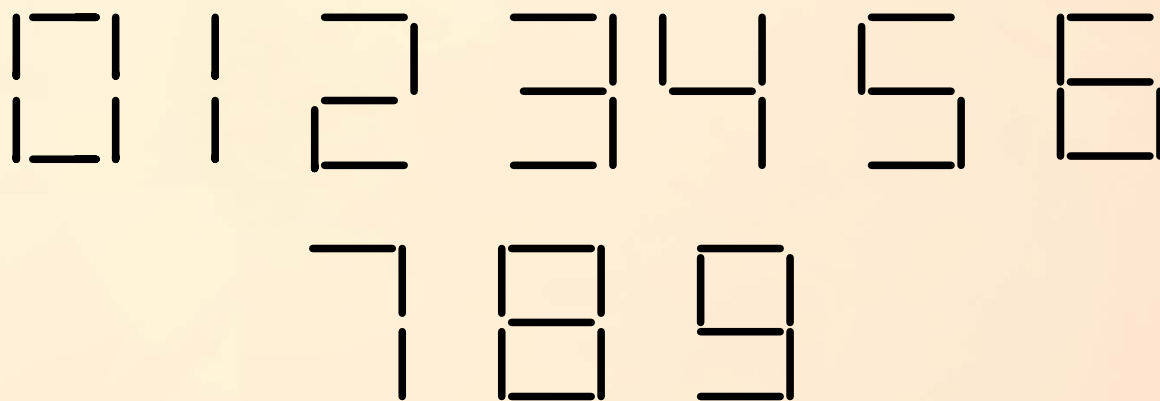
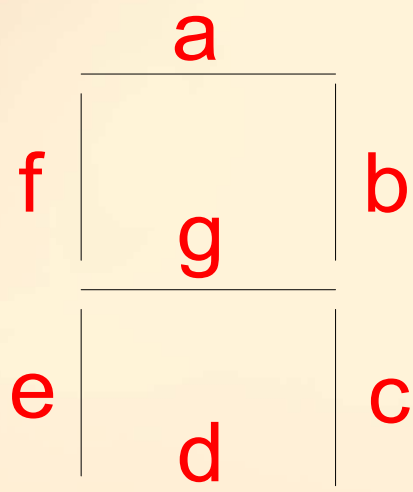
0 1 2 3 4 5 6 7 8 9

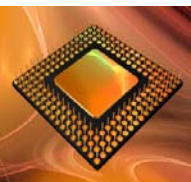


	A	B	C	D	g	f	e	d	c	b	a
0	0	0	0	0	0	1	1	1	1	1	1
1	0	0	0	1	0	0	0	0	1	1	0
2	0	0	1	0	1	0	1	1	0	1	1
3	0	0	1	1	1	0	0	1	1	1	1
4	0	1	0	0	1	1	0	0	1	1	0
5	0	1	0	1	1	1	0	1	1	0	1
6	0	1	1	0	1	1	1	1	1	0	1
7	0	1	1	1	0	0	0	0	1	1	1
8	1	0	0	0	1	1	1	1	1	1	1
9	1	0	0	1	1	1	0	1	1	1	1



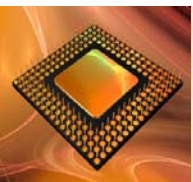
# Design of BCD/7Seg. Converter





0 1 2 3 4 5 6 7 8 9

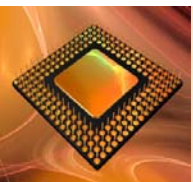
	A	B	C	D	a	b	c	d	e	f	g
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0 1 2 3 4 5 6 7 8 9

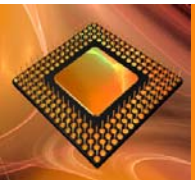
	A	B	C	D	a	b	c	d	e	f	g
0	0	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	0	1	1	0	1	1	0	1
3	0	0	1	1	1	1	1	1	0	0	1
4	0	1	0	0	0	1	1	0	0	1	1
5	0	1	0	1	1	0	1	1	0	1	1
6	0	1	1	0	1	0	1	1	1	1	1
7	0	1	1	1	1	1	1	0	0	0	0
8	1	0	0	0	1	1	1	1	1	1	1





0 1 2 3 4 5 6 7 8 9

	A	B	C	D	a	b	c	d	e	f	g
0	0	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	0	1	1	0	1	1	0	1
3	0	0	1	1	1	1	1	1	0	0	1
4	0	1	0	0	0	1	1	0	0	1	1
5	0	1	0	1	1	0	1	1	0	1	1
6	0	1	1	0	1	0	1	1	1	1	1
7	0	1	1	1	1	1	1	0	0	0	0
8	1	0	0	0	1	1	1	1	1	1	1
9	1	0	0	1	1	1	1	1	0	1	1

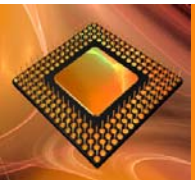


# Design of BCD/7Seg. Converter



$$a = \sum m(0, 2, 3, 5, 6, 7, 8, 9)$$

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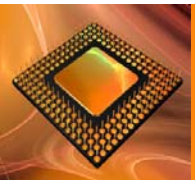


# Design of BCD/7Seg. Converter



$$a = \sum m(0, 2, 3, 5, 6, 7, 8, 9)$$

			D	
C	X	X	X	X
		X	X	A
			B	

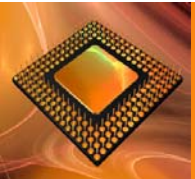


# Design of BCD/7Seg. Converter



$$a = \sum m(0, 2, 3, 5, 6, 7, 8, 9)$$

		D			
C		1	1	1	1
		X	X	X	X
		1	X	X	1
		1		1	
		B		A	

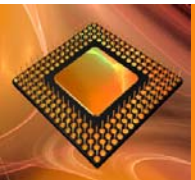


# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

				D
	1	1	1	1
C	X	X	X	X
	1	X	X	1
	1		1	
		B		
				A



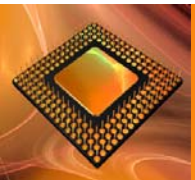
# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = \sum m(0,1,3,4,7,8,9)$$

			D	
C	X	X	X	X
		X	X	A
			B	



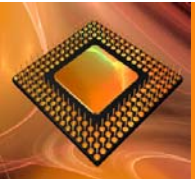
# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = \sum m(0,1,3,4,7,8,9)$$

		D		
		1	1	
C	X	X	X	A
	1	X	X	
	1	1		1
		B		



# Design of BCD/7Seg. Converter

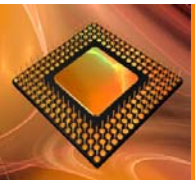


$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

			D	
			1	1
C	X	X	X	X
	1	X	X	1
	1	1		1
		B		





# Design of BCD/7Seg. Converter

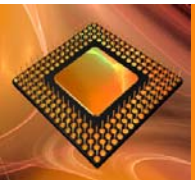


$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = \sum m(0, 1, 3, 4, 5, 6, 7, 8)$$

			D	
C	X	X	X	X
		X	X	
			B	
				A



# Design of BCD/7Seg. Converter

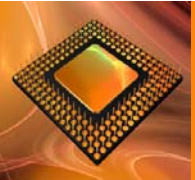


$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = \sum m(0, 1, 3, 4, 5, 6, 7, 8)$$

		D			
			1	1	1
C		X	X	X	X
		1	X	X	
		1	1	1	1
		B			
					A



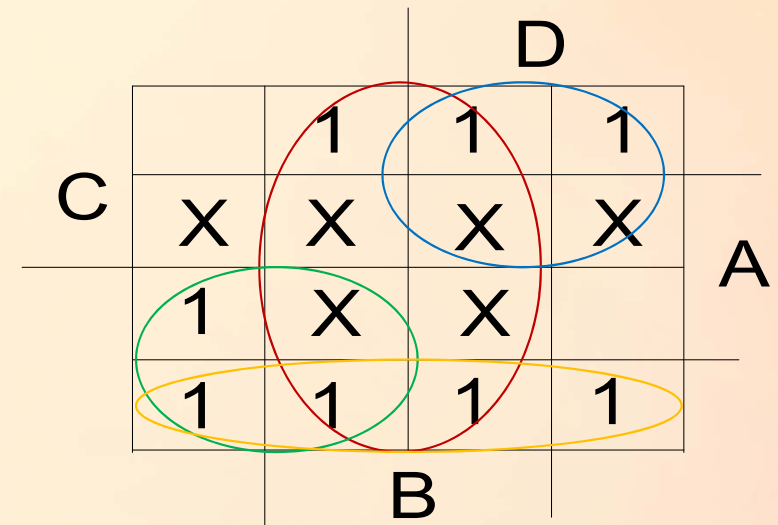
# Design of BCD/7Seg. Converter

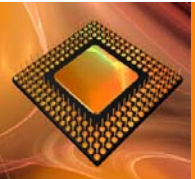


$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$





# Design of BCD/7Seg. Converter



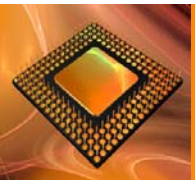
$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = \sum m(0, 2, 3, 5, 6, 8, 9)$$

			D	
C	X	X	X	X
		X	X	
			B	
				A



# Design of BCD/7Seg. Converter



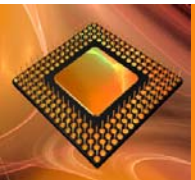
$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = \sum m(0, 2, 3, 5, 6, 8, 9)$$

		D			
		1	1		1
C		X	X	X	X
	A	1	X	X	1
		1		1	
		B			



# Design of BCD/7Seg. Converter



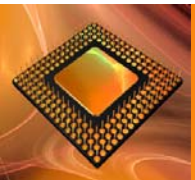
$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

			D	
	1	1		1
C	X	X	X	X
	1	X	X	1
	1		1	
		B		
				A



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

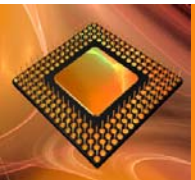
$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = \sum m(0, 2, 6, 8)$$

				D
C	X	X	X	X
		X	X	
				B
				A



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

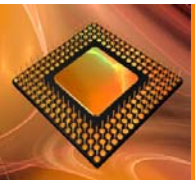
$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = \sum m(0, 2, 6, 8)$$

		D			
		1	1		
C		X	X	X	X
		1	X	X	
		1			
		B		A	





# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

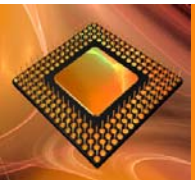
$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

		D		
C	B			
		1	1	
		X	X	X
		1	X	X
		1		



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

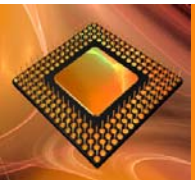
$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = \sum m(0, 4, 5, 6, 8, 9)$$

			D	
C	X	X	X	X
		X	X	A
			B	



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

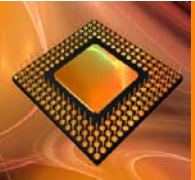
$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = \sum m(0, 4, 5, 6, 8, 9)$$

		D		
		1		
C	X	X	X	X
	1	X	X	1
	1	1	1	
		B		



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

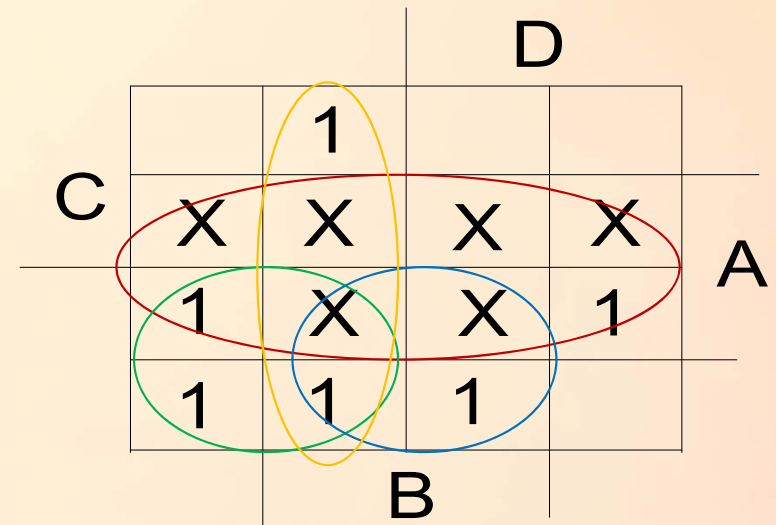
$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

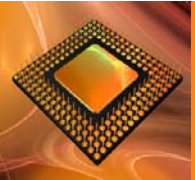
$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = A + B \overline{C} + \overline{C} \overline{D} + B \overline{D}$$





# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

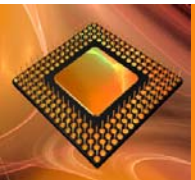
$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = A + B \overline{C} + \overline{C} \overline{D} + B \overline{D}$$

$$g = \sum m(2, 3, 4, 5, 6, 8)$$

			D	
C	X	X	X	X
		X	X	
			B	
				A



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

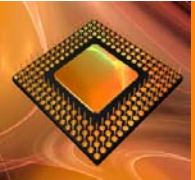
$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = A + B \overline{C} + \overline{C} \overline{D} + B \overline{D}$$

$$g = \sum m(2, 3, 4, 5, 6, 8)$$

			D	
	1	1		1
C	X	X	X	X
	1	X	X	
		1	1	
			B	
				A



# Design of BCD/7Seg. Converter



$$a = C + A + \overline{B} \overline{D} + BD$$

$$b = A + CD + \overline{C} \overline{D} + \overline{B} D$$

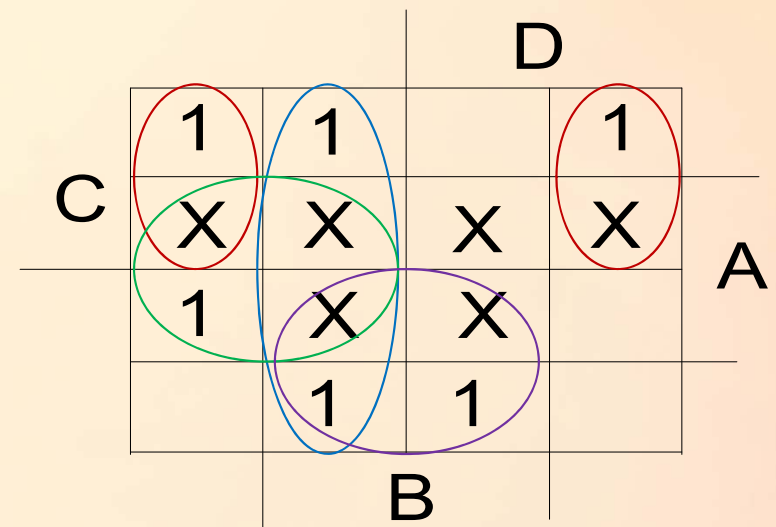
$$c = B + CD + \overline{C} \overline{D} + \overline{A} \overline{C}$$

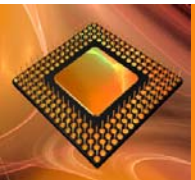
$$d = A + C \overline{D} + C \overline{B} + \overline{B} \overline{D} + B \overline{C} D$$

$$e = CD + \overline{B} \overline{D}$$

$$f = A + B \overline{C} + \overline{C} \overline{D} + B \overline{D}$$

$$g = C \overline{B} + B \overline{D} + A \overline{D} + B \overline{C}$$



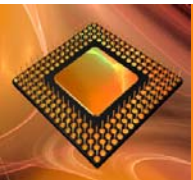


# Design of BCD/Aiken Convertor



	A	B	C	D	A'	B'	C'	D'
0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1
2	0	0	1	0	0	0	1	0
3	0	0	1	1	0	0	1	1
4	0	1	0	0	0	1	0	0
5	0	1	0	1	1	0	1	1
6	0	1	1	0	1	1	0	0
7	0	1	1	1	1	1	0	1

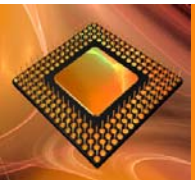




# Design of BCD/Aiken Convertor



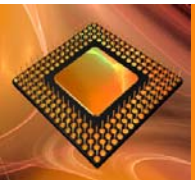
	A	B	C	D	A'	B'	C'	D'
0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1
2	0	0	1	0	0	0	1	0
3	0	0	1	1	0	0	1	1
4	0	1	0	0	0	1	0	0
5	0	1	0	1	1	0	1	1
6	0	1	1	0	1	1	0	0
7	0	1	1	1	1	1	0	1
8	1	0	0	0	1	1	1	0



# Design of BCD/Aiken Convertor



	A	B	C	D	A'	B'	C'	D'
0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1
2	0	0	1	0	0	0	1	0
3	0	0	1	1	0	0	1	1
4	0	1	0	0	0	1	0	0
5	0	1	0	1	1	0	1	1
6	0	1	1	0	1	1	0	0
7	0	1	1	1	1	1	0	1
8	1	0	0	0	1	1	1	0
9	1	0	0	1	1	1	1	1

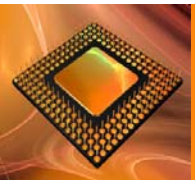


# Design of BCD/Aiken Converter



$$A' = \sum m(5, 6, 7, 8, 9)$$

			D	
C	X	X	X	X
		X	X	
			B	
				A

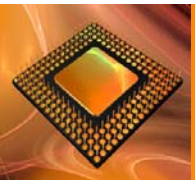


# Design of BCD/Aiken Converter



$$A' = \sum m(5, 6, 7, 8, 9)$$

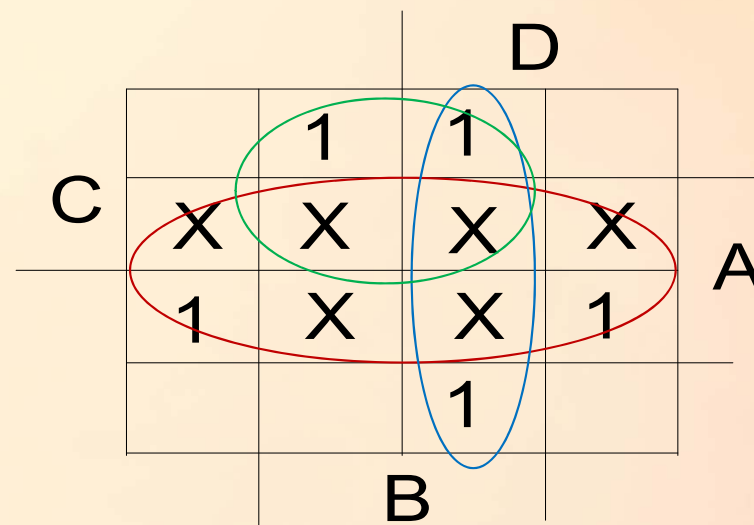
		D	
C		1	1
	X	X	X
	1	X	1
			1
		B	
		A	

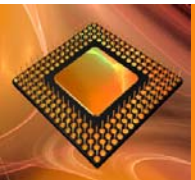


# Design of BCD/Aiken Converter



$$A' = A + CD + BD$$





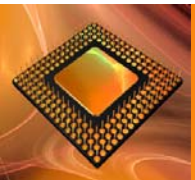
# Design of BCD/Aiken Convertor



$$A' = A + CD + BD$$

$$B' = \sum m(4, 6, 7, 8, 9)$$

			D	
C	X	X	X	X
		X	X	
			B	



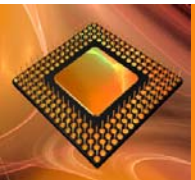
# Design of BCD/Aiken Converter



$$A' = A + CD + BD$$

$$B' = \sum m(4, 6, 7, 8, 9)$$

			D	
		1	1	
C	X	X	X	X
	1	X	X	1
		1		
		B		A

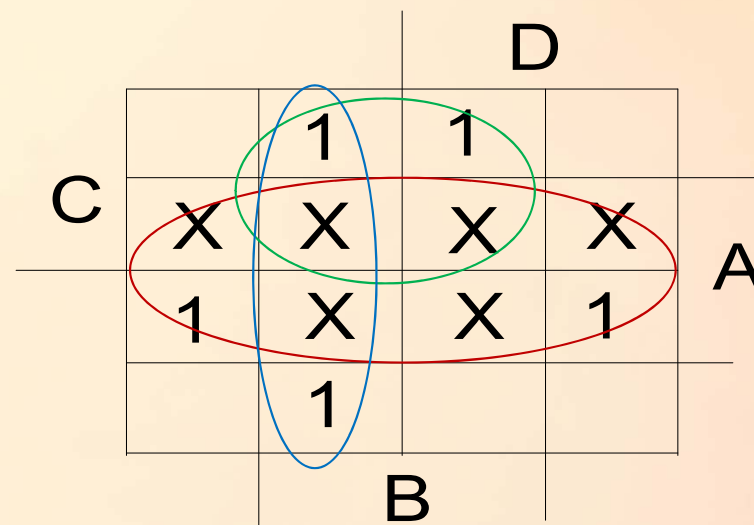


# Design of BCD/Aiken Converter

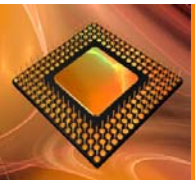


$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$







# Design of BCD/Aiken Converter

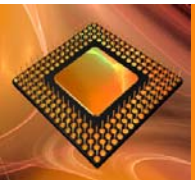


$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

$$C' = \sum m(2, 3, 5, 8, 9)$$

			D	
C	X	X	X	X
		X	X	
			B	
				A



# Design of BCD/Aiken Converter

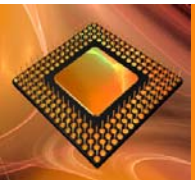


$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

$$C' = \sum m(2, 3, 5, 8, 9)$$

			D	
	1			1
C	X	X	X	X
	1	X	X	1
			1	
		B		
				A



# Design of BCD/Aiken Convertor

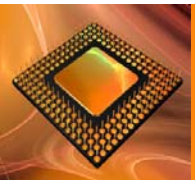


$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

$$C' = A + \overline{B}C + B\overline{C}D$$

			D	
	1			1
C	X	X	X	X
	1	X	X	1
			1	
		B		
				A



# Design of BCD/Aiken Converter



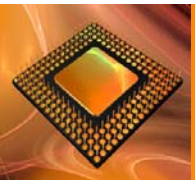
$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

$$C' = A + \overline{B}C + B\overline{C}D$$

$$D' = \sum m(1, 3, 5, 7, 9)$$

			D	
C	X	X	X	X
		X	X	
		B		
				A



# Design of BCD/Aiken Converter



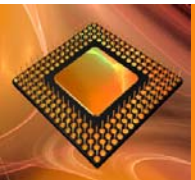
$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

$$C' = A + \overline{B}C + B\overline{C}D$$

$$D' = \sum m(1, 3, 5, 7, 9)$$

		D		
		1	1	
C	X	X	X	A
	X	X	1	
		1	1	B



# Design of BCD/Aiken Converter



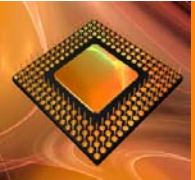
$$A' = A + CD + BD$$

$$B' = A + CD + B\overline{D}$$

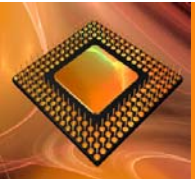
$$C' = A + \overline{B}C + B\overline{C}D$$

$$D' = D$$

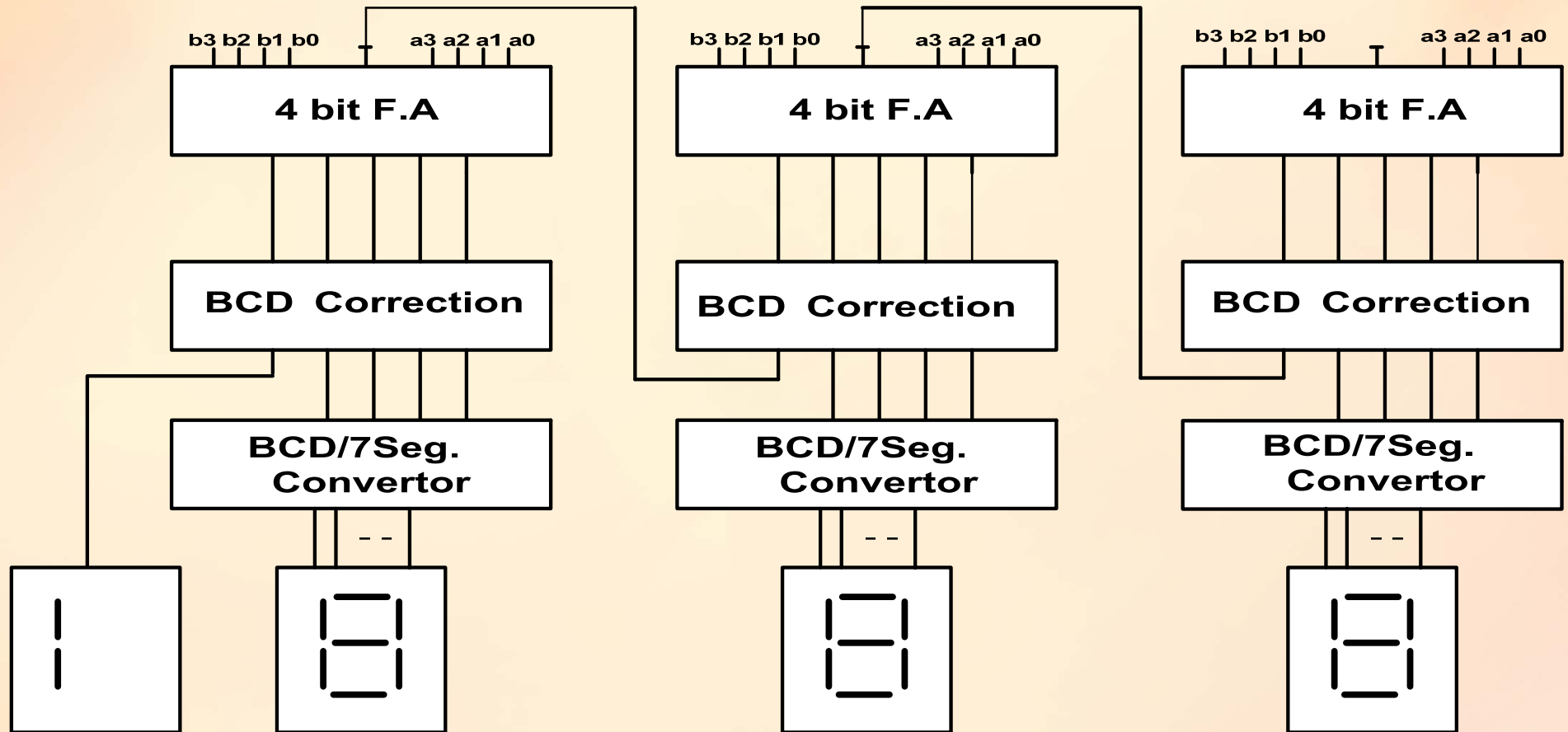
		D	
C			
		1	1
	X	X	X
		X	1
		1	1
		B	
		A	



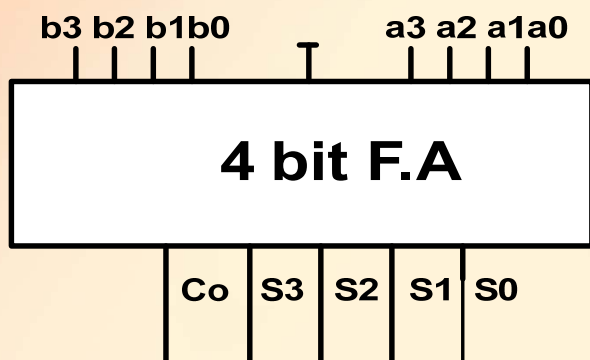
# BCD CORRECTION



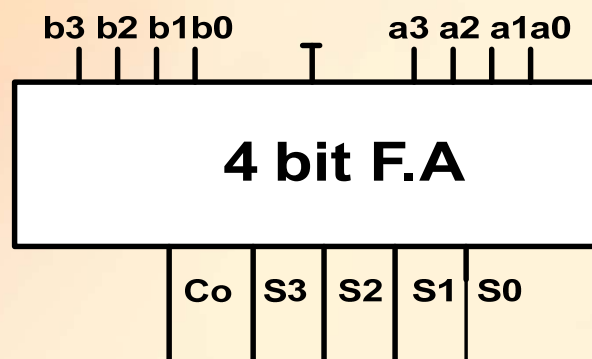
# Add in BCD code



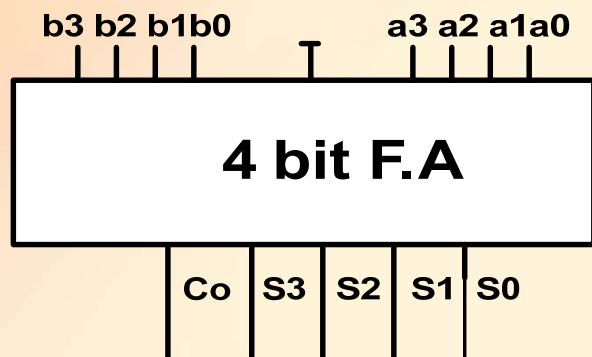




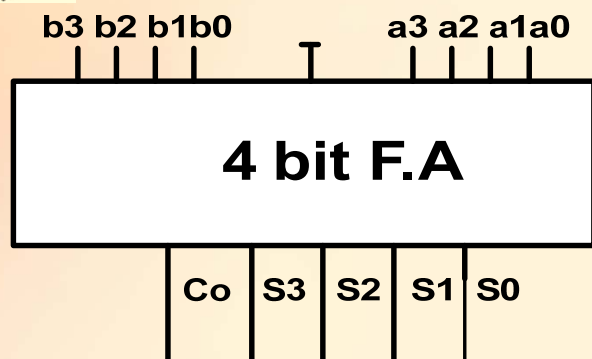
Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0						
0	0	0	0	1						
0	0	0	1	0						
0	0	0	1	1						
0	0	1	0	0						
0	0	1	0	1						
0	0	1	1	0						
0	0	1	1	1						
0	1	0	0	0						
0	1	0	0	1						
0	1	0	1	0						
0	1	0	1	1						
0	1	1	0	0						
0	1	1	0	1						
0	1	1	1	0						
0	1	1	1	1						
1	0	0	0	0						
1	0	0	0	1						
1	0	0	1	0						
1	0	0	1	1						



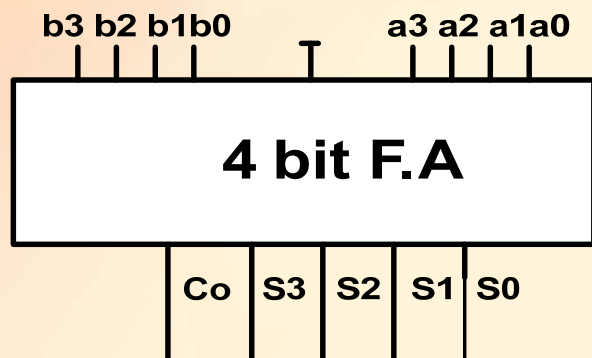
Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0		0	0	0	0	0
0	0	0	0	1		0	0	0	0	1
0	0	0	1	0		0	0	0	1	0
0	0	0	1	1		0	0	0	1	1
0	0	1	0	0		0	0	1	0	0
0	0	1	0	1		0	0	1	0	1
0	0	1	1	0		0	0	1	1	0
0	0	1	1	1		0	0	1	1	1
0	1	0	0	0		0	1	0	0	0
0	1	0	0	1		0	1	0	0	1
0	1	0	1	0		1	0	0	0	0
0	1	0	1	1		1	0	0	0	1
0	1	1	0	0		1	0	0	1	0
0	1	1	0	1		1	0	0	1	1
0	1	1	1	0		1	0	1	0	0
0	1	1	1	1		1	0	1	0	1
1	0	0	0	0		1	0	1	1	0
1	0	0	0	1		1	0	1	1	1
1	0	0	1	0		1	1	0	0	0
1	0	0	1	1		1	1	0	0	1



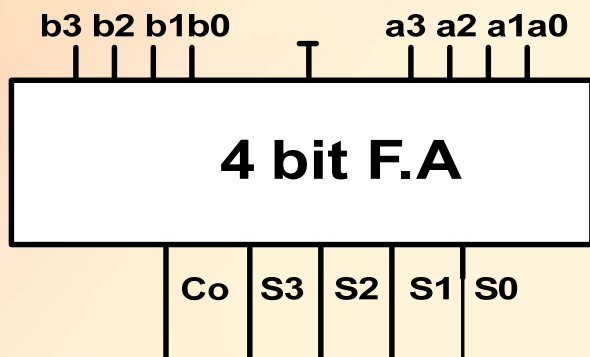
Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0		0	0	0	0	0
0	0	0	0	1		0	0	0	0	1
0	0	0	1	0		0	0	0	1	0
0	0	0	1	1		0	0	0	1	1
0	0	1	0	0		0	0	1	0	0
0	0	1	0	1		0	0	1	0	1
0	0	1	1	0		0	0	1	1	0
0	0	1	1	1		0	0	1	1	1
0	1	0	0	0		0	1	0	0	0
0	1	0	0	1		0	1	0	0	1
0	1	0	1	0		1	0	0	0	0
0	1	0	1	1		1	0	0	0	1
0	1	1	0	0		1	0	0	1	0
0	1	1	0	1		1	0	0	1	1
0	1	1	1	0		1	0	1	0	0
0	1	1	1	1		1	0	1	0	1
1	0	0	0	0		1	0	1	1	0
1	0	0	0	1		1	0	1	1	1
1	0	0	1	0		1	1	0	0	0
1	0	0	1	1		1	1	0	0	1



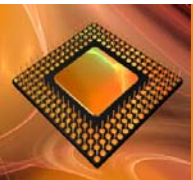
Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0		0	0	0	0	0
0	0	0	0	1		0	0	0	0	1
0	0	0	1	0		0	0	0	1	0
0	0	0	1	1		0	0	0	1	1
0	0	1	0	0		0	0	1	0	0
0	0	1	0	1		0	0	1	0	1
0	0	1	1	0		0	0	1	1	0
0	0	1	1	1		0	0	1	1	1
0	1	0	0	0		0	1	0	0	0
0	1	0	0	1		0	1	0	0	1
0	1	0	1	0		1	0	0	0	0
0	1	0	1	1		1	0	0	0	1
0	1	1	0	0		1	0	0	1	0
0	1	1	0	1		1	0	0	1	1
0	1	1	1	0		1	0	1	0	0
0	1	1	1	1		1	0	1	0	1
1	0	0	0	0		1	0	1	1	0
1	0	0	0	1		1	0	1	1	1
1	0	0	1	0		1	1	0	0	0
1	0	0	1	1		1	1	0	0	1



Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0		0	0	0	0	0
0	0	0	0	1		0	0	0	0	1
0	0	0	1	0		0	0	0	1	0
0	0	0	1	1		0	0	0	1	1
0	0	1	0	0		0	0	1	0	0
0	0	1	0	1		0	0	1	0	1
0	0	1	1	0		0	0	1	1	0
0	0	1	1	1		0	0	1	1	1
0	1	0	0	0		0	1	0	0	0
0	1	0	0	1		0	1	0	0	1
0	1	0	1	0		1	0	0	0	0
0	1	0	1	1		1	0	0	0	1
0	1	1	0	0		1	0	0	1	0
0	1	1	0	1		1	0	0	1	1
0	1	1	1	0		1	0	1	0	0
0	1	1	1	1		1	0	1	0	1
1	0	0	0	0		1	0	1	1	0
1	0	0	0	1		1	0	1	1	1
1	0	0	1	0		1	1	0	0	0
1	0	0	1	1		1	1	0	0	1



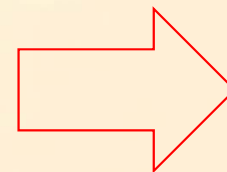
Co	S3	S2	S1	S0		Co'	S3'	S2'	S1'	S0'
0	0	0	0	0		0	0	0	0	0
0	0	0	0	1		0	0	0	0	1
0	0	0	1	0		0	0	0	1	0
0	0	0	1	1		0	0	0	1	1
0	0	1	0	0		0	0	1	0	0
0	0	1	0	1		0	0	1	0	1
0	0	1	1	0		0	0	1	1	0
0	0	1	1	1		0	0	1	1	1
0	1	0	0	0		0	1	0	0	0
0	1	0	0	1		0	1	0	0	1
0	1	0	1	0		1	0	0	0	0
0	1	0	1	1		1	0	0	0	1
0	1	1	0	0		1	0	0	1	0
0	1	1	0	1		1	0	0	1	1
0	1	1	1	0		1	0	1	0	0
0	1	1	1	1		1	0	1	0	1
1	0	0	0	0		1	0	1	1	0
1	0	0	0	1		1	0	1	1	1
1	0	0	1	0		1	1	0	0	0
1	0	0	1	1		1	1	0	0	1



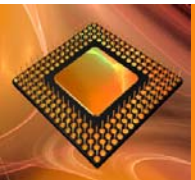
# Add in BCD code



C0	S3	S2	S1	S0
0	0	0	0	0
0	0	0	0	1
0	0	0	1	0
0	0	0	1	1
0	0	1	0	0
0	0	1	0	1
0	0	1	1	0
0	0	1	1	1
0	1	0	0	0
0	1	0	0	1



Without correction



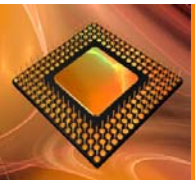
# Add in BCD code



↑  
correction  
area  
↓

C0	S3	S2	S1	S0
0	1	0	1	0
0	1	0	1	1
0	1	1	0	0
0	1	1	0	1
0	1	1	1	0
0	1	1	1	1
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1



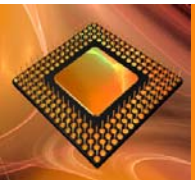


# Add in BCD code

$$F = S3.S1$$

↑  
correction  
area  
↓

Co	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
0	1	0	1	0
0	1	0	1	1
0	1	1	0	0
0	1	1	0	1
0	1	1	1	0
0	1	1	1	1
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1



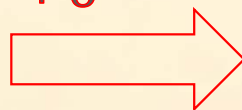
# Add in BCD code

$$F = S3.S1$$

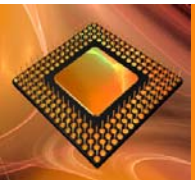
↑  
correction  
area  
↓

C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
0	1	0	1	0
0	1	0	1	1
0	1	1	0	0
0	1	1	0	1
0	1	1	1	0
0	1	1	1	1
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1

+ 6



C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
1	0	0	0	0
1	0	0	0	1



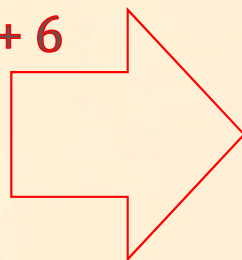
# Add in BCD code

$$F = S_3.S_1 + S_3.S_2$$

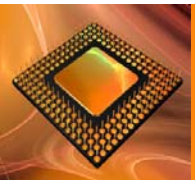
↑  
correction  
area  
↓

C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
0	1	0	1	0
0	1	0	1	1
0	1	1	0	0
0	1	1	0	1
0	1	1	1	0
0	1	1	1	1
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1

+ 6



C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1
1	0	1	0	0
1	0	1	0	1



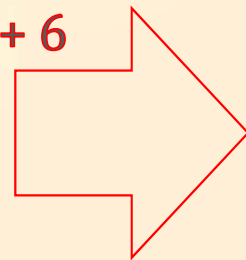
# Add in BCD code

$$F = S_3.S_1 + S_3.S_2 + C_0$$

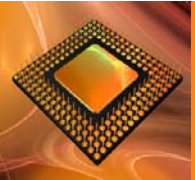
↑  
correction  
area  
↓

C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
0	1	0	1	0
0	1	0	1	1
0	1	1	0	0
0	1	1	0	1
0	1	1	1	0
0	1	1	1	1
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1

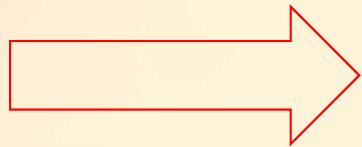
+ 6



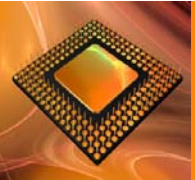
C <sub>0</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>
1	0	0	0	0
1	0	0	0	1
1	0	0	1	0
1	0	0	1	1
1	0	1	0	0
1	0	1	0	1
1	0	1	1	0
1	0	1	1	1
1	1	0	0	0
1	1	0	0	1



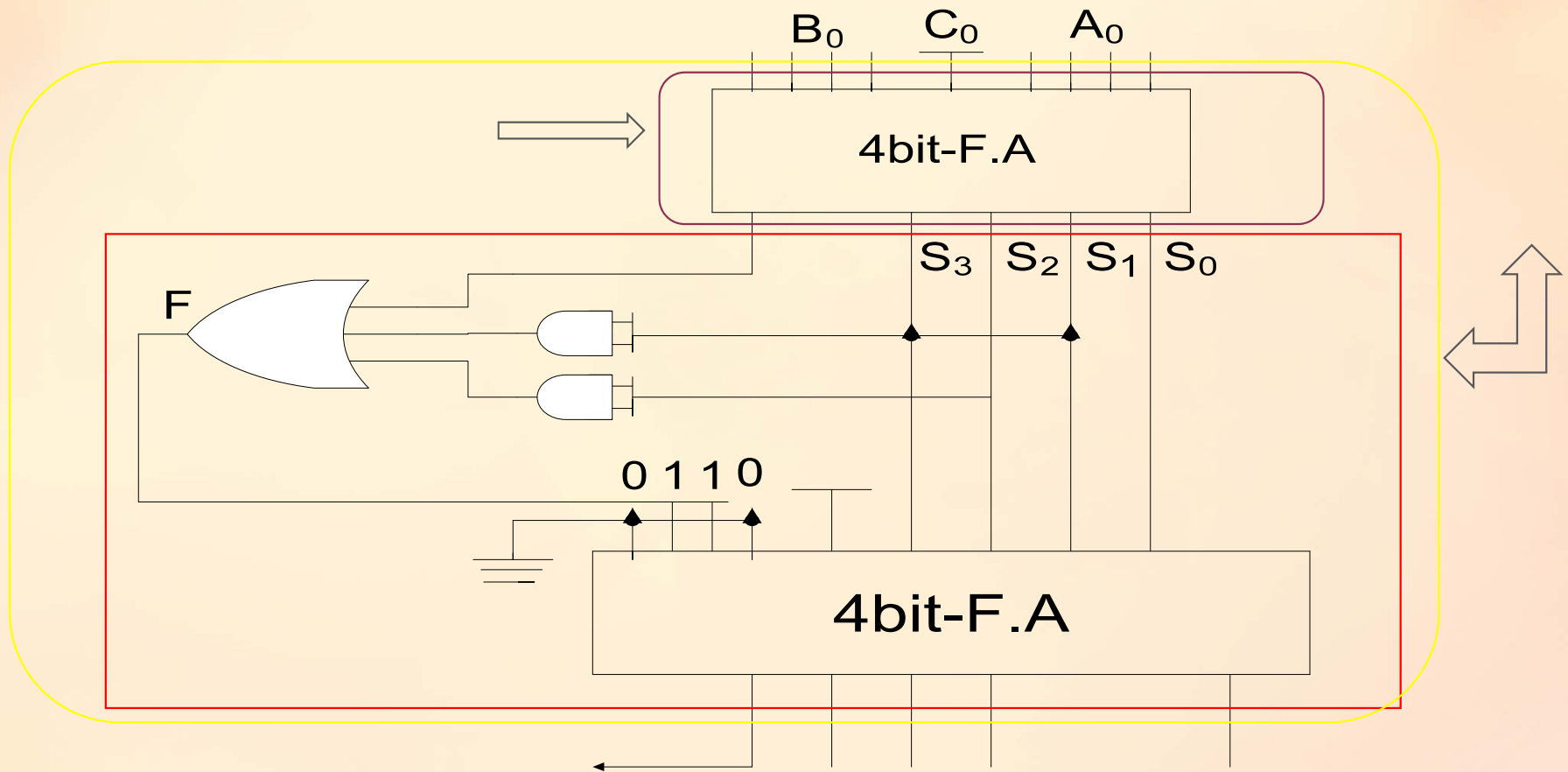
# Add in BCD code



$$F = S3.S1 + S3.S2 + Co$$



# Add in BCD code





**Thank You**