Exam 2 topics

Combinational logic circuit design

1. Designing combinational logic circuit
2. Technology mapping
3. Decoding
4. Decoder and Enabling Combinations
5. Decoder based Combinational Circuit
6. Encoding
7. Priority Encoder
8. Multiplexer
9. Multiplexer based Combinational Circuit

Arithmetic Functions

1. Binary Adder
2. Half Adder
3. Full Adder
4. Binary ripple carry Adder
5. Binary Subtraction
6. Complements
7. Binary Subtraction with 2's compliment
8. Signed binary number
9. Addition and subtraction of Signed binary numbers
10. Overflow
11. Other arithmetic Functions