Software System Verification and Validation 2015-2016 Laboratory Assignment 01 www.cs.ubbcluj.ro/~avescan

Unit Design Phase Defects Checklist

Source: http://www.softwaretestinggenius.com/unit-design-phase-defects-checklist

Nr	Check Point / Defect Statement	Check Mark (√) the Appropriate Column	
		Yes	N/A
01	Is the if-then-else construct used incorrectly?		
02	Is the dowhile construct used incorrectly?		
03	Are there infinite loops?		
04	Is the program readable?		
05	Is the program efficient?		
06	Is the algorithm expression too complicated?		
07	Is the nesting too deep?		
08	Is there any negative Boolean logic?		
09	Are there any compounded Boolean expressions?		
10	Is there any jumping in and out of loops?		