Software System Verification and Validation 2015-2016 Laboratory Assignment 01 www.cs.ubbcluj.ro/~avescan

Coding Phase Defects Checklist

Source: http://www.softwaretestinggenius.com/unit-design-phase-defects-checklist

Nr	Check Point / Defect Statement	Check Mark (√) the Appropriate Column	
		Yes	N/A
01	Decision logic is erroneous or inadequate.		
02	Branching is erroneous.		
03	There are undefined loop terminations.		
04	I/O format errors exist.		
05	Subprogram invocations are violated.		
06	There are errors in preparing or processing input data.		
07	Output processing errors exist.		
80	Error message processing errors exist.		
09	There is confusion in the use of parameters.		
10	There are errors in loop counters.		
11	Errors are made in writing out variable names.		
12	Variable type and dimensions are incorrectly declared.		