Flicker Noise Corner Extraction

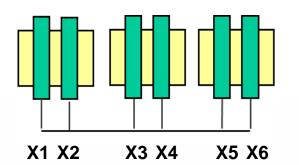
LVS 2018/01/30

"TOTAL" Parameter for Logic MOS



- Turn on "FLICKER_CORNER_EXTRACTION" switch will enable "total" instance parameter extraction for logic MOS. It is used to handle flicker noise corner of multiple devices connected in parallel.
- total: number of same devices of the same size connected in parallel
 - L/nfin must be the same (grouping for same device/bias/size)

	Device	NFIN	L	Parallel	OD
total	0	0	0	0	*
O: need to be the same					



*: don't care

LPE netlist example

- X1 d g s b nch_svt_mac nfin=2 l=8n total=6
- X2 d g s b nch_svt_mac nfin=2 l=8n total=6
- X3 d g s b nch_svt_mac nfin=2 l=8n total=6
- X4 d g s b nch_svt_mac nfin=2 l=8n total=6
- X5 d g s b nch svt mac nfin=2 l=8n total=6
- X6 d g s b nch_svt_mac nfin=2 l=8n total=6