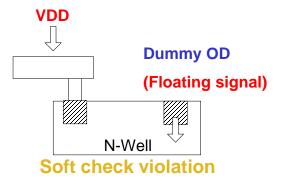
### **N3 Dummy Pickup Checking**

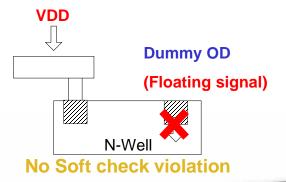
LVSRCE/DMKD TSMC

### Filter Dummy OD in Soft connect Check

- Soft connect check violation in Dummy Cell's OD
  - Reason : Dummy Cell's OD are floating

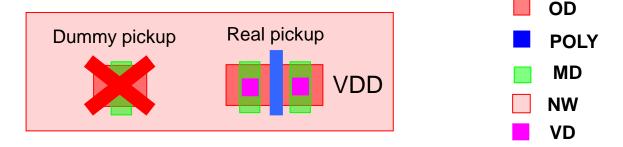


- Avoid soft connect check violation in dummy section
  - Remove dummy OD to WELL connection in LVS flow



### **Dummy Pickup for Soft-connect Check**

- Dummy pickup means that the OD is not able to connect out ( OD's MD w/o VD/VDR/MD\_text )
- To avoid soft-connect false error, the dummy pickup is ignored during LVS stage



LPE extraction includes all pickups (real pickup and dummy pickup) connection

# Security C –

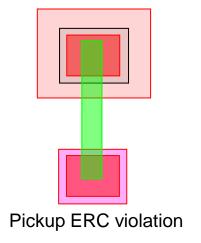
### **Dummy Pickup checking(1/4)**

- Purpose
  - Check Dummy pickups are connected by separated WELL in LVS
    Stage
- Three kinds of Pickup Checking
  - N-pickup to P-pickup ERC checking
  - N-pickup to N-pickup ERC checking
  - P-pickup to P-pickup ERC checking

### Security C – TSMC Secret

### **Dummy Pickup checking(2/4)**

- N-pickup to P-pickup ERC checking
  - Highlight MD condition
    - MD w/o ( VD/VDR/MD\_text ) interacts N-pickup and P-pickup
  - Example



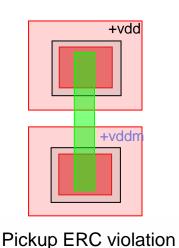


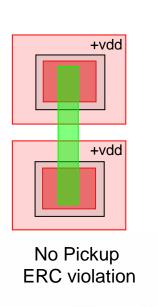
## Security C – TSMC Secret

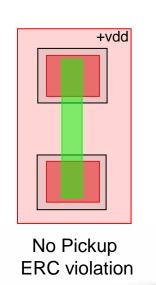
### **Dummy Pickup checking(3/4)**

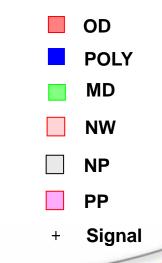
- N-pickup to N-pickup ERC checking
  - Highlight MD with two conditions
    - MD w/o ( VD/VDR/MD\_text ) interacts more than one N-pickup which is different OD region
    - ◆ Crossover NWELLs which has different node information

#### **■** Example







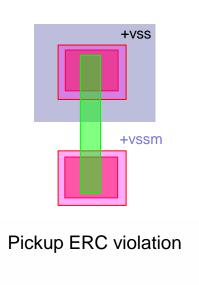


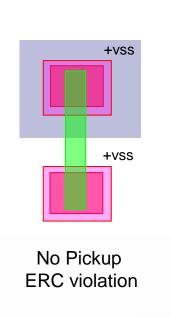
## Security C – TSMC Secret

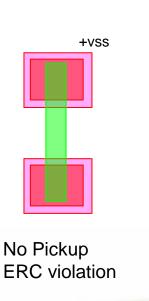
### **Dummy Pickup checking(4/4)**

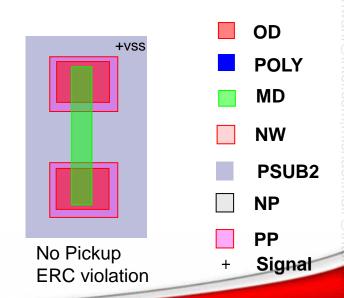
- P-pickup to P-pickup ERC checking
  - Highlight MD with two conditions
    - MD w/o ( VD/VDR/MD\_text ) interacts more than one P-pickup which is different OD region
    - ♦ Crossover PWELLs which has different node information

#### **■** Example









### Dummy Pickup checking(5/5)



TSMC Secret

- P-pickup to P-pickup ERC checking with respect to PSUB2
  - LVS waive violations caused by any kinds of bridging crossing PSUB2(111;0). The bridging is formed by SR\_DOD(6;7) and SR\_DMD(82;170)
  - Example

