



Taiwan Semiconductor Manufacturing Co., LTD

Ver	Eff_Date	ECN_No.	Author	Change Description
1.0_3A	05-07-22	E120202216054	Yao-Jen Chuang (DRC)	Please refer to 4_ReleaseNotes.txt
1.0_1A	12-15-21	E120202146289	Jui-Ping Chiang (DRC)	Please refer to 4_ReleaseNotes.txt
1.0A	09-28-21	E120202139067	Jui-Ping Chiang (DRC)	Please refer to 4_ReleaseNotes.txt
0.9_1A	06-21-21	E120202123138	Jui-Ping Chiang (DRC)	Please refer to 4_ReleaseNotes.txt
0.5_1A	12-29-20	E120202045227	Yao-Jen Chuang (DRC)	First Release
Document No. : T-N03-CL-DR-007-C7				Title: TSMC 3NM CMOS LOGIC ESD LUP CHECKER COMMAND FILE (CALIBRE) (FOR U015 ONLY)
Technology				
3 NM CMOS LOGIC				
ESD LUP CHECKER COMMAND FILE (CALIBRE)				
File: ESD_LUP_Checker_Calibre_3nm_007_V10_3a.tar.gz Size: 47,145,401 (bytes)				