

# THERE'S PLENTY OF ROOM AT THE BOTTOM: INCREMENTAL COMBINATIONS OF SIGN-ERROR LMS FILTERS

Luiz F. O. Chamon

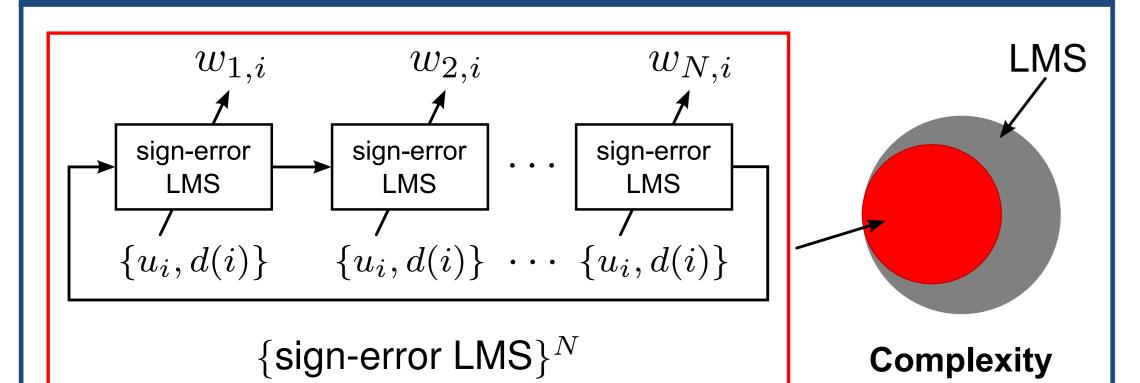
chamon@usp.br

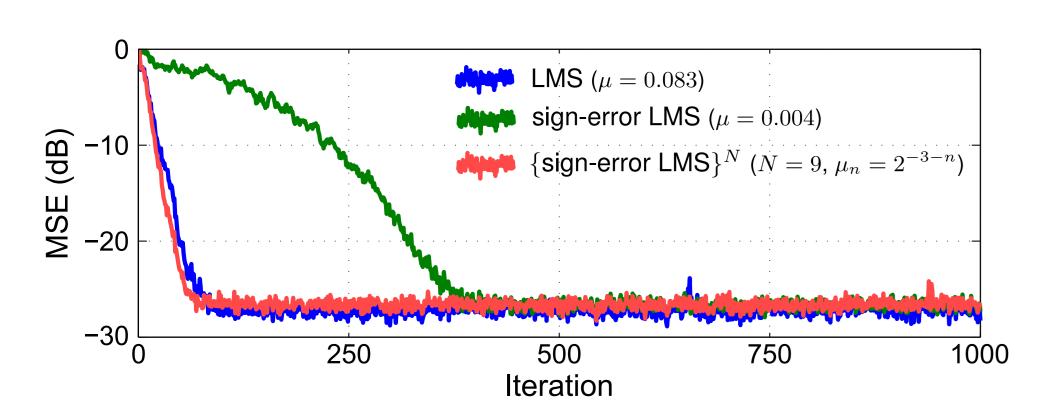
cassio@lps.usp.br

## CONTRIBUTIONS

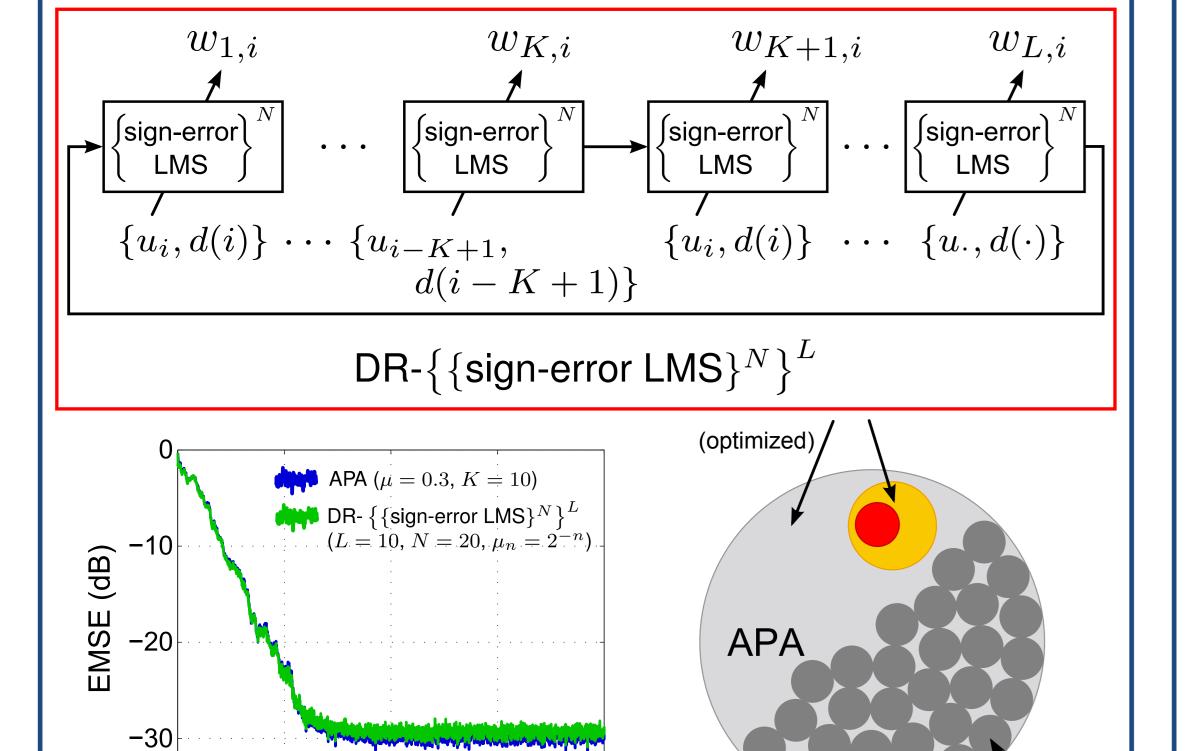
- (i) Incremental combination of sign-error LMS filters:  $\{$ sign-error LMS $\}^N$
- (ii)  $\{\text{sign-error LMS}\}^N \to \text{NLMS}, N \to \infty$
- (iii) Design  $\mu_n$  in  $\{\text{sign-error LMS}\}^N$  to minimize N
- (iv) DR- $\left\{ \{ \text{sign-error LMS} \}^{N} \right\}^{L}$

# **OVERVIEW**





White stationary scenario (fixed point quantization, 16 bits)



1500 2000

Iteration

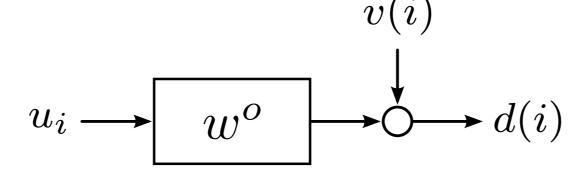
White stationary scenario

LMS

Complexity

#### BACKGROUND

## **Adaptive filters**



$$w_{i} = w_{i-1} + \mu u_{i}^{T} \operatorname{sign}[e(i)]$$

$$w_{i} = w_{i-1} + \mu u_{i}^{T} e(i)$$

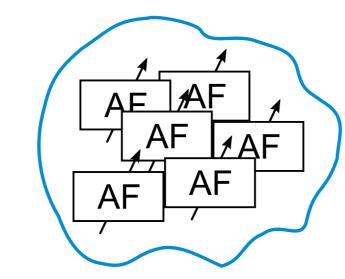
$$w_{i} = w_{i-1} + \frac{\mu}{\|u_{i}\|^{2} + \epsilon} u_{i}^{T} e(i)$$

$$w_{i} = w_{i-1} + \mu U_{i}^{T} (U_{i} U_{i}^{T} + \epsilon I)^{-1} e_{i}$$

$$U_{i} = \begin{bmatrix} u_{i} \\ \vdots \\ u_{i-K+1} \end{bmatrix} \begin{matrix} \uparrow \\ \kappa \\ d_{i} = \begin{bmatrix} d(i) \\ \vdots \\ d(i-K+1) \end{bmatrix} \begin{matrix} \uparrow \\ \kappa \\ \end{pmatrix}$$

#### **Combination of AFs**

• **Definition:** set of AFs combined by a supervisor



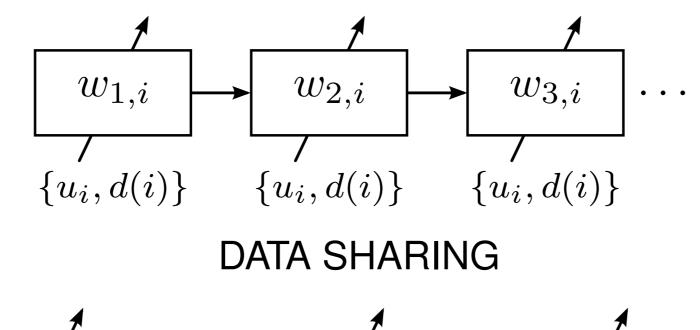
Better performance

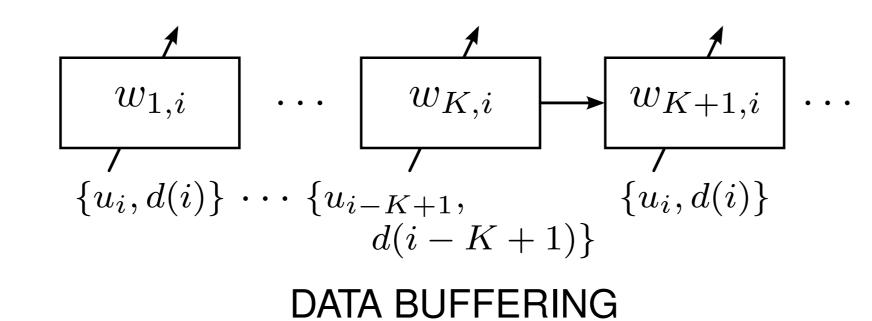
Higher complexity  $\left(\approx\sum\mathcal{O}[AF]\right)$ 

• "Combination as a complexity reduction technique"

e.g., 
$$\mathcal{O}\begin{bmatrix} \text{Combination} \\ \text{of LMS} \end{bmatrix} < \mathcal{O}[\text{APA}]$$
(same performance)

### DR incremental combinations





# PLENTY OF ROOM AT THE BOTTOM

Cássio G. Lopes

# **Algorithm 1** The $\{\text{sign-error LMS}\}^N$

$$||u_i||^2 = ||u_{i-1}||^2 - |u(i-M)|^2 + |u(i)|^2$$

$$y(i) = u_i w_{i-1} ; e_1(i) = d(i) - y(i)$$

$$v_{0,i} = w_{i-1}$$

$$\downarrow (1) \times (M) \times (M)$$

 $w_{0,i} = w_{i-1}$ 

for 
$$n = 1, ..., N$$

$$w_{n,i} = w_{n-1,i} + \mu_n u_i^T \operatorname{sign}[e_n(i)]$$

$$e_{n+1}(i) = e_n(i) - \mu_n ||u_i||^2 \operatorname{sign}[e_n(i)]$$

 $w_i = w_{N,i}$ 

- ✓ Low complexity:  $(M+1) \times$  (does not depend on N)
- Suited for finite precision & FPGA implementation

# $\{\text{sign-error LMS}\}^N \to \text{NLMS}$

$$\{\operatorname{sign-error} \mathsf{LMS}\}^N \Rightarrow w_i = w_{i-1} + \overline{\mu}(i) u_i^T \operatorname{sign}[e(i)]$$

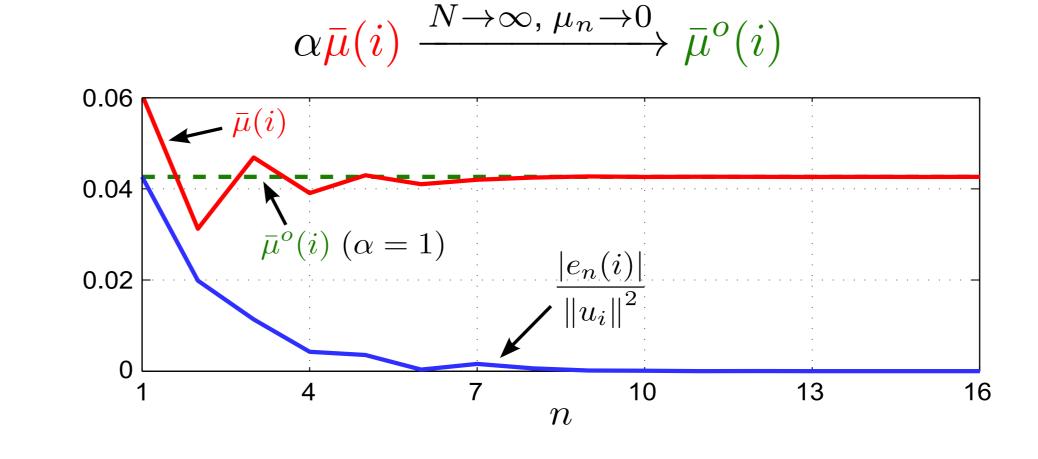
$$\overline{\mu}(i) = \mu_1 + \sum_{n=2}^N \mu_n \prod_{k=1}^{n-1} \operatorname{sign}\left[|e_k(i)| - \mu_k \|u_i\|^2\right]$$

From the sign-error LMS theory:

$$||w_i||^2 \le ||w_{i-1}||^2 \Leftrightarrow |e(i)| \ge \overline{\mu}(i) ||u_i||^2$$

$$\Rightarrow \overline{\mu}^o(i) = \alpha \frac{|e(i)|}{||u_i||^2}, \quad \alpha \in (0, 1]$$

$$w_i = w_{i-1} + \frac{\alpha}{\|u_i\|^2} u_i^T e(i)$$



same

performance /

Minimizing N: 
$$\mu_n=2^{-P-n}, \quad P\in\mathbb{Z}$$

 $\mathsf{DR} ext{-}\big\{\{\mathsf{sign} ext{-}\mathsf{error}\;\mathsf{LMS}\}^N\big\}^L$ 

$$\mathcal{O}\left[\begin{cases} \text{sign-error} \\ \text{LMS} \end{cases}^N \right] < \mathcal{O}\left[\text{LMS}\right]$$

$$+ \ \mathcal{O}\left[\,\mathsf{DR}\text{-}\{\mathsf{LMS}\}^L\,\right] < \mathcal{O}\left[\,\mathsf{APA}\,\right]$$

$$\mathcal{O}\left[\left\{ \left\{ \underset{\mathsf{LMS}}{\mathsf{sign-error}} \right\}^N \right\}^L \right] \ll \mathcal{O}\left[\mathsf{APA}\right]$$

#### SIMULATIONS

$$x(i) \sim \mathcal{N}(0, 1)$$
  $v(i) \sim \mathcal{N}(0, 10^{-3})$  (white)

White inputs: 
$$u(i) = x(i)$$

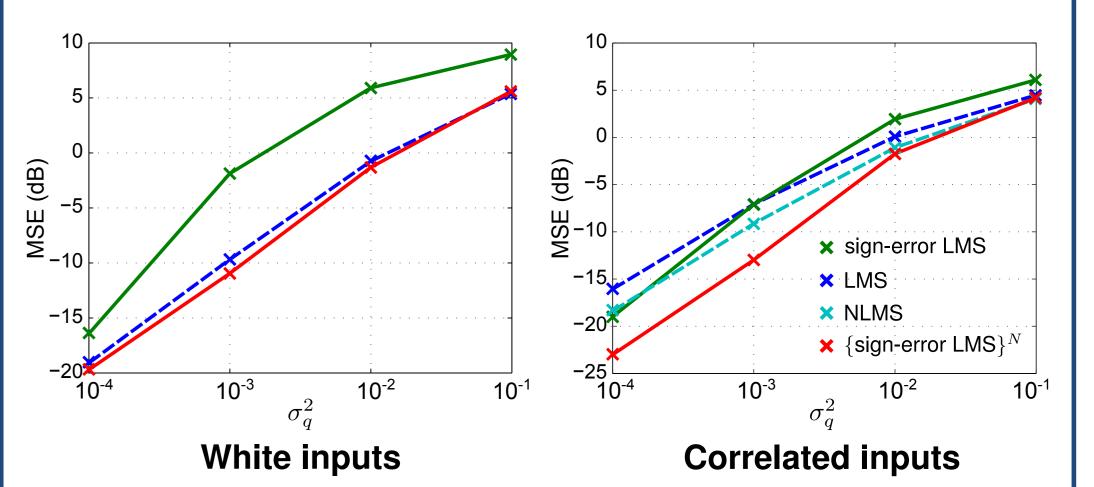
Correlated inputs: 
$$u(i) = \beta u(i-1) + \sqrt{1-\beta^2}x(i)$$
,  $\beta = 0.95$ 

Q: fixed point, 16 bits, F = 13 bits (signed Q2.13)

# $\{sign-error LMS\}^N$ : nonstationary scenario

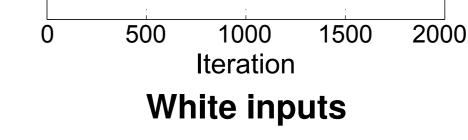
(M = 10, fixed point quantization, 16 bits)

$$w_i^o = \mathcal{Q}[w_{i-1}^o + q_i] \qquad q_i \sim \mathcal{N}(0, \sigma_q^2 I)$$

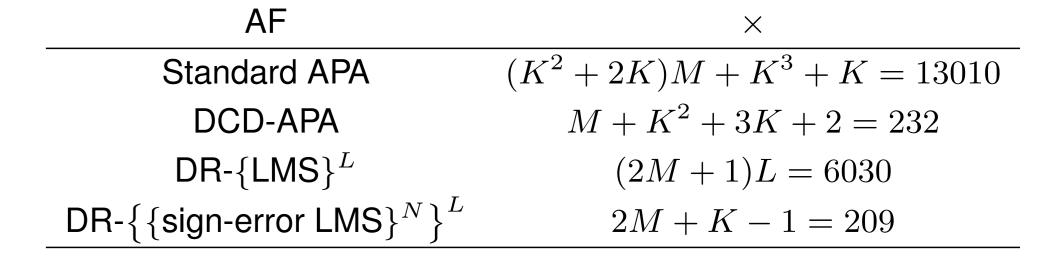


# $\left\{ \left\{ \text{sign-error LMS} \right\}^{N} \right\}^{L}$ and APA $(M=100,\,K=10,\,\text{double precision})$

 $\square$  DR-  $\{\{\text{sign-error LMS}\}^N\}^L$ Ш S Ы Ы



**Correlated inputs** 



### ACKNOWLEDGEMENT

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