

Date: 13/07/2023	Responsible: Leif Gunnar Ellingsen	Approved: ES/SBT	Document Name: PCB 0016 REV2	Rev: 2
CADint Ver.4.90.1.1f			Product description: NOFENCE BATTERY PCM TI CHIP PCB BARE	



TECHNICAL INFORMATION

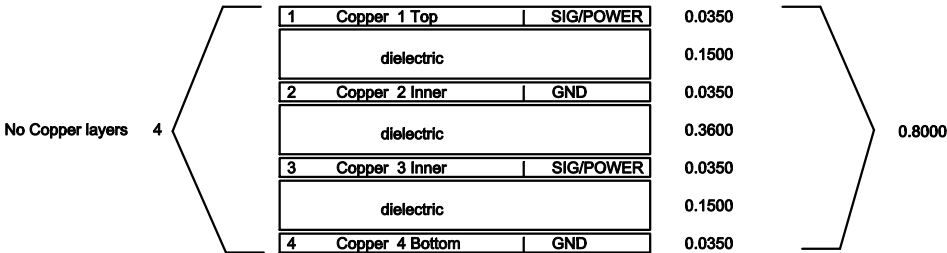
Board size in mm (page 2)	50.0000 X 7.0000	Laminate	FR4 Tg.170 deg
Cuts of outer contour (page 2)	0	Laminate thickness in mm	0.8000
Number of layers	4	Layer Stackup	PCB 0016 REV2-m.LOG
Cu-thickness outer layers in mm	0.0350	Electrical test	Specified on order
Cu-thickness inner layers in mm	0.0350	Surface	ENIG
Min. conductor width in mm	0.2500	Soldermask	Wetfilm both sides
Min. spacing/gap in mm	0.0800	SMD technique	TOP SIDE
Min. finished hole size in mm	0.3000	Pasta stencil not reduced	TOP SIDE
Min. annularring width in mm	0.2500	Legend	Specified on order
Tented/closed vias	NONE	Carbon printing	NO
Buried/blind vias	No	Chemical Ni/Au	NO
Qty. of plated holes	114	Gold plated contacts	NO
Qty. of plated hole sizes	1	Thickness Ni	5-15µm
Qty. of non-plated holes	0	Thickness Au	>1.5µm
Qty. of non-plated hole sizes	0	Hole tolerances	± 0.10mm
Total qty. of holes	114	Quality requirements	IPC-A-600
Finished hole size for via 0.10-0.25mm is not	critical	D-codes	GERBER EXTENDED

PCB 0016 REV2.ZIP		ZIP-file for manufacturing of this PCB following files:		
File-name	Manufacturing Layer	File-type	This document page	
			1	
PCB 0016 REV2 -SilkScreen Top.GBR	SILKSCREEN TOP	Gerber Extended / PDF	8	
PCB 0016 REV2 -SolderMask Top.GBR	SOLDERMASK TOP	Gerber Extended		
PCB 0016 REV2 -PasteMask Top.GBR	PASTEMASK TOP (NOT REDUCED)	Gerber Extended		
PCB 0016 REV2 -Copper 1 Top.GBR	COPPER 1 TOP	Gerber Extended / PDF	4	
PCB 0016 REV2 -Copper 2 Inner.GBR	COPPER 2 INNER	Gerber Extended / PDF	5	
PCB 0016 REV2 -Copper 3 Inner.GBR	COPPER 3 INNER	Gerber Extended / PDF	6	
PCB 0016 REV2 -Copper 4 Bottom.GBR	COPPER 4 BOTTOM	Gerber Extended / PDF	7	
PCB 0016 REV2 -SilkScreen Bottom.GBR	SILKSCREEN BOTTOM	Gerber Extended / PDF	9	
PCB 0016 REV2 -SolderMask Bottom.GBR	SOLDERMASK BOTTOM	Gerber Extended		
PCB 0016 REV2 -PasteMask Bottom.GBR	PASTEMASK BOTTOM (NOT REDUCED)	Gerber Extended		
PCB 0016 REV2 -m.LOG	GERBER&DRILL OUTPUT FILES REPORT	Text File		
PCB 0016 REV2 -Hole Plated T-B.NCD	HOLE PLATED	Drill File		
PCB 0016 REV2 -Hole Unplated T-B.NCD	HOLE UNPLATED	Drill File		
PCB 0016 REV2 -Board Outline.GBR	BOARDFORMAT1	Gerber Extended		
	OUTLINE AND DRILL INFORMATION	PDF	3	
PCB 0016 REV2 .P_P	PICK & PLACE	ASCII File		
	LAYER STACKUP	PDF	2	
	IMPEDANSE MATCHING	PDF	10	

SPECIAL REQUIREMENTS (note)

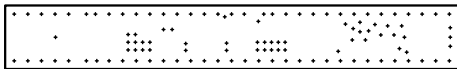
No Layers 7
No Copper layers 4
Board Thickness 0.8000

No	NoCopper	Material Type	Material Name	Copper Name	Layer Thickness	Er/Bulk	Tan/Temp.Coeff.
1	1	metal	Cu	Copper 1 Top	0.0350	1.724000e-08	3.930000e-03
2		dielectric	FR4		0.1500	4.700000e+00	2.000000e-02
3	2	metal	Cu	Copper 2 Inner	0.0350	1.724000e-08	3.930000e-03
4		dielectric	FR4		0.3600	4.700000e+00	2.000000e-02
5	3	metal	Cu	Copper 3 Inner	0.0350	1.724000e-08	3.930000e-03
6		dielectric	FR4		0.1500	4.700000e+00	2.000000e-02
7	4	metal	Cu	Copper 4 Bottom	0.0350	1.724000e-08	3.930000e-03



LAYER STACKUP - USE MANUFACTURER STD

DESIGNE DESCRIPTION: NOFENCE BATTERY PCM TI CHIP PCB BARE NOFENCE DOC NAME: PCB 0016 REV2	DESIGNED: LGE
	TRACED: LGE
	CHECKED BY: ES/SBT
	DATE: 13/07/2023
PAGE: 2 (9)	

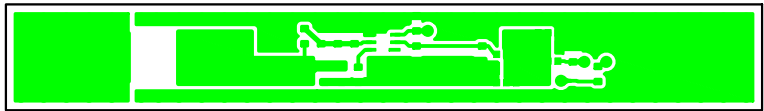


Code	Diameter	Qty	Type
Unmarked	0.3000	114	Hole Plated

No of Components	23
No of Components TOP	18
No of Components BOT	5
No of Pads Total	55
No of Pads SMD TOP	50
No of Pads SMD BOT	5
No of Pads Through	0
No of Pads SMD Total	55
No of Pads Error Inner	0
No of Pads Error Undef.	0
No of Nets	10
No of Vias	114
No of Vias Through	114
No of Vias Blind TOP	0
No of Vias Blind BOT	0
No of Vias Buried	0
Board Width ->	50.0000
Board Hight ->	7.0000
Board Area ->	350.0000

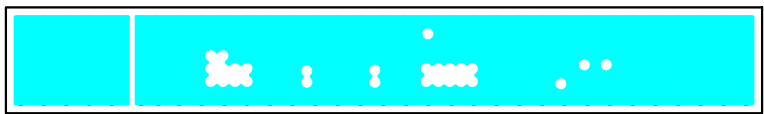
OUTLINE AND HOLE DRAWING

DESIGNE DESCRIPTION:		DESIGNED:	LGE
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED:	LGE
NOFENCE		CHECKED BY:	ES/SBT
N-6631 BATNFIJORDSØRA		DATE:	13/07/2023
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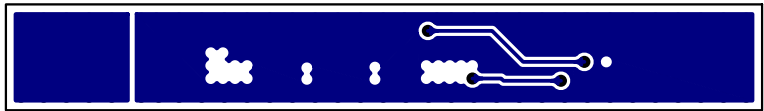
ARTWORK SEEN FROM TOPSIDE - COPPER 1 TOP

DESIGNE DESCRIPTION:		DESIGNED: LGE	
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED: LGE	
		CHECKED BY: ES/SBT	
NOFENCE		DATE: 13/07/2023	
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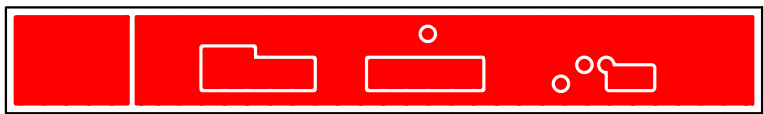
ARTWORK SEEN FROM TOPSIDE - COPPER 2 INNER

DESIGNE DESCRIPTION:		DESIGNED: LGE	
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED: LGE	
		CHECKED BY: ES/ST	
NOFENCE		DATE: 13/07/2023	
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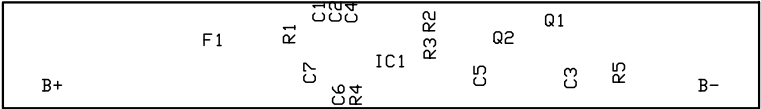
ARTWORK SEEN FROM TOPSIDE - COPPER 3 INNER

DESIGNE DESCRIPTION:		DESIGNED: LGE	
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED: LGE	
		CHECKED BY: ES/SBT	
NOFENCE		DATE: 13/07/2023	
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ARTWORK SEEN FROM TOPSIDE - COPPER 4 BOTTOM

DESIGNE DESCRIPTION:		DESIGNED: LGE	
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED:	LGE
		CHECKED BY:	ES/ST
NOFENCE		DATE:	13/07/2023
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SILKSCREEN TOP

DESIGNE DESCRIPTION:		DESIGNED: LGE	
NOFENCE BATTERY PCM TI CHIP PCB BARE		TRACED: LGE	
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TP-	No fence 0016 rev2		P+	TP+
	T	P-		

SILKSCREEN BOTTOM

DESIGNE DESCRIPTION:		DESIGNED:	LGE
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