LRC with multi-writer coherence protocol

P1

Lock(L);

x ←
y ←   } x,y,z pages modified in c-s ⟹ {x_d, y_d, z_d} diffs
z ←

Unlock(L);

P2

Lock(L);

Q ← x

fetch at Point of access

P3

Lock(L);

x ← ⟹ x_d

unlock(L);

invalidate → x,y,z at lock acquisition (LRC)