

Address Editor

Cell	Slave Interface	Base Name	Offset Address	Range	High Address
ps_0					
Data (32 address bits : 0x40000000 [1G])					
Fast_ADC/axi_intc_0	s_axi	Reg	0x4000_0000	4K	0x4000_0FFF
cfg_0	S_AXI	reg0	0x4000_1000	4K	0x4000_1FFF
sts_0	S_AXI	reg0	0x4000_2000	4K	0x4000_2FFF
xadc_wiz_0	s_axi_lite	Reg	0x4000_3000	4K	0x4000_3FFF
Fast_ADC/writer_0					
M_AXI (32 address bits : 4G)					
ps_0	S_AXI_HP0	HP0_DDR_LO...	0x0000_0000	512M	0x1FFF_FFFF