TABLE I
SPECIFICATIONS OF THE REDPITAYA STEMLAB BOARD

Processor	Xilinx Zynq-7010 SoC ARM dual core CPU and Artix 7 FPGA
System disk	MicroSD
RAM	512MB
Access	USB console, Ethernet, WiFi dongle
Power	5V x 2A max., 0.9A typ.
Fast DAC	Dual channel, 14-bit, 125MSPS
Fast ADC	Dual channel, 14-bit, 125MSPS
Other I/Os	Slow DAC x 4, slow ADC x 4, digital I/O x 16, daisy chain connector