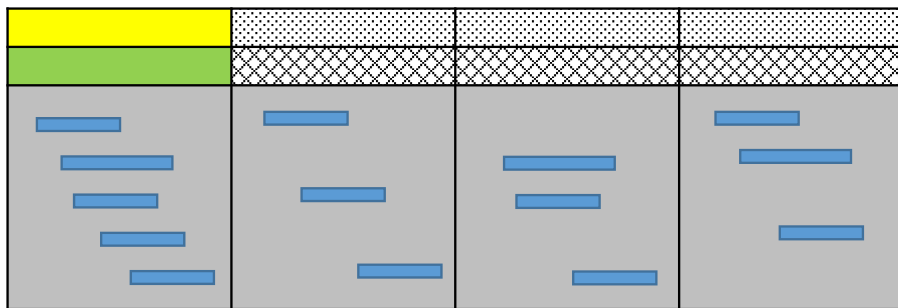
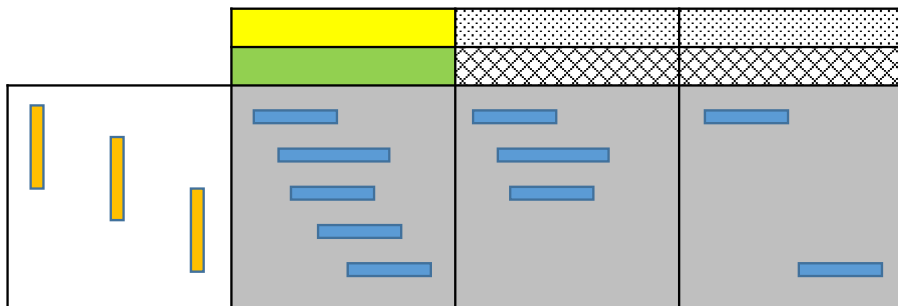


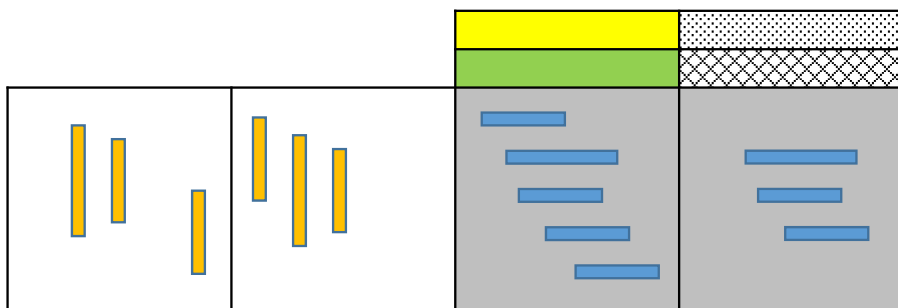
CPE0



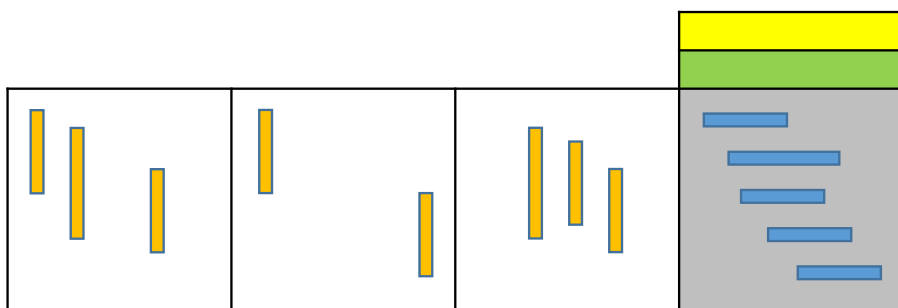
CPE1



CPE2



CPE3



x, b

b

A, row, col

N

x

b

①

DMA

x, b

DMA

A, row, col

②

col (DMA)

RLC

col (RLC)

③

x (DMA)

RLC

x (RLC)

④

b (Computed)

RLC

b (DMA)