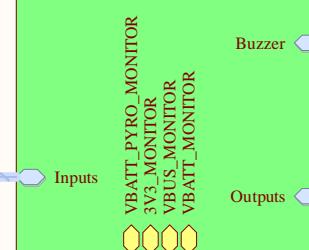
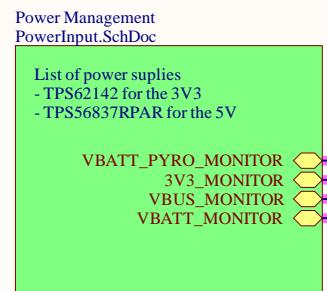
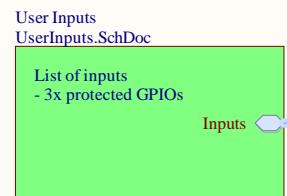
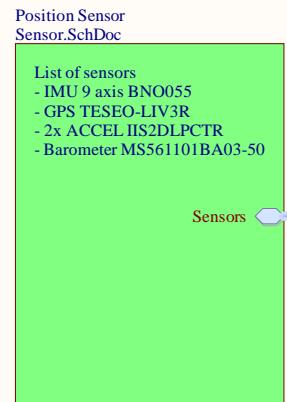
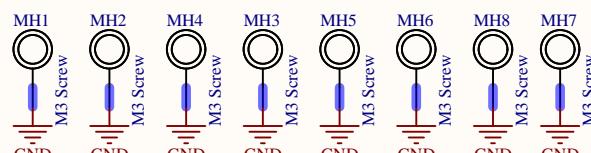


⚠ 2x 9V battery for the whole project.  
One is for the Pyro triggering, the other is for powering the MCU + Position servos  
1A slow blow fuses  
-Nominal current = 250 / 300 mA for the VBATT  
-Nominal current = 2A for < 2s for the VBAT PYRO (200% of fuse = ~20s manufacturer).  
Diodes for reverse polarity (trigger the fuses).

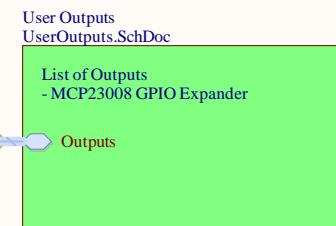
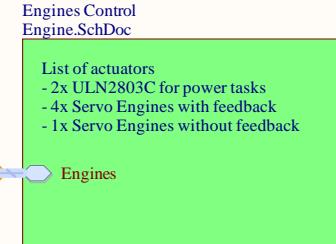
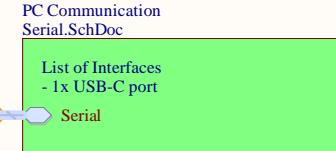


Serial

Engines

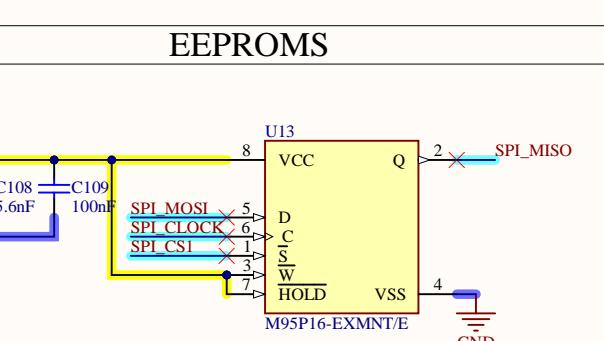
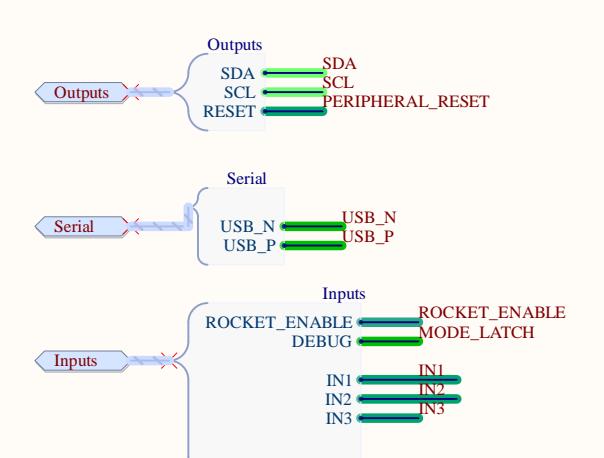
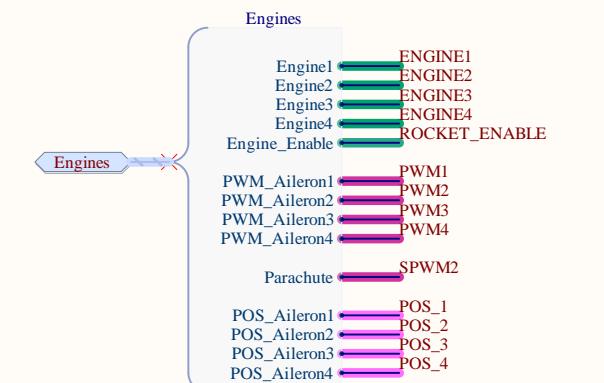
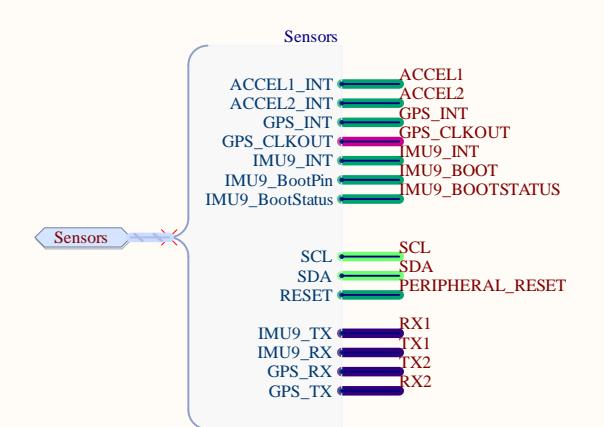
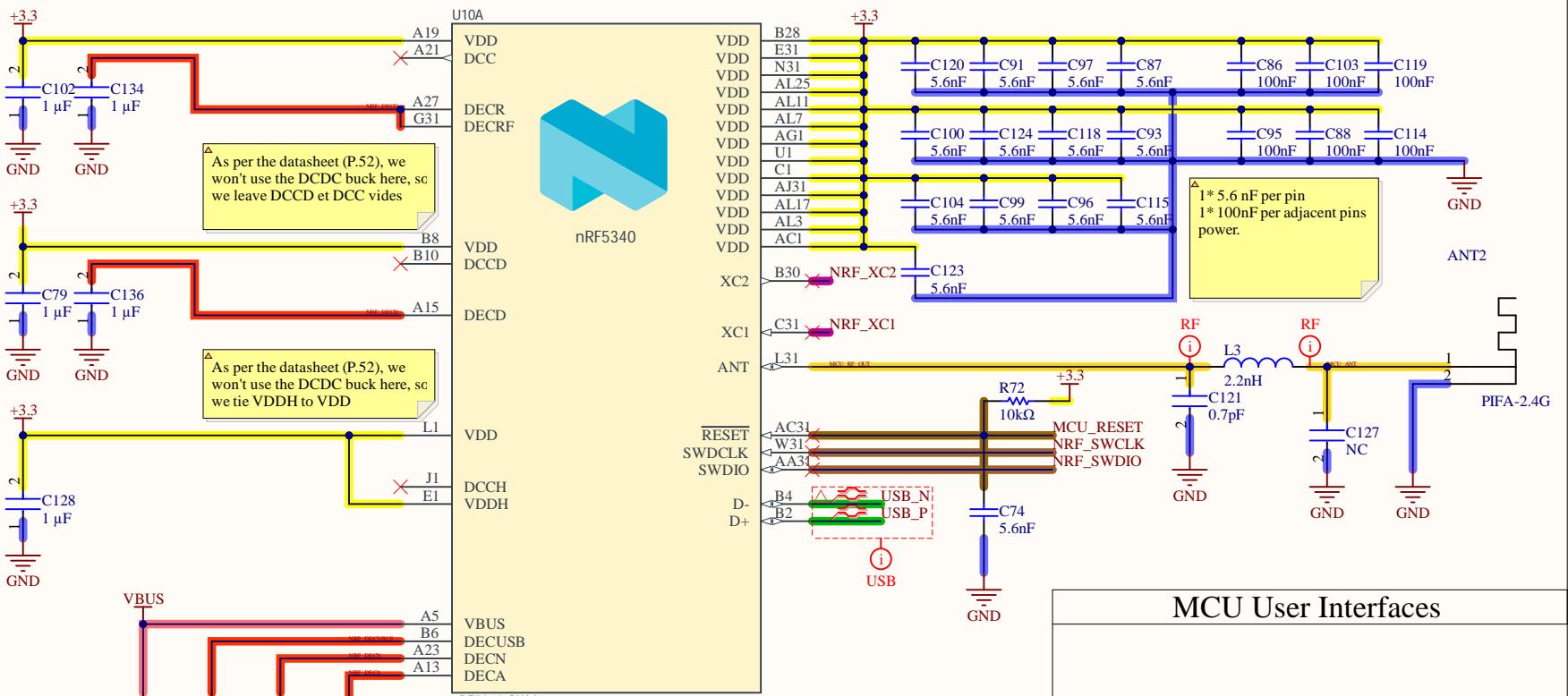
Buzzer

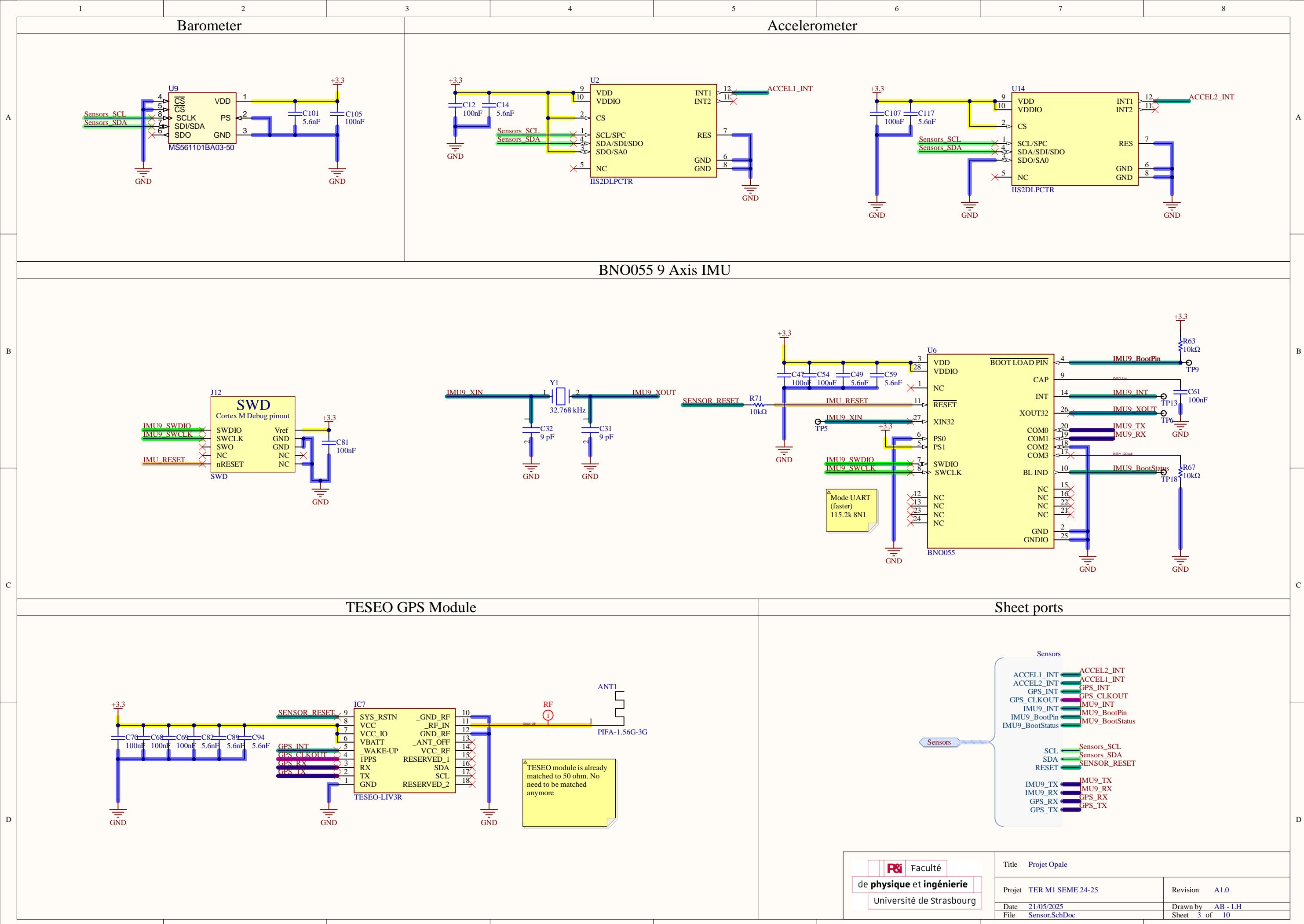
Outputs

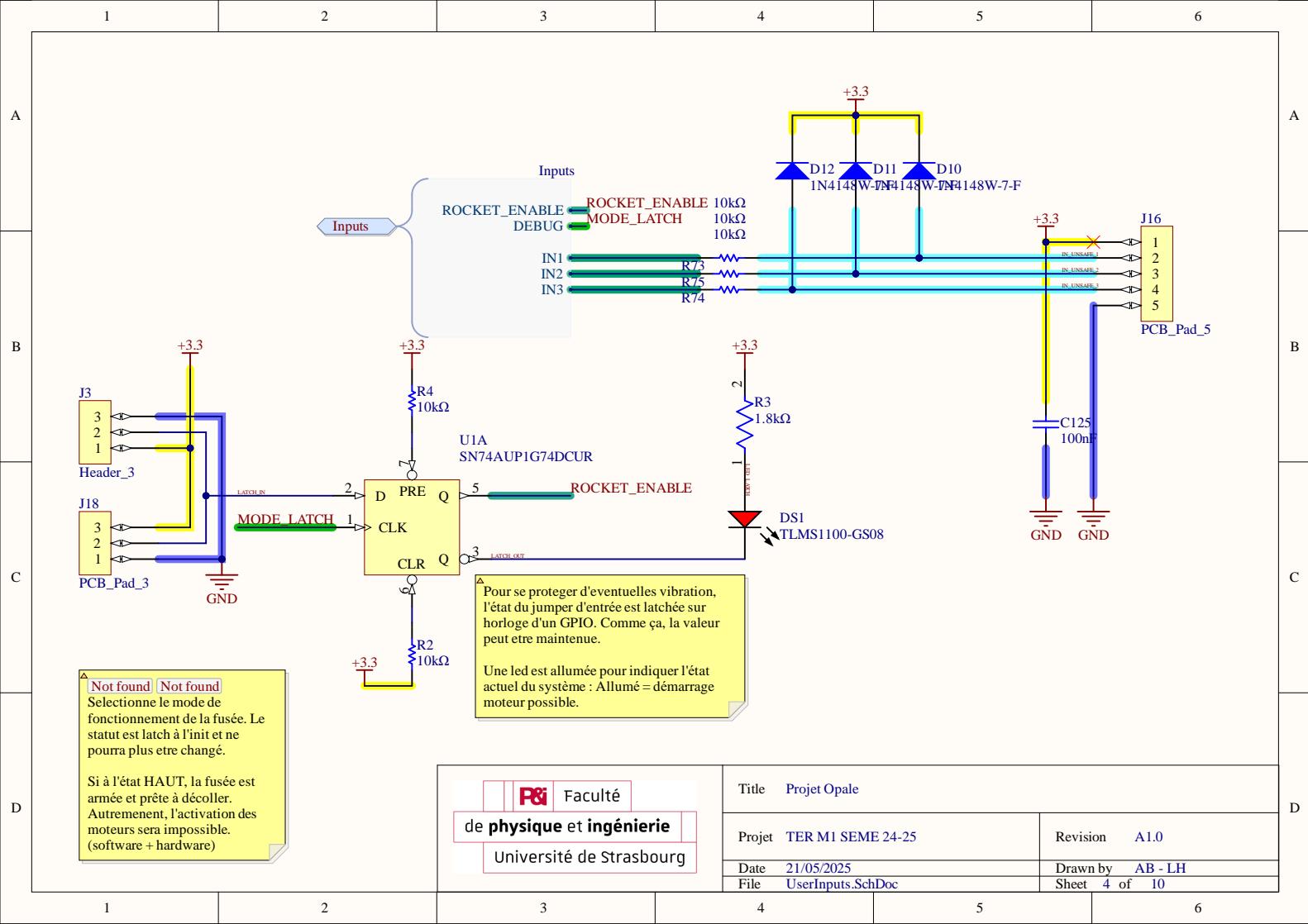


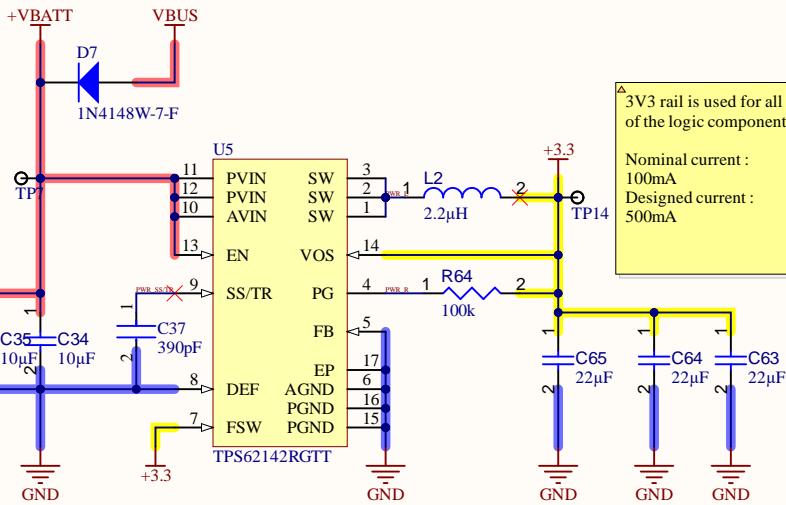
## MCU Power + SWD Programmation

## Sheet ports





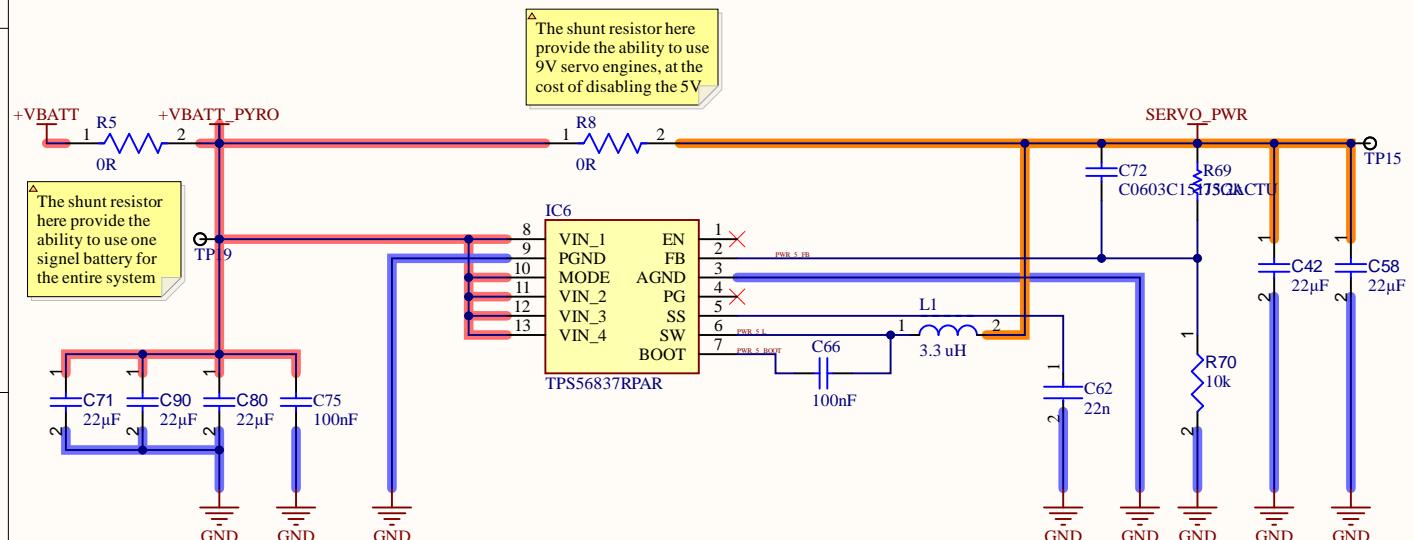




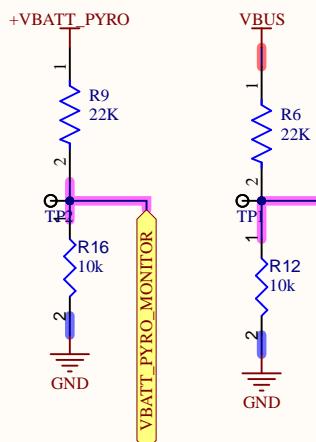
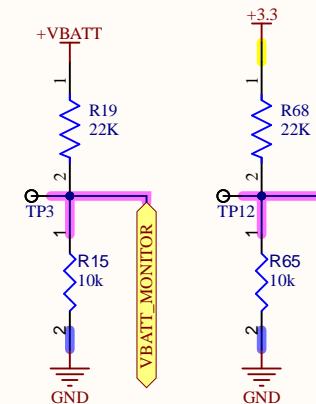
3V3 rail is used for all of the logic components

Nominal current :  
100mA  
Designed current :  
500mA

5V buck for servo engines



▲ 5V Rail is designed for all of the servo motor power.  
Nominal current : 800mA  
Designed current : 2A



A

A

B

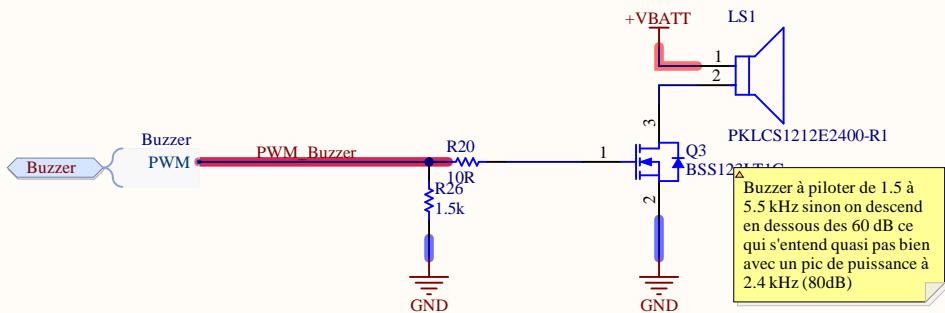
B

C

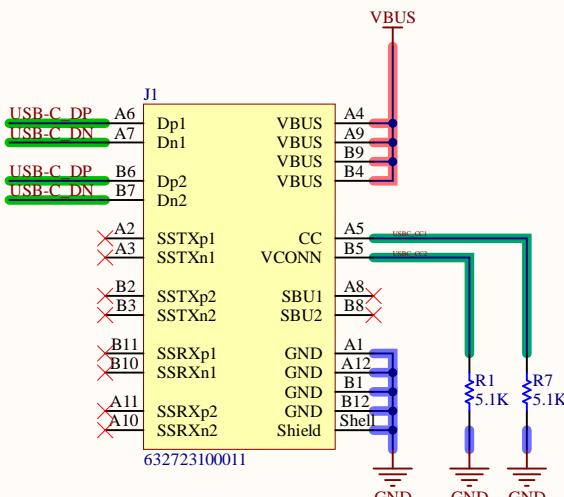
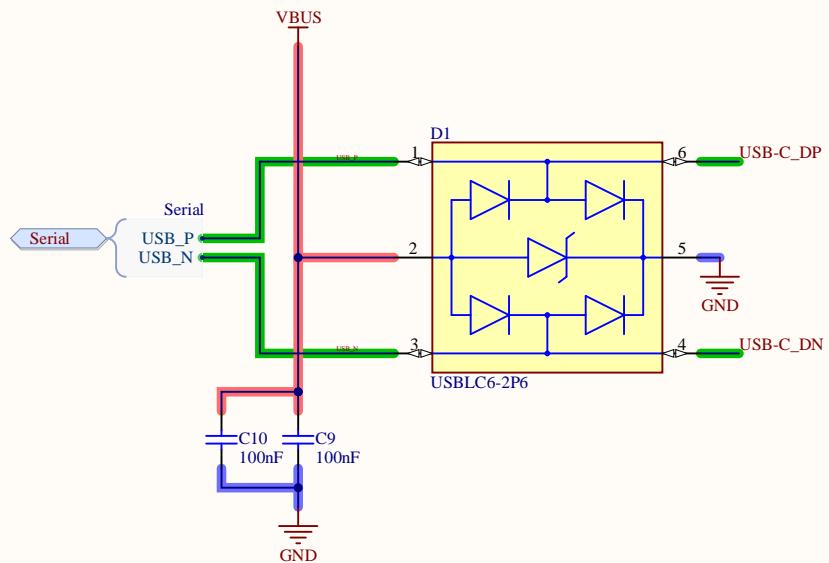
C

D

D



A

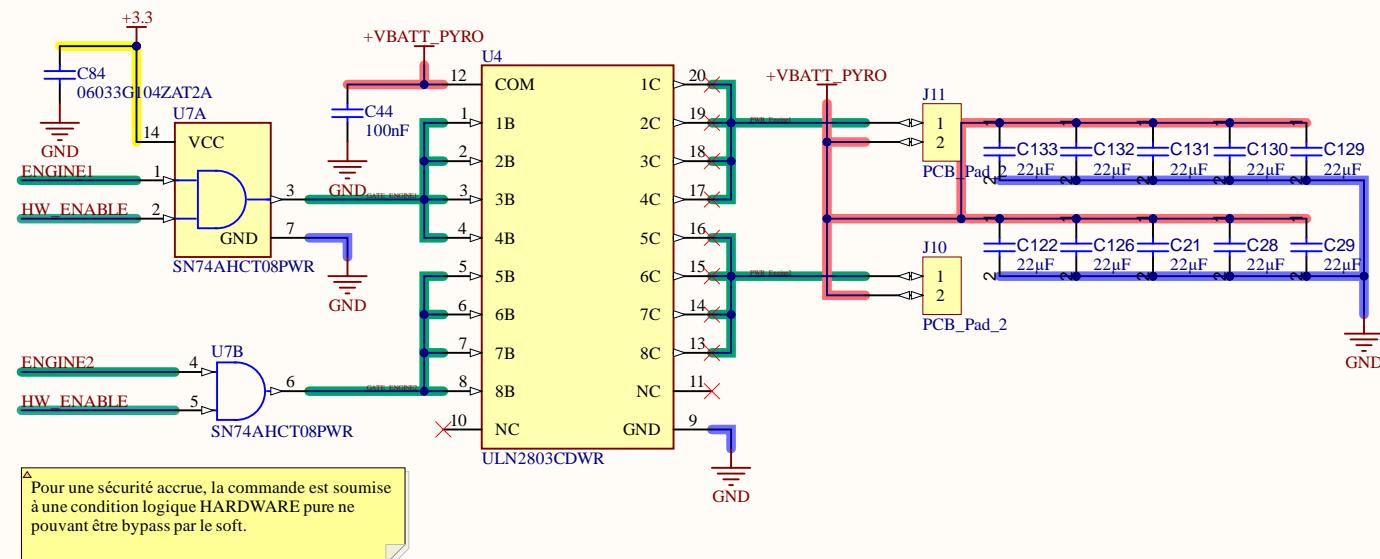
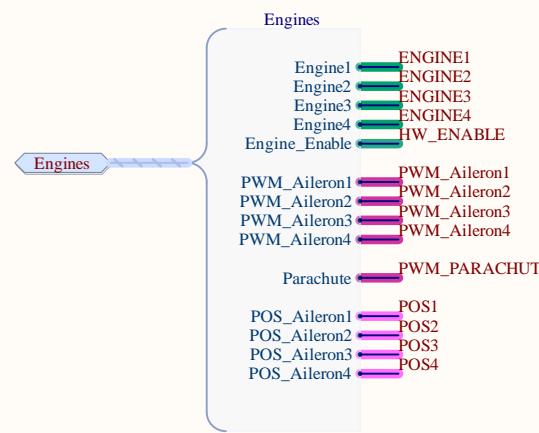


Two 5.1k resistors on CC1 and CC2 since the board require a 5V VBUS to be present for the D+ and D- lines to be driven by the MCU.

This is part of the USB-C specs

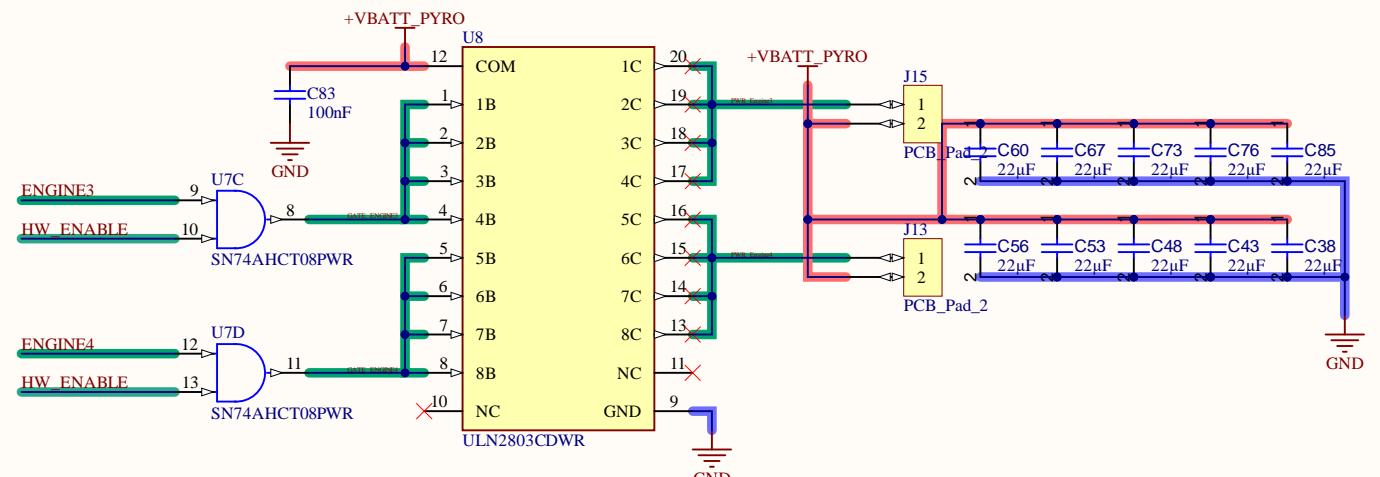
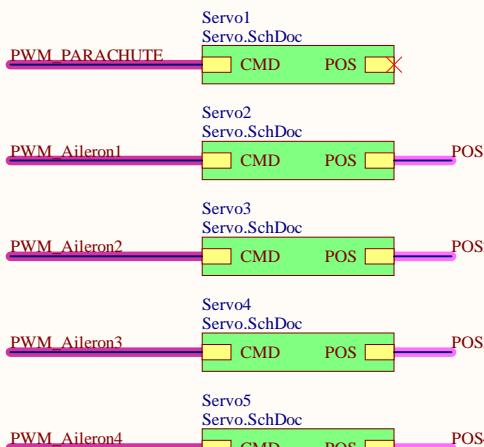
## Sheet ports

## Engine power circuitry for 1&amp;2



## Servo engines circuitry

## Engine power circuitry for 3&amp;4



A

A

B

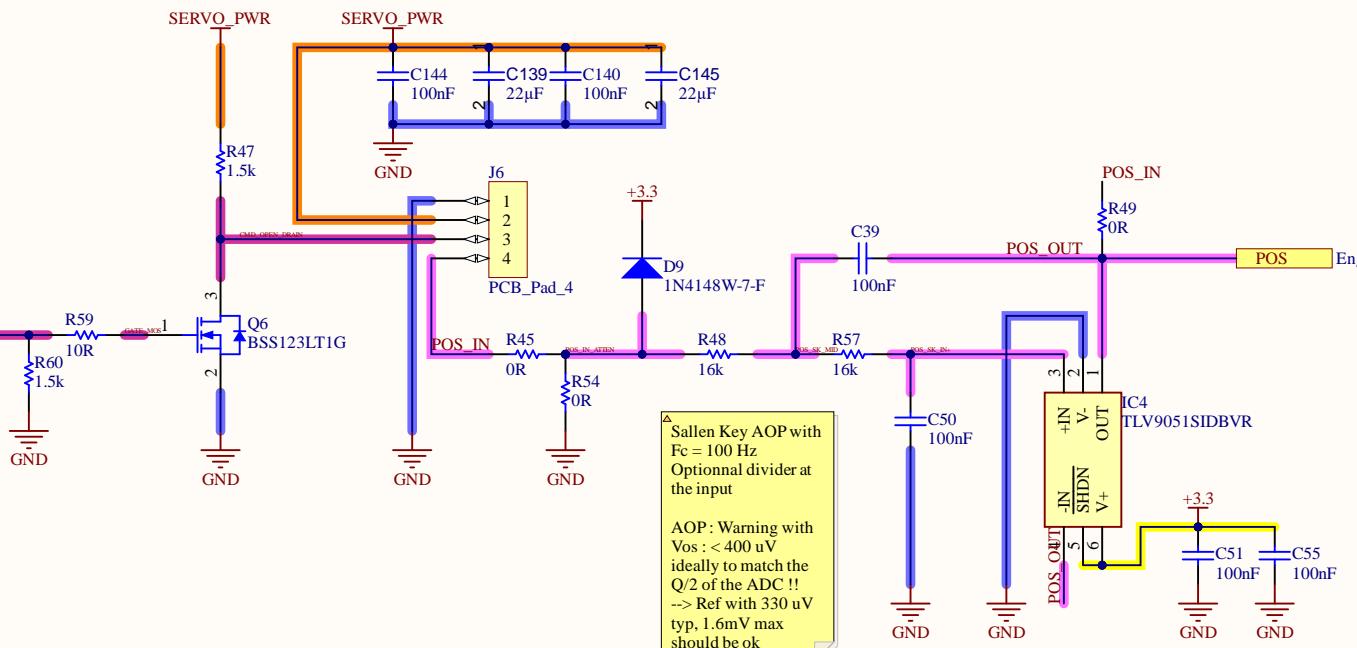
B

C

C

D

D



A

A

B

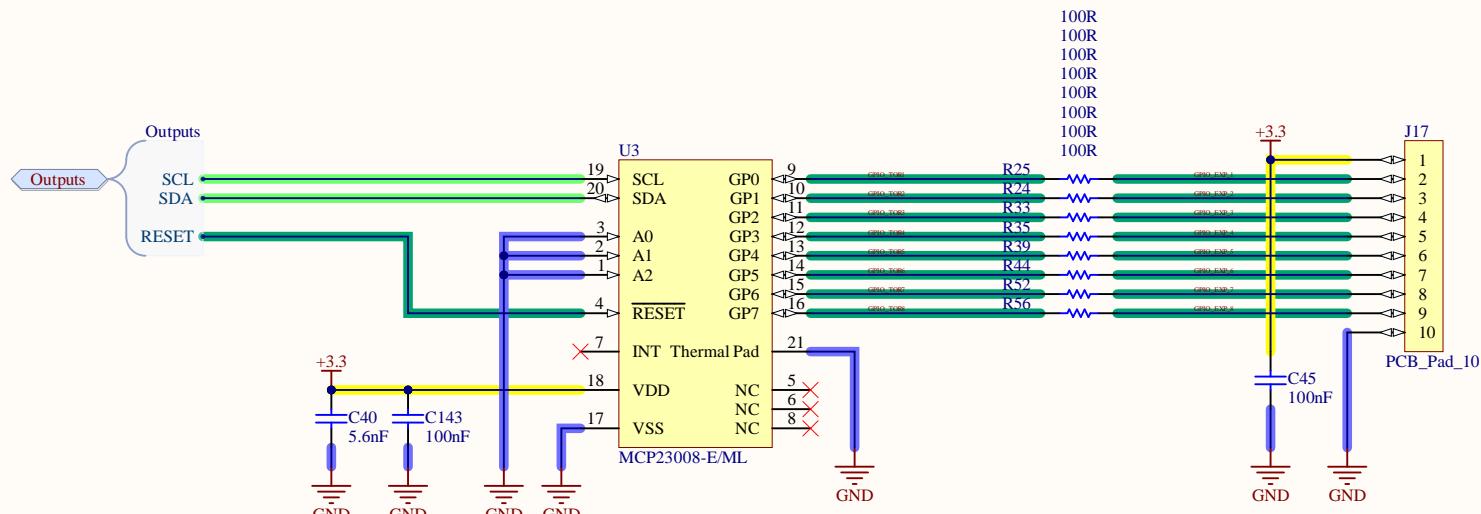
B

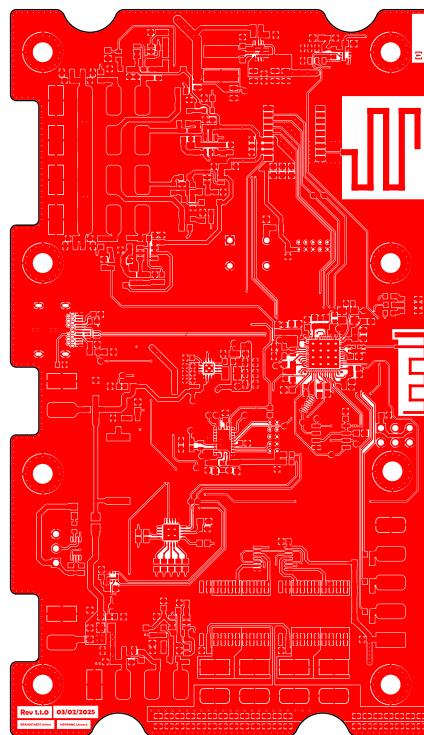
C

C

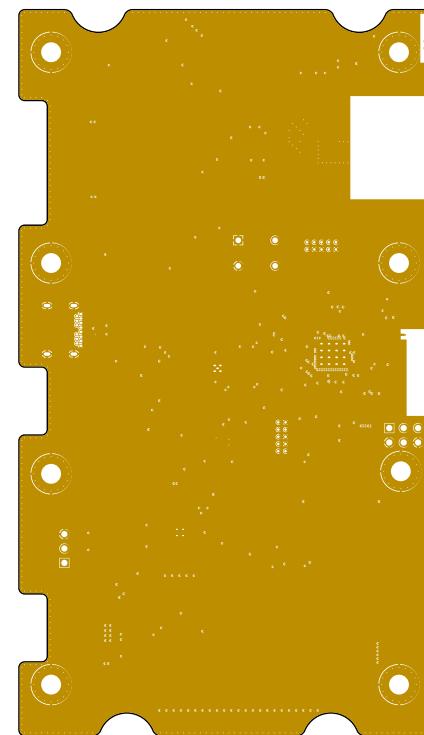
D

D

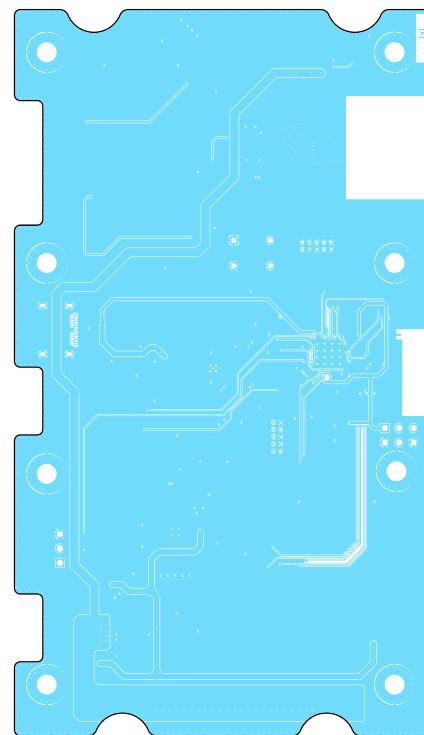




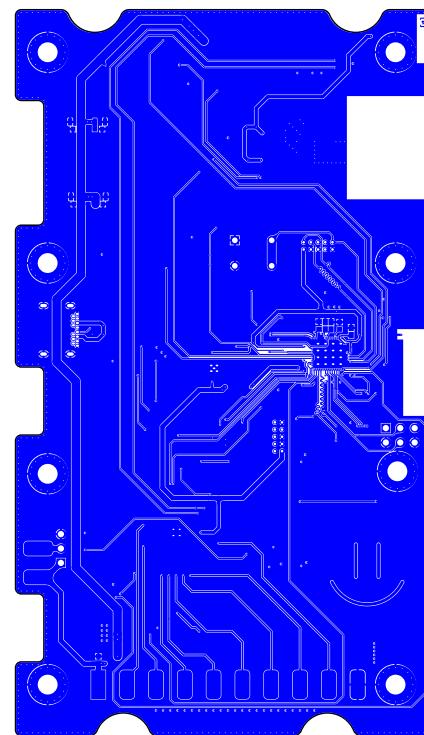
Top (Scale 1:1.33)



GND1 (Scale 1:1.33)



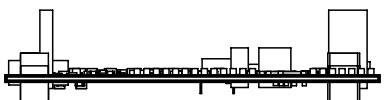
Int (Scale 1:1.33)



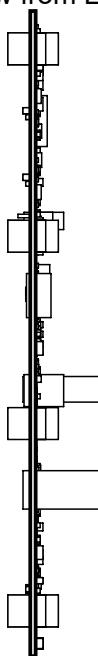
Bot (Scale 1:1.33)

A

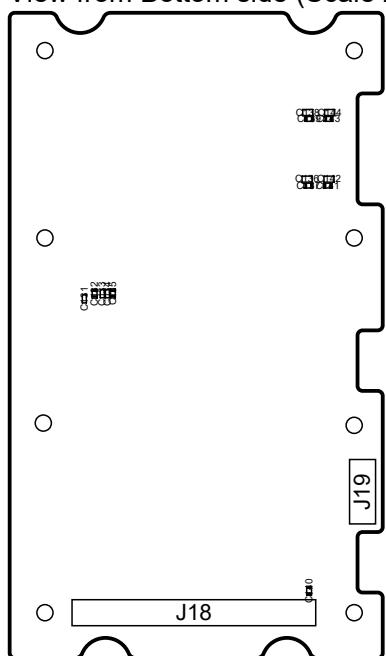
View from Front side (Scale 2:3)



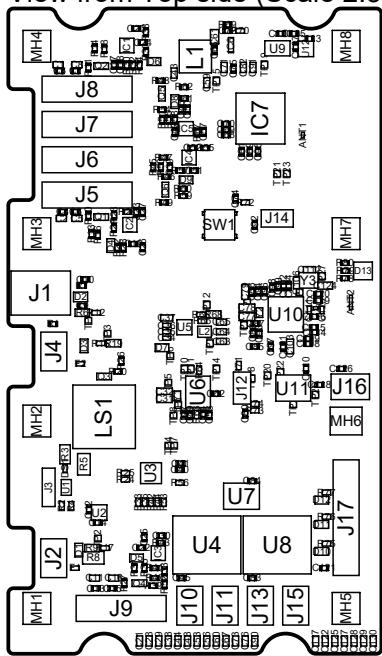
View from Left side (Scale 2:3)



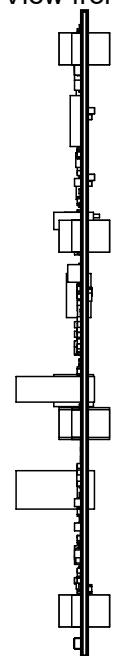
3) View from Bottom side (Scale 2:3)



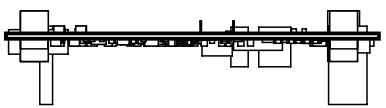
View from Top side (Scale 2:3)



### View from Right side (Scale)



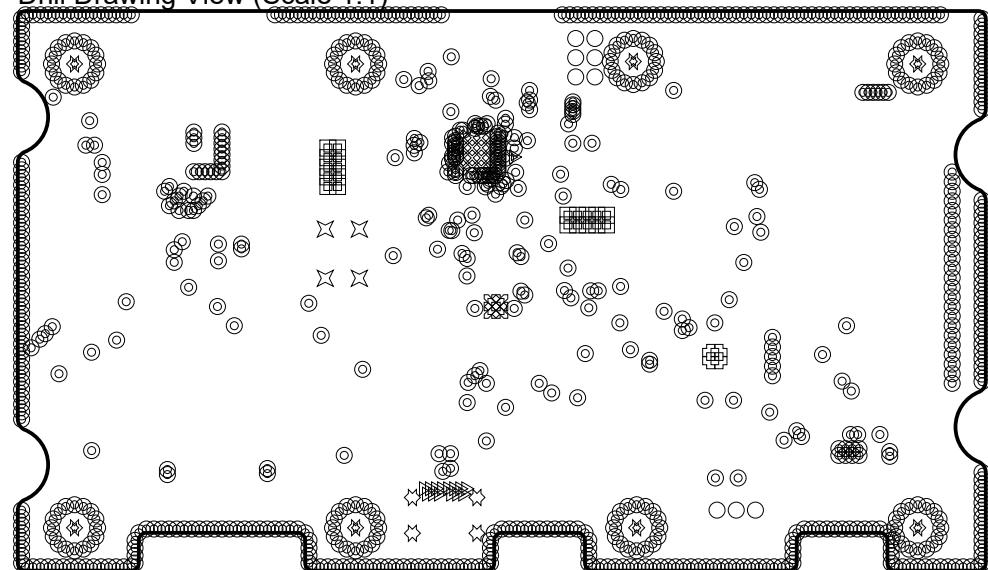
View from Back side (Scale 2:3)



## Layer Stack Legend

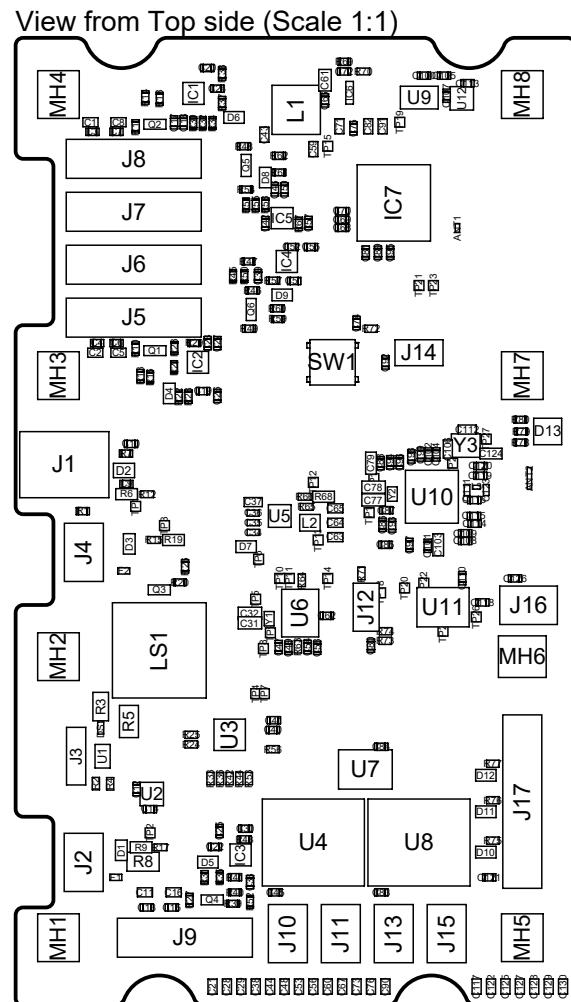
	Material	Layer	Thickness	Dielectric Material	Type	Gerber
1	Top Overlay				Legend	GTO
	Surface Material	Top Solder	0.03mm	Solder Resist	Solder Mask	GTS
	<b>CF-004</b>	<b>Top</b>	<b>0.04mm</b>		<b>Signal</b>	<b>GTL</b>
	Prepreg		0.10mm	PP-015	Dielectric	
	<b>CF-003</b>	<b>GND1</b>	<b>0.02mm</b>		<b>Signal</b>	<b>G1</b>
			0.70mm	FR-4	Dielectric	
2	<b>CF-003</b>	<b>Int</b>	<b>0.02mm</b>		<b>Signal</b>	<b>G2</b>
	Prepreg		0.10mm	PP-015	Dielectric	
	<b>CF-004</b>	<b>Bot</b>	<b>0.04mm</b>		<b>Signal</b>	<b>GBL</b>
	Surface Material	Bottom Solder	0.03mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay				Legend	GBO
	Total thickness: 1.06mm					

## Drill Drawing View (Scale 1:1)



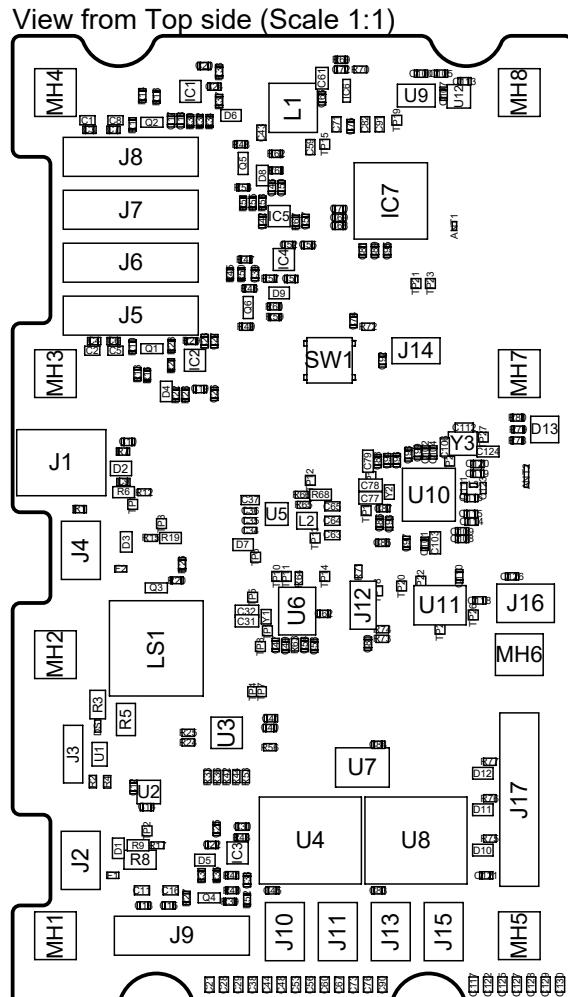
## Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
○	745	0.15mm	Plated	
+	4	0.20mm	Plated	
▽	1	0.25mm	Plated	
✗	5	0.30mm	Plated	
◇	16	0.30mm	Plated	
▽	14	0.40mm	Plated	
□	20	0.45mm	Plated	
✗	4	0.60mm	Plated	
○	9	1.10mm	Plated	
✗	8	3.50mm	Non-Plated	
	830 Total			



Bill Of Materials

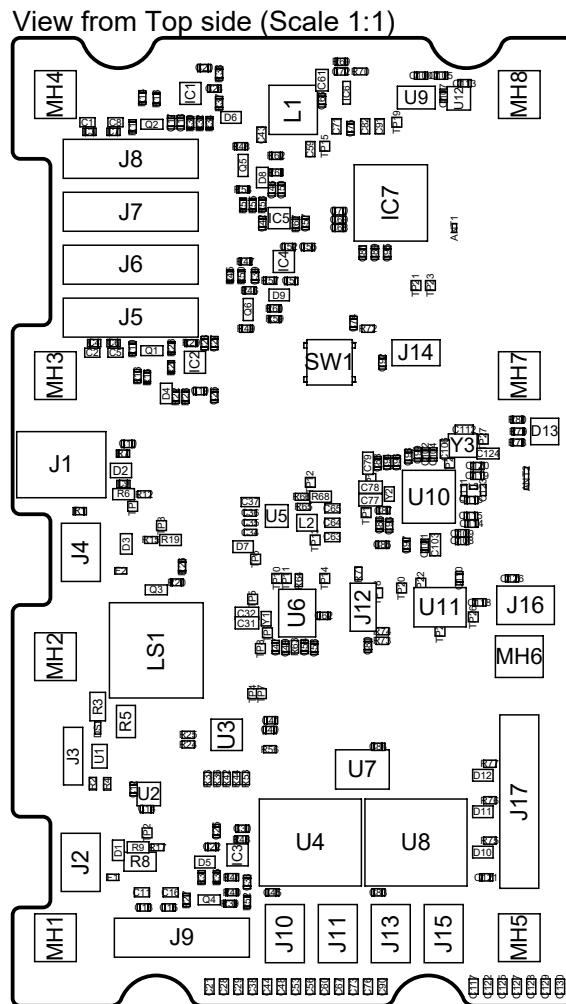
Line #	Designator	Name	Quantity
	C1, C9, C10, C12, C39_Servo1, C39_Servo2, C39_Servo3, C39_Servo4, C39_Servo5, C44, C45, C47, C50_Servo1, C50_Servo2, C50_Servo3, C50_Servo4, C50_Servo5, C51_Servo1, C51_Servo2, C51_Servo3, C51_Servo4, C51_Servo5, C54, C55_Servo1, C55_Servo2, C55_Servo3, C55_Servo4, C55_Servo5, C61, C66, C68, C69, C70, C75, C81, C83, C84, C86, C88, C92, C95, C103, C105, C107, C109, C114, C119, C125, C135, C140_Servo1, C140_Servo2, C140_Servo3, C140_Servo4, C140_Servo5, C143, C144_Servo1, C144_Servo2, C144_Servo3, C144_Servo4, C144_Servo5	100nF, 06033G104ZAT2A	60
	C14, C40, C49, C59, C74, C82, C87, C89, C91, C93, C94, C96, C97, C99, C100, C101, C104, C108, C115, C117, C118, C120, C123, C124	5.6nF	24
	C21, C28, C29, C38, C42, C43, C48, C53, C56, C58, C60, C63, C64, C65, C67, C71, C73, C76, C80, C85, C90, C122, C126, C129, C130, C131, C132, C133, C139_Servo1, C139_Servo2, C139_Servo3, C139_Servo4, C139_Servo5, C145_Servo1, C145_Servo2, C145_Servo3, C145_Servo4, C145_Servo5	Capacitor 22µF +/-40% 16V 0805	38
	C31, C32, C77, C78	9 pF	4
	C34, C35, C36	Capacitor 10µF +/-20% 10V 0603	3
	C37	390pF	1
	C62	22n	1
	C72	C0603C151J5GACTU	1
	C79, C102, C128, C134, C136, C137	1 µF	6
	C98, C138	4.7 uF	2
	C106, C116	10 pF	2
	C121, C127	0.7pF, NC	2
	D1	USBL6-2P6	1



Bill Of Materials

Line #	Designator	Name	Quantity
	D2, D3, D7, D9_Servo1, D9_Servo2, D9_Servo3, D9_Servo4, D9_Servo5, D10, D11, D12	1N4148W-7-F	11
	D13	3535 LEDS	1
	DS1	TLMS1100-GS08	1
	F1, F2	0408001.WR	2
	IC4_Servo1, IC4_Servo2, IC4_Servo3, IC4_Servo4, IC4_Servo5	TLV9051SIDBVR	5
	IC6	TPS56837RPAR	1
	IC7	TESEO-LIV3R	1
	J1	632723100011	1
	J3	Header_3	1
	J5	Header_3x2	1
	J12, J14	SWD	2
	L1	3.3 uH	1
	L2	LQM2HPN2R2MG0L	1
	L3	2.2nH	1
	LS1	PKLCS1212E2400-R1	1
	MH1, MH2, MH3, MH4, MH5, MH6, MH7, MH8	M3 Screw	8
	Q3, Q6_Servo1, Q6_Servo2, Q6_Servo3, Q6_Servo4, Q6_Servo5	BSS123LT1G	6
	R1, R7	5.1K	2
	R2, R4, R10, R11, R63, R67, R71, R72, R73, R74, R75	10kΩ	11
	R3	1.8kΩ	1
	R5, R8	0R	2
	R6, R9, R19, R68	22K	4
	R12, R15, R16, R65, R70	Resistor 10k +/-1% 0603 100 mW	5
	R20, R59_Servo1, R59_Servo2, R59_Servo3, R59_Servo4, R59_Servo5	10R	6
	R24, R25, R33, R35, R39, R44, R52, R56	100R	8
	R26, R47_Servo1, R47_Servo2, R47_Servo3, R47_Servo4, R47_Servo5, R60_Servo1, R60_Servo2, R60_Servo3, R60_Servo4, R60_Servo5	1.5k	11
	R45_Servo1, R45_Servo2, R45_Servo3, R45_Servo4, R45_Servo5, R49_Servo1, R49_Servo2, R49_Servo3, R49_Servo4, R49_Servo5, R54_Servo1, R54_Servo2, R54_Servo3, R54_Servo4, R54_Servo5	0R	15

Bill Of Materials



A

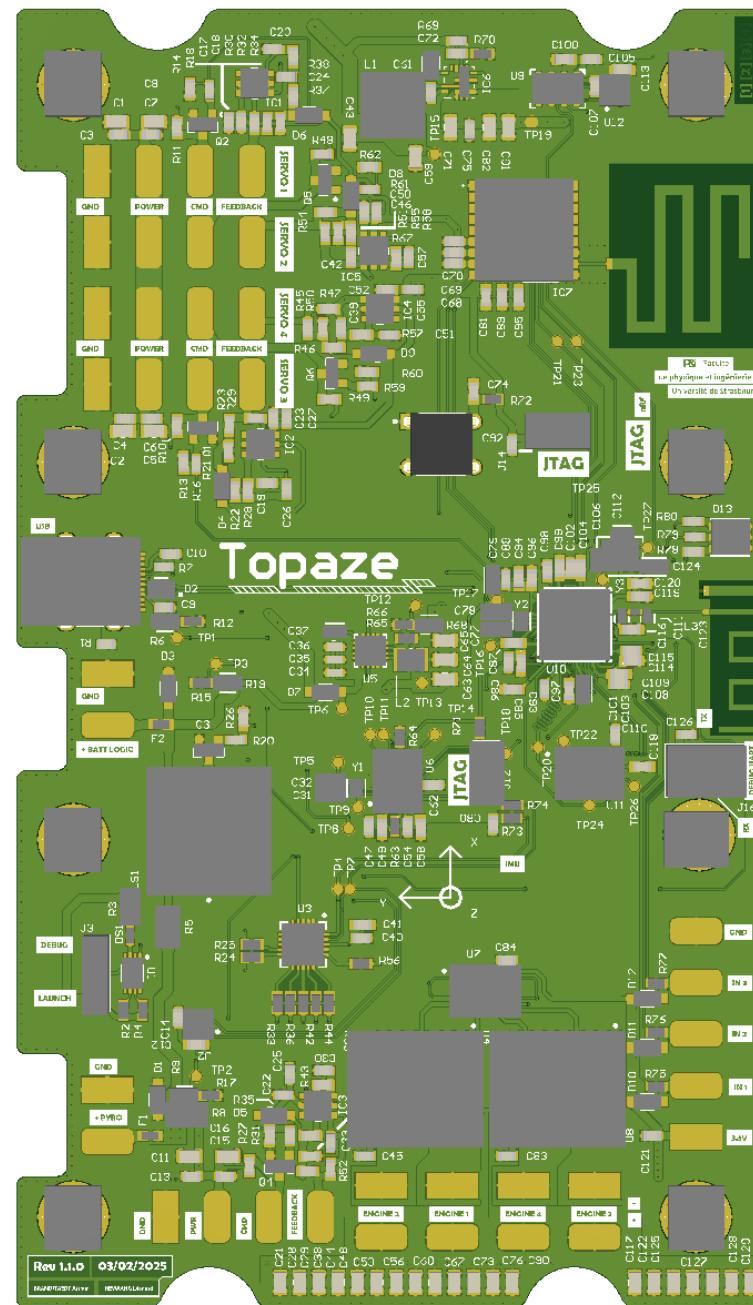
B

C

D

E

### Realistic View



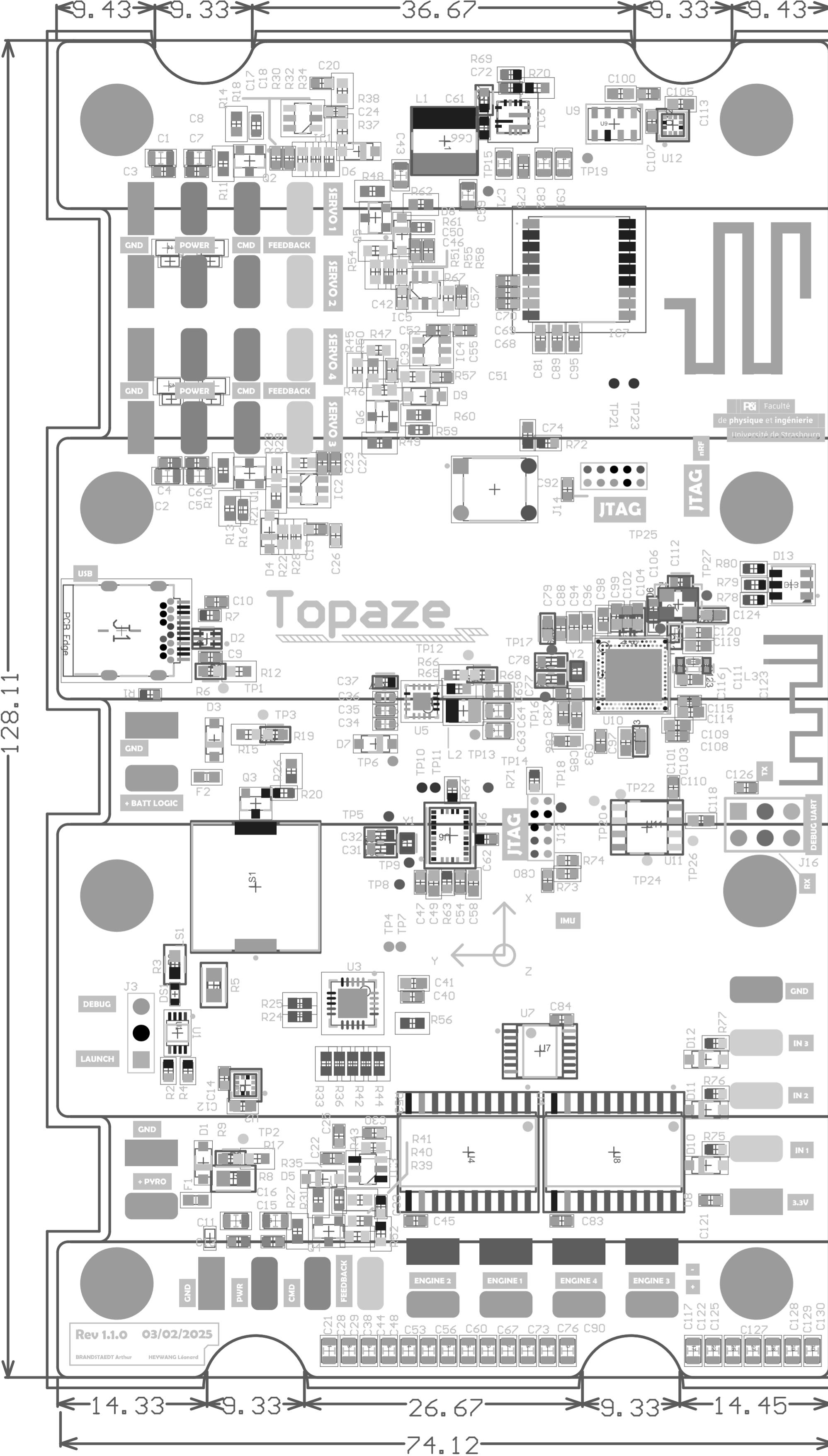
A

B

C

D

E

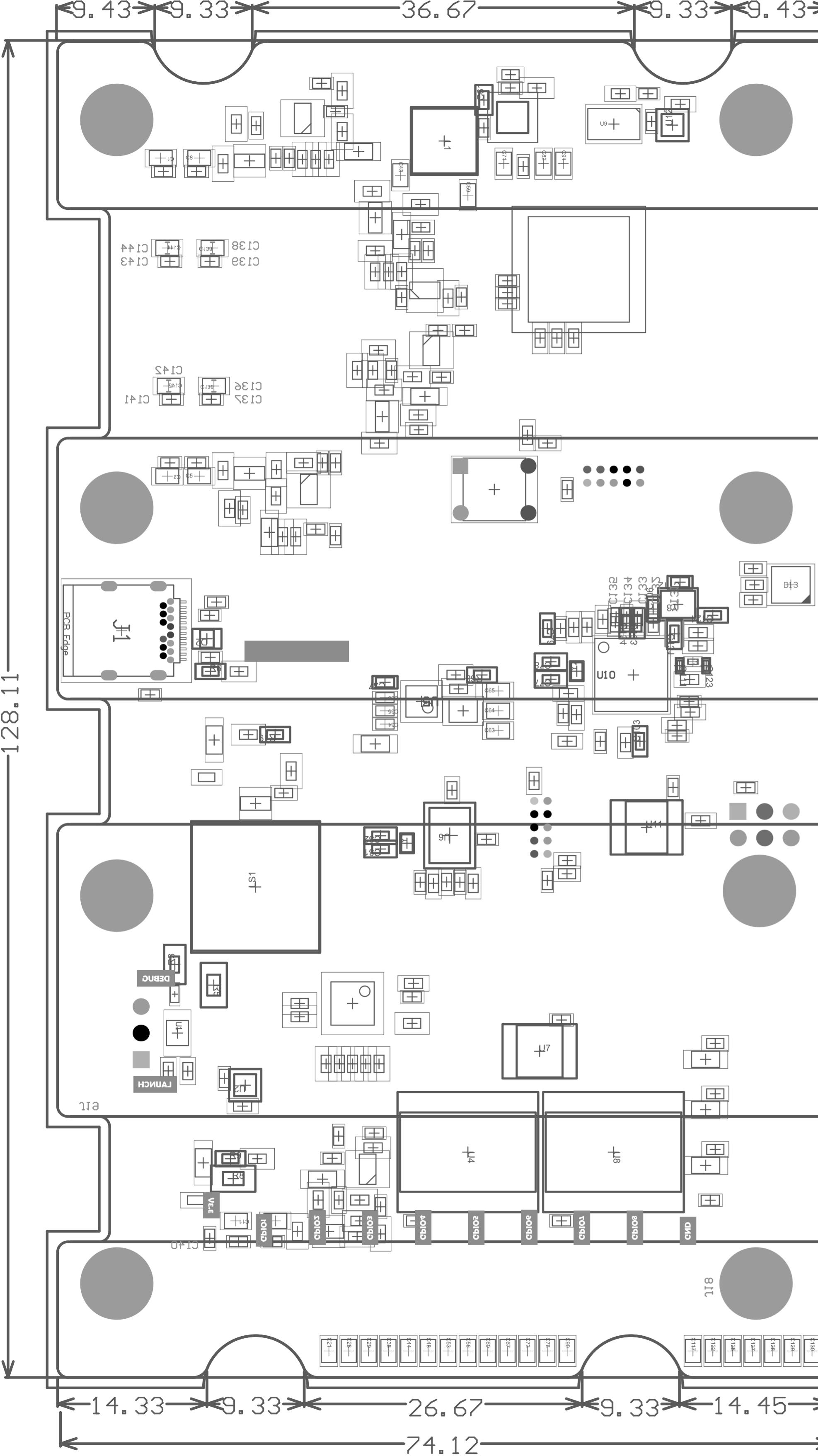


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

This layer configuration correspond to the JLCo4101H-3313 stackup with a 1mm thick PCB

Make sure when ordering to check this stackup !!!

To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the pads.

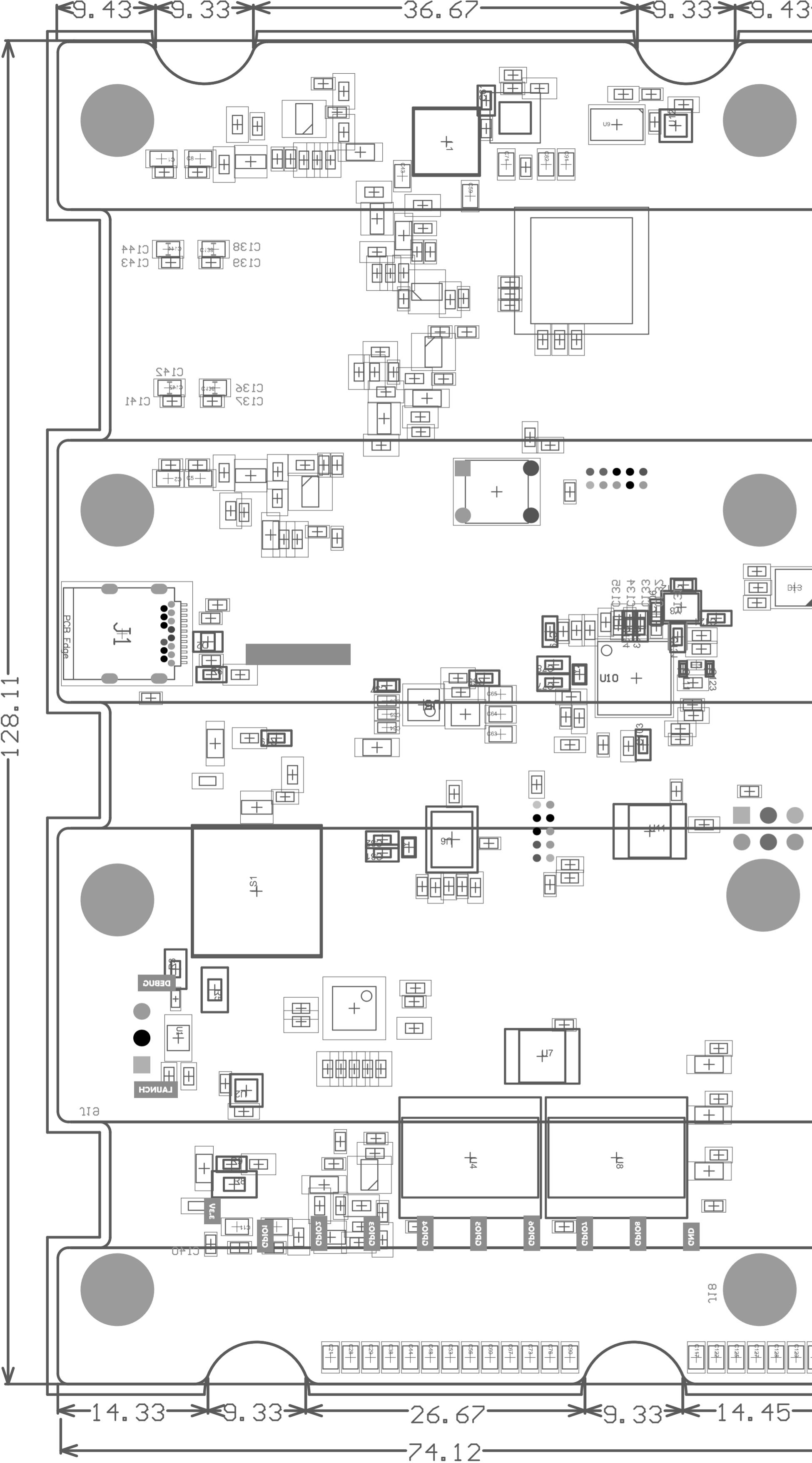


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

**This layer configuration correspond to the JLC04101H-3313 stackup with a 1mm thick**

**Make sure when ordering to check this box**

**To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the the pads.**

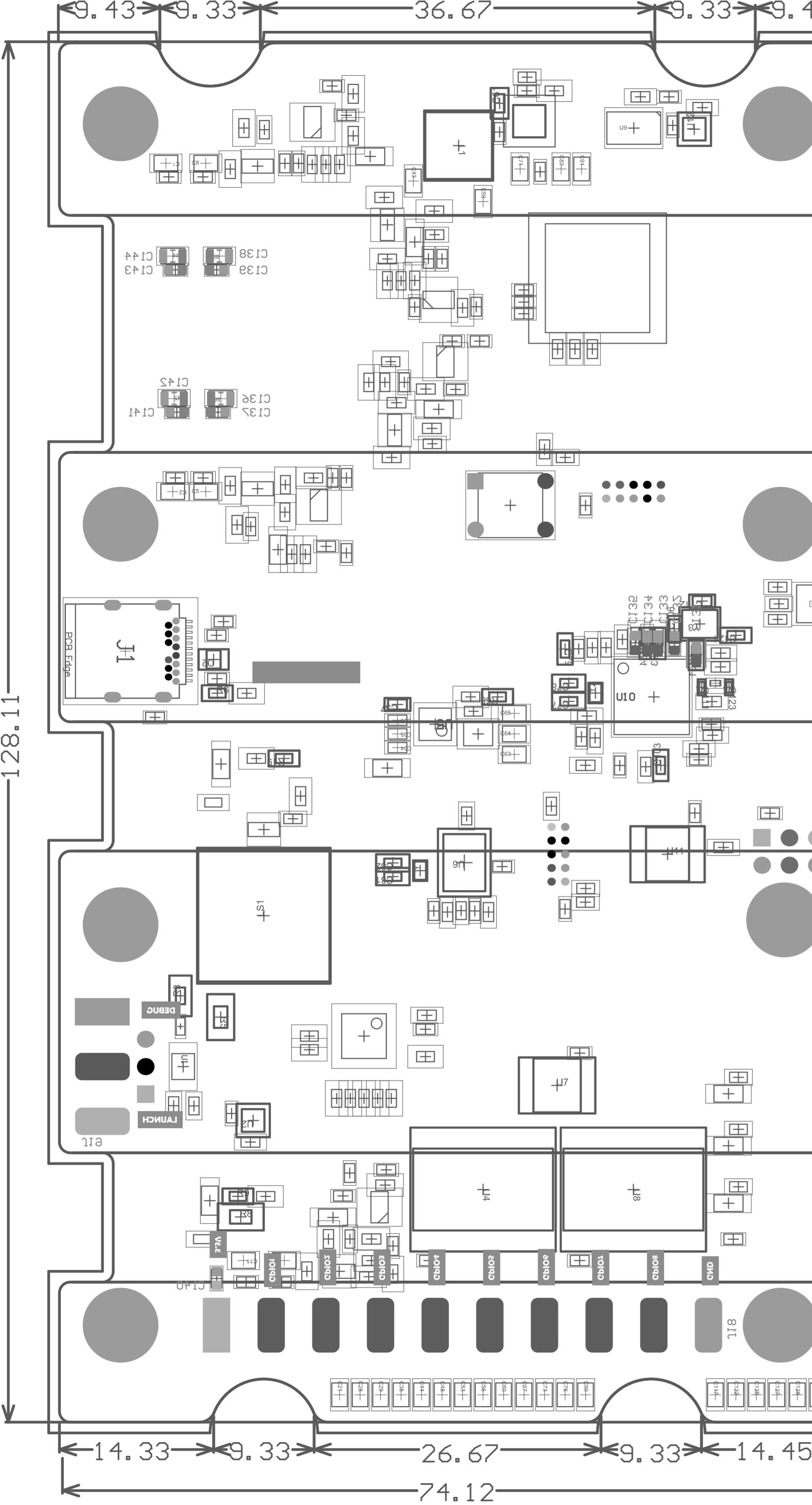


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

This layer configuration correspond to the JLCo4101H-3313 stackup with a 1mm thick PCB

Make sure when ordering to check this stackup !!!

To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the the pads.

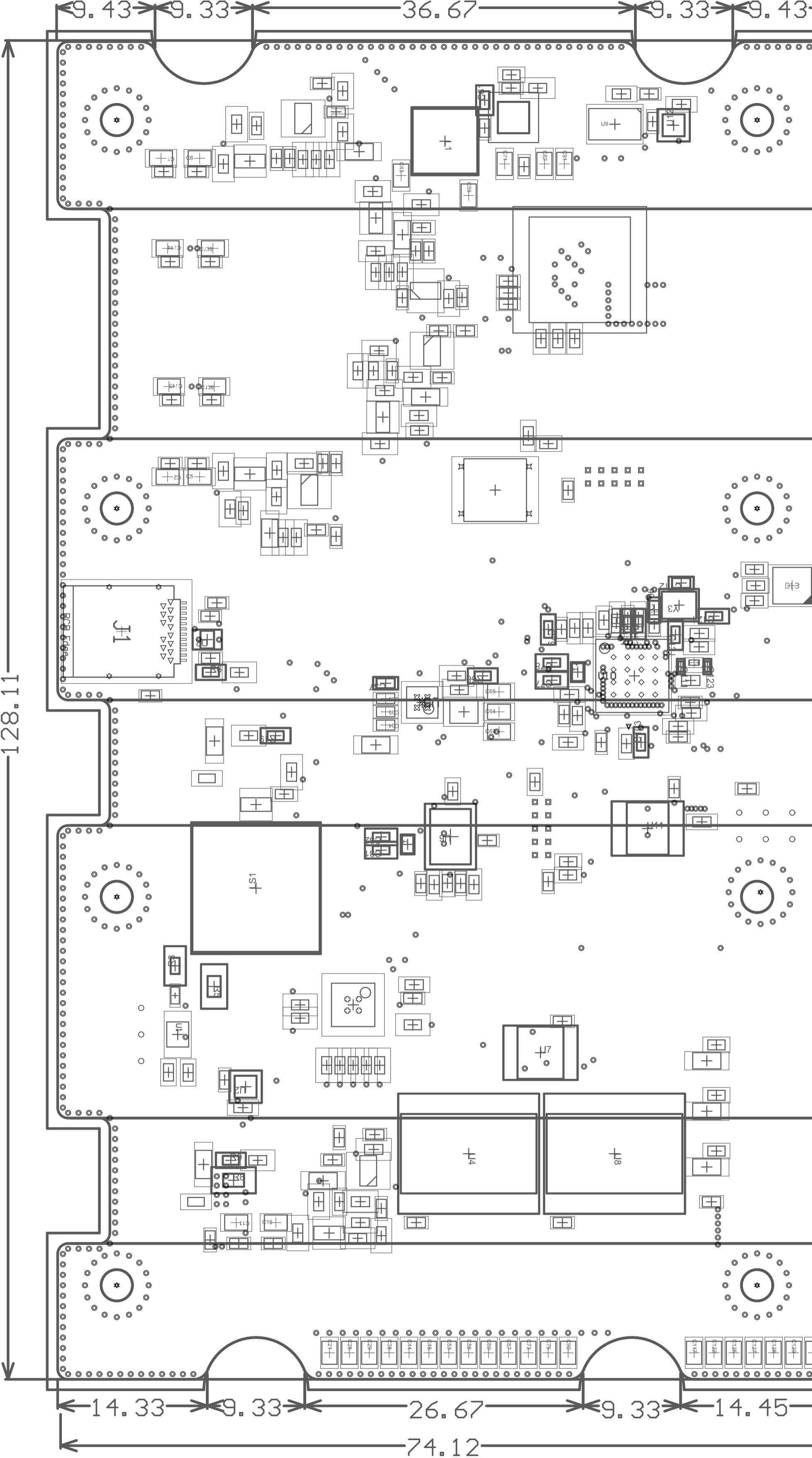


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

**This layer configuration correspond to the JLCo4101H-3313 stackup with a 1mm thick**

**Make sure when ordering to check this**

**To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the the pads.**



Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

This layer configuration correspond to the JLCo4101H-3313 stackup with a 1mm thick PCB

Make sure when ordering to check this stackup !!!

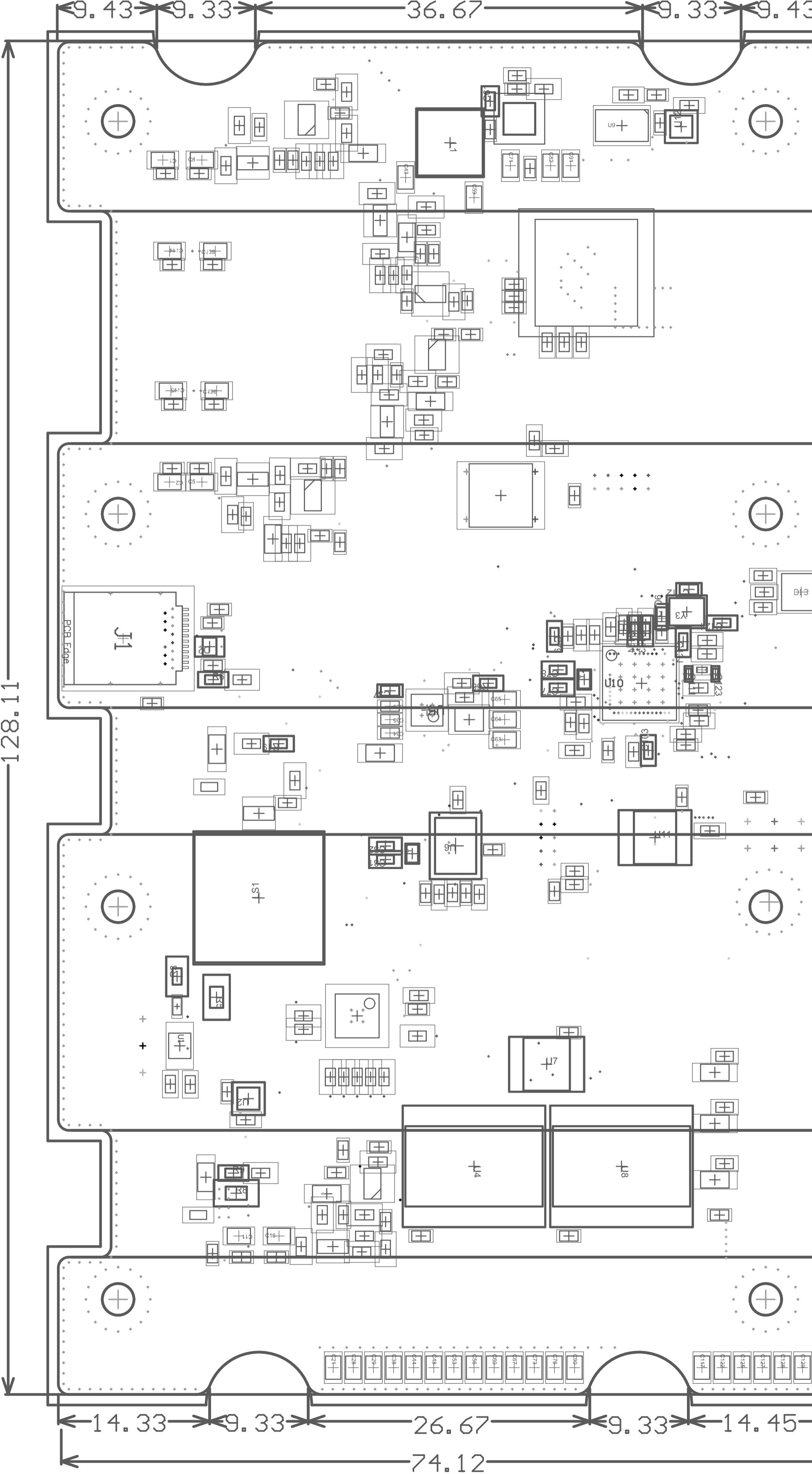
To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the pads.

Symbol	Via/Pad	Count	Hole Size	Plated	Hole Type	Pad Shape	Template	Drill Layer Pair
○	Via	745	0.150mm (5.91mil)	PTH	Round	Rounded	<Mixed>	Top - Bot
+	Via	4	0.200mm (7.87mil)	PTH	Round	Rounded	v50h20	Top - Bot
▼	Via	1	0.250mm (9.84mil)	PTH	Round	Rounded	v45h25m0mx0	Top - Bot
☒	Via	5	0.300mm (11.81mil)	PTH	Round	Rounded	v50h30	Top - Bot
◊	Via	16	0.305mm (12.00mil)	PTH	Round	Rounded	v61h30	Top - Bot
▽	Pad	14	0.400mm (15.75mil)	PTH	Round	Rounded	<Mixed>	Top - Bot
□	Pad	20	0.450mm (17.72mil)	PTH	Round	Rounded	c75h45	Top - Bot
❖	Pad	4	0.600mm (23.62mil)	PTH	Slot	Rounded	r100_160h60_120r50	Top - Bot
☒	Pad	4	1.000mm (39.37mil)	PTH	Round	(Mixed)	<Mixed>	Top - Bot
○	Pad	9	1.100mm (43.31mil)	PTH	Round	(Mixed)	<Mixed>	Top - Bot
❖	Pad	8	3.500mm (137.79mil)	NPTH	Round	Rounded	<Mixed>	Top - Bot
830 Total								

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.

Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Epoxy filled vias



Layer	Name	Material	Thickness	Constant	Board Layer S
	Top Overlay				
	Top Solder	Solder Resist	1.20mil	3.8	
1	Top	CF-004	1.38mil		
	Dielectric 2	PP-015	3.91mil	4.1	
2	GND1	CF-003	0.60mil		
	Dielectric 1	FR-4	27.56mil	4.6	
3	Int	CF-003	0.60mil		
	Dielectric 3	PP-015	3.91mil	4.1	
4	Bot	CF-004	1.38mil		
	Bottom Solder	Solder Resist	1.20mil	3.8	
	Bottom Overlay				

**This layer configuration correspond to the JLCo4101H-3313 standard**

**Make sure when a**

**To ensure a correct fabrication, make sure to order with via filled with copper paste, for the nRF pads where VIAS are placed directly under the pads.**



# Design Rules Verification Report

Filename : C:\Users\Public\Documents\Altium\Opale\PCB1\_2.PcbDoc

Warnings 8  
Rule Violations 119

Warnings	
Unplated multi-layer pad(s) detected	8
Total	8

Rule Violations	
Clearance Constraint (Gap=0.09mm) (All), (All)	2
Clearance Constraint (Gap=0.46mm) (InNetClass('RF') AND (OnOutside OR OnMultiLayer)), ((OnOutside OF	0
Clearance Constraint (Gap=0.55mm) (InNetClass('USB') AND (OnOutside OR OnMultiLayer)), ((OnOutside OF	0
Short-Circuit Constraint (Allowed=No) (All), (All)	2
Un-Routed Net Constraint ( (All) )	3
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.09mm) (Max=10mm) (Preferred=0.12mm) (All)	0
Routing Topology Rule(Topology=Shortest) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm	0
Minimum Annular Ring (Minimum=0.05mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=20mm) (All)	0
Hole To Hole Clearance (Gap=0.198mm) (All), (All)	0
Minimum Solder Mask Sliver (Gap=0.09mm) (All), (All)	0
Silk To Solder Mask (Clearance=0.1mm) (IsPad), (All)	8
Silk to Silk (Clearance=0.15mm) (All), (All)	6
Net Antennae (Tolerance=0mm) (All)	96
Board Clearance Constraint (Gap=0mm) (All)	0
Matched Lengths(Tolerance=0.254mm) (InNetClass('USB'))	0
Maximum Via Count Constraint (Limit=0) (InNetClass('USB'))	2
Maximum Via Count Constraint (Limit=0) (InNetClass('RF'))	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	119

Unplated multi-layer pad(s) detected
Pad MH7-1(67mm,83.333mm) on Multi-Layer on Net GND
Pad MH8-1(67mm,120.5mm) on Multi-Layer on Net GND
Pad MH1-1(5.664mm,9mm) on Multi-Layer on Net GND
Pad MH6-1(67.325mm,46.614mm) on Multi-Layer on Net GND
Pad MH5-1(67mm,9mm) on Multi-Layer on Net GND
Pad MH3-1(5.664mm,83.333mm) on Multi-Layer on Net GND
Pad MH4-1(5.664mm,120.5mm) on Multi-Layer on Net GND
Pad MH2-1(5.664mm,46.167mm) on Multi-Layer on Net GND

Clearance Constraint (Gap=0.09mm) (All),(All)
Clearance Constraint: (Collision < 0.09mm) Between Area Fill (70.396mm,65.88mm) (70.896mm,71.28mm) on Top And Pad ANT2-1(67.945mm,68.58mm) on Top
Clearance Constraint: (Collision < 0.09mm) Between Area Fill (70.396mm,65.88mm) (70.896mm,71.28mm) on Top And Track (62.23mm,68.58mm)(67.945mm,68.58mm) on Top

Short-Circuit Constraint (Allowed=No) (All),(All)
Short-Circuit Constraint: Between Area Fill (70.396mm,65.88mm) (70.896mm,71.28mm) on Top And Pad ANT2-1(67.945mm,68.58mm) on Top Location : [X = 99.071mm][Y = 99.58mm]
Short-Circuit Constraint: Between Area Fill (70.396mm,65.88mm) (70.896mm,71.28mm) on Top And Track (62.23mm,68.58mm)(67.945mm,68.58mm) on Top Location : [X = 98.986mm][Y = 99.58mm]

Un-Routed Net Constraint ( All )
Un-Routed Net Constraint: Arc (62mm,30mm) on Bot Dead Copper - Net Not Assigned
Un-Routed Net Constraint: Track (60mm,28mm)(60mm,33.715mm) on Bot Dead Copper - Net Not Assigned
Un-Routed Net Constraint: Track (64mm,28mm)(64mm,33.715mm) on Bot Dead Copper - Net Not Assigned

Silk To Solder Mask (Clearance=0.1mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.1mm) Between Area Fill (20.034mm,79.8mm) (20.542mm,81.387mm) on Top Overlay And Pad R22-1(21.59mm,81.28mm) on Top [Top Overlay] to [Top Solder]
Silk To Solder Mask Clearance Constraint: (0.06mm < 0.1mm) Between Area Fill (20.034mm,79.8mm) (20.542mm,81.387mm) on Top Overlay And Pad R22-2(21.59mm,79.83mm) on Top [Top Overlay] to [Top Solder]
Silk To Solder Mask Clearance Constraint: (0.063mm < 0.1mm) Between Area Fill (24.79mm,18.238mm) (25.298mm,19.825mm) on Top Overlay And Pad R31-2(25mm,17.725mm) on Top [Top Overlay] to [Top Solder]
Silk To Solder Mask Clearance Constraint: (Collision < 0.1mm) Between Area Fill (32.798mm,109.113mm) (33.306mm,110.7mm) on Top Overlay And Pad R61-2(34.169mm,110.207mm) on Top [Top Overlay] to [Top Solder]
Silk To Solder Mask Clearance Constraint: (Collision < 0.1mm) Between Pad C23-2(25.4mm,88.28mm) on Top And Text "SERVO 3" (26.029mm,93.5mm) on Top Overlay [Top Overlay] to [Top Solder] clearance [0mm]
Silk To Solder Mask Clearance Constraint: (Collision < 0.1mm) Between Pad C27-2(26.67mm,88.28mm) on Top And Text "SERVO 3" (26.029mm,93.5mm) on Top Overlay [Top Overlay] to [Top Solder] clearance [0mm]
Silk To Solder Mask Clearance Constraint: (0.017mm < 0.1mm) Between Pad C85-1(48.145mm,60.96mm) on Top And Text "C86" (46.853mm,61.771mm) on Top Overlay [Top Overlay] to [Top Solder] clearance [0.017mm]
Silk To Solder Mask Clearance Constraint: (Collision < 0.1mm) Between Pad U10-A1(51.845mm,70.51mm) on Top And Region (0 hole(s)) Top Overlay [Top Overlay] to [Top Solder] clearance [0mm]

Silk to Silk (Clearance=0.15mm) (All),(All)
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "DEBUG UART" (72.782mm,50.551mm) on Top Overlay And Track (64.018mm,50.42mm)(71.638mm,50.42mm) on Top Overlay Silk Text to Silk Clearance [0mm]
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "DEBUG UART" (72.782mm,50.551mm) on Top Overlay And Track (64.018mm,55.5mm)(71.638mm,55.5mm) on Top Overlay Silk Text to Silk Clearance [0mm]
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "DEBUG UART" (72.782mm,50.551mm) on Top Overlay And Track (71.638mm,50.42mm)(71.638mm,55.5mm) on Top Overlay Silk Text to Silk Clearance [0mm]
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "JTAG" (44.374mm,50.03mm) on Top Overlay And Track (45.085mm,49.53mm)(45.085mm,55.88mm) on Top Overlay Silk Text to Silk Clearance [0mm]
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "JTAG" (44.374mm,50.03mm) on Top Overlay And Track (45.085mm,49.53mm)(47.625mm,49.53mm) on Top Overlay Silk Text to Silk Clearance [0mm]
Silk To Silk Clearance Constraint: (Collision < 0.15mm) Between Text "RX" (72.162mm,46.838mm) on Top Overlay And Track (69.215mm,50.165mm)(71.12mm,48.26mm) on Top Overlay Silk Text to Silk Clearance [0mm]

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (2.848mm,10.166mm) from Top to Bot
Net Antennae: Via (2.848mm,119.334mm) from Top to Bot
Net Antennae: Via (2.848mm,121.666mm) from Top to Bot
Net Antennae: Via (2.848mm,45.001mm) from Top to Bot
Net Antennae: Via (2.848mm,47.333mm) from Top to Bot
Net Antennae: Via (2.848mm,7.834mm) from Top to Bot
Net Antennae: Via (2.848mm,82.167mm) from Top to Bot
Net Antennae: Via (2.848mm,84.499mm) from Top to Bot
Net Antennae: Via (3.509mm,11.155mm) from Top to Bot
Net Antennae: Via (3.509mm,118.345mm) from Top to Bot
Net Antennae: Via (3.509mm,122.655mm) from Top to Bot
Net Antennae: Via (3.509mm,44.012mm) from Top to Bot
Net Antennae: Via (3.509mm,48.322mm) from Top to Bot
Net Antennae: Via (3.509mm,6.845mm) from Top to Bot
Net Antennae: Via (3.509mm,81.178mm) from Top to Bot
Net Antennae: Via (3.509mm,85.488mm) from Top to Bot
Net Antennae: Via (4.498mm,11.816mm) from Top to Bot
Net Antennae: Via (4.498mm,117.684mm) from Top to Bot
Net Antennae: Via (4.498mm,123.316mm) from Top to Bot
Net Antennae: Via (4.498mm,43.351mm) from Top to Bot
Net Antennae: Via (4.498mm,48.983mm) from Top to Bot
Net Antennae: Via (4.498mm,6.184mm) from Top to Bot
Net Antennae: Via (4.498mm,80.517mm) from Top to Bot
Net Antennae: Via (4.498mm,86.149mm) from Top to Bot
Net Antennae: Via (6.83mm,11.816mm) from Top to Bot
Net Antennae: Via (6.83mm,117.684mm) from Top to Bot
Net Antennae: Via (6.83mm,123.316mm) from Top to Bot
Net Antennae: Via (6.83mm,43.351mm) from Top to Bot
Net Antennae: Via (6.83mm,48.983mm) from Top to Bot
Net Antennae: Via (6.83mm,6.184mm) from Top to Bot
Net Antennae: Via (6.83mm,80.517mm) from Top to Bot
Net Antennae: Via (6.83mm,86.149mm) from Top to Bot
Net Antennae: Via (64.184mm,10.166mm) from Top to Bot
Net Antennae: Via (64.184mm,119.334mm) from Top to Bot
Net Antennae: Via (64.184mm,121.666mm) from Top to Bot
Net Antennae: Via (64.184mm,7.834mm) from Top to Bot

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (64.184mm,82.167mm) from Top to Bot
Net Antennae: Via (64.184mm,84.499mm) from Top to Bot
Net Antennae: Via (64.509mm,45.448mm) from Top to Bot
Net Antennae: Via (64.509mm,47.78mm) from Top to Bot
Net Antennae: Via (64.845mm,11.155mm) from Top to Bot
Net Antennae: Via (64.845mm,118.345mm) from Top to Bot
Net Antennae: Via (64.845mm,122.655mm) from Top to Bot
Net Antennae: Via (64.845mm,6.845mm) from Top to Bot
Net Antennae: Via (64.845mm,81.178mm) from Top to Bot
Net Antennae: Via (64.845mm,85.488mm) from Top to Bot
Net Antennae: Via (65.17mm,44.459mm) from Top to Bot
Net Antennae: Via (65.17mm,48.769mm) from Top to Bot
Net Antennae: Via (65.834mm,11.816mm) from Top to Bot
Net Antennae: Via (65.834mm,117.684mm) from Top to Bot
Net Antennae: Via (65.834mm,123.316mm) from Top to Bot
Net Antennae: Via (65.834mm,6.184mm) from Top to Bot
Net Antennae: Via (65.834mm,80.517mm) from Top to Bot
Net Antennae: Via (65.834mm,86.149mm) from Top to Bot
Net Antennae: Via (66.159mm,43.798mm) from Top to Bot
Net Antennae: Via (66.159mm,49.43mm) from Top to Bot
Net Antennae: Via (68.166mm,11.816mm) from Top to Bot
Net Antennae: Via (68.166mm,117.684mm) from Top to Bot
Net Antennae: Via (68.166mm,123.316mm) from Top to Bot
Net Antennae: Via (68.166mm,6.184mm) from Top to Bot
Net Antennae: Via (68.166mm,80.517mm) from Top to Bot
Net Antennae: Via (68.166mm,86.149mm) from Top to Bot
Net Antennae: Via (68.491mm,43.798mm) from Top to Bot
Net Antennae: Via (68.491mm,49.43mm) from Top to Bot
Net Antennae: Via (69.155mm,11.155mm) from Top to Bot
Net Antennae: Via (69.155mm,118.345mm) from Top to Bot
Net Antennae: Via (69.155mm,122.655mm) from Top to Bot
Net Antennae: Via (69.155mm,6.845mm) from Top to Bot
Net Antennae: Via (69.155mm,81.178mm) from Top to Bot
Net Antennae: Via (69.155mm,85.488mm) from Top to Bot
Net Antennae: Via (69.48mm,44.459mm) from Top to Bot
Net Antennae: Via (69.48mm,48.769mm) from Top to Bot

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (69.816mm,10.166mm) from Top to Bot
Net Antennae: Via (69.816mm,119.334mm) from Top to Bot
Net Antennae: Via (69.816mm,121.666mm) from Top to Bot
Net Antennae: Via (69.816mm,7.834mm) from Top to Bot
Net Antennae: Via (69.816mm,82.167mm) from Top to Bot
Net Antennae: Via (69.816mm,84.499mm) from Top to Bot
Net Antennae: Via (7.819mm,11.155mm) from Top to Bot
Net Antennae: Via (7.819mm,118.345mm) from Top to Bot
Net Antennae: Via (7.819mm,122.655mm) from Top to Bot
Net Antennae: Via (7.819mm,44.012mm) from Top to Bot
Net Antennae: Via (7.819mm,48.322mm) from Top to Bot
Net Antennae: Via (7.819mm,6.845mm) from Top to Bot
Net Antennae: Via (7.819mm,81.178mm) from Top to Bot
Net Antennae: Via (7.819mm,85.488mm) from Top to Bot
Net Antennae: Via (70.141mm,45.448mm) from Top to Bot
Net Antennae: Via (70.141mm,47.78mm) from Top to Bot
Net Antennae: Via (8.48mm,10.166mm) from Top to Bot
Net Antennae: Via (8.48mm,119.334mm) from Top to Bot
Net Antennae: Via (8.48mm,121.666mm) from Top to Bot
Net Antennae: Via (8.48mm,45.001mm) from Top to Bot
Net Antennae: Via (8.48mm,47.333mm) from Top to Bot
Net Antennae: Via (8.48mm,7.834mm) from Top to Bot
Net Antennae: Via (8.48mm,82.167mm) from Top to Bot
Net Antennae: Via (8.48mm,84.499mm) from Top to Bot

Maximum Via Count Constraint (Limit=0) (InNetClass('USB'))
Maximum Via Count Constraint: Net USB_N (Via Count=2)
Maximum Via Count Constraint: Net USB_P (Via Count=2)