Adress	Length (bytes)	Final address	Page	Values
0x0	1	0x1	0-0	**UNUSED**
0x1	3	0x4	0-0	**EEPROM DATA VERSION** : Major.Median.Minor - Major : 1.x.x : 8b (int) - Median : x.0.x : 8b (int) - Minor : x.x.0 : 8b (int)
0x4	7	0xB	0-0	**EEPROM WRITE DATE** : Y-M-D-H-Min-S - Y : Year : 16b (int) - M : Month : 8b (int) - D : Day : 8b (int) - H : Hour : 8b (int) - Min : Minutes : 8b (int) - S : Seconds : 8b (int)
0xB	2	0xD	0-0	**HARDWARE REVISION :** Major.Minor - Major : 1.x : 8b (int) - Minor : x.0 : 8b (int)
0xD	2	0xF	0-0	**BOM REVISION** : Major.Minor - Major : 1.x : 8b (char) - Minor : x.0 : 8b (int)
0xF	8	0x17	0-0	**SERIAL NB** : xx.yyyyyy - XX : Two letters - YYYYYY : 6 decimal numbers.
0x17	8	0x1F	0-0	**UNUSED**
0x1F	7	0x26	0-0	**SPEAKER FABRICATION DATE** : Y-M-D-H-Min-S - Y : Year : 16b (int) - M : Month : 8b (int) - D : Day : 8b (int) - H : Hour : 8b (int) - Min : Minutes : 8b (int) - S : Seconds : 8b (int)
0x26	2	0x28	0-0	**HEADER CRC16**
0x28	2	0x2A	0-0	**CONFIG CRC16**
0x2A	8	0x32	0-0	**UNUSED**
0x32	1	0x33	0-0	**Number of DSP Settings stored. (0 -15)**
0x33	13	0x40	0-1	**UNUSED**

Adress	Length (bytes)	Final address	Page	Values
0x40	8	0x48	1-1	**DSP Profile Info 1** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x48	8	0x50	1-1	**DSP Profile Info 2** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x50	8	0x58	1-1	**DSP Profile Info 3** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x58	8	0x60	1-1	**DSP Profile Info 4** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x60	8	0x68	1-1	**DSP Profile Info 5** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x68	8	0x70	1-1	**DSP Profile Info 6** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x70	8	0x78	1-1	**DSP Profile Info 7** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)
0x78	8	0x80	1-2	**DSP Profile Info 8** - Address (16 bits) - Length (16 bits) - CRC32 (32 bits)

0x8A	9	0x93	2-2	**DAC Config** - Automute delay - Global Volume - Left Volume - Right Volume - Left Data Source - Right Data Source - Left Analog Attenuation - Right Analog Attenuation - Number of DSP Profiles
0x93	3	0x96	2-2	**AMP Struct(s)** - Bass power limit - Left Power limit - Right Power limit
0x96	8	0x9E	2-2	**LEDS Struct(s)** - Brightness - MaxBrightness
0x9E	2	0xA0	2-2	**Capacitive Struct** - Threshold value - Sensivity
0xA0	5	0xA5	2-2	**Power delivery 1** - Enable PPS - Voltage : INT.FLOAT part, with float a count of 20 mV steps. - Current : INT.FLOAT part, with float a count of 50 mA steps.
0xA5	5	0xAA	2-2	**Power delivery 1** - Enable PPS - Voltage : INT.FLOAT part, with float a count of 20 mV steps. - Current : INT.FLOAT part, with float a count of 50 mA steps.
0xAA	22	0xC0	2-3	**UNUSED**
0xC0	64	0x100	3-4	**Speaker Friendly Name**
0x100	128	0x180	4-6	**UNUSED**

Adress	Length (bytes)	Final address	Page	Values
0x1C0	5662	0x17DE	7-95	**DSP Profile 1 (1024 instructions)***
0x17DE	2846	0x22FC	95-139	**DSP Profile 2 (512 instructions)***
0x22FC	1438	0x289A	139-162	**DSP Profile 3 (256 instructions)***
0x289A	20935	0x7A61	162-489	...
0x7A61	1438	0x7FFF	489-511	**DSP Profile 16***

*There is no order nor size order for the DSP Profiles.

The only limitation is the EEPROM Size, which is limited and therefore cannot be exceeded.

The write procedures will ensure the new DSP Profile won't exceed any limitations.